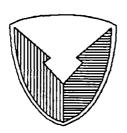
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CIN: C02 092LA 0013 00

23 July 1990



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Distributed Issues for	Ada Real-Time Systems		MDA 903-87- C- 0056
6. AUTHOR(S)			
Thomas E. Griest			
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES)		8. PERFORMING ORGANIZATION REPORT NUMBER
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11. SUPPLEMENTARY NOTES		,	<u> </u>
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STATEMENT A UNLIMITED			
13. ABSTRACT (Maximum 200 words)			1
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DISTRIBUTED ISSUES PROJECT FINAL REPORT

PREPARED FOR:

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DATE:

13 July1990

The views, opinions, and/or findings contained in this report are those of the author and should not be construed as an official Department of the Army position, policy, or decision, unless so designated by other documentation.

EXECUTIVE SUMMARY

The use of distributed systems to obtain gains in system throughput and reliability is expected to continue for the foreseeable future. These systems provide substantial benefits in performance and fault tolerance at the expense of significantly increased complexity. This work addresses an approach to reduce the complexity of these systems by extending the standard Ada tasking model to handle the distributed processor and reliability issues. It uses software developed in a previous CECOM research project that addressed the performance benefits that could be gained by the distribution of real-time Ada systems.

The Ada tasking model is already reasonably well understood from a uniprocessor point of view but it is currently not defined to support characteristics of distributed systems. However, by taking advantage of the same model for distributed systems, fewer new concepts need to be introduced. This reduction in concepts results in a less complex and more flexible system. To achieve increased performance, a new approach to improving parallel execution was studied. The approach was to create a data structure consisting of an array of tasks and distribute the elements of the array across a set of processors. This task array provides the ability to achieve performance benefits as a function of the available processors.

The Ada tasking model is currently silent on failure semantics. This study introduces a simple failure recovery mechanism appropriate for a class of applications which can tolerate interruptions in service for up to one second. Enhanced fault detection and recovery logic have been added to demonstrate the ability to continue operation in the presence of some hardware failures. To improve the flexibility of the system configuration, a new runtime interface has been established to allow dynamic reconfiguration at runtime.

Table of Contents

1.	Introduction	1
2.	Complexity of Distributed Systems	2
3.	Use of the Ada Tasking Model for Distributed Systems	4
4.	Responding to Failures in a Distributed System	6
5.	Demonstration Application	8
	5.1 Enhancements to the Demonstration Application Software	9
	5.2 Improvements to the Distributed Runtime	10
	5.2.1 Distribution Control Details	12
6.	Performance Characteristics	15
	6.1 Benchmark Results	17
	6.2 Performance Gains	22
7.	Design of Systems Using Distributed Ada	25
	7.1 Hardware Considerations	25
	7.2 Software Considerations	26
	7.3 Differences Between Distributed and Uniprocessor Implementations	28
8.	Limitations of the Distribution Support	31
	8.1 Task Identification	31
	8.2 Code Replication	32
	8.3 Network Error Recovery	32
9.	Compiler and Runtime Problems	34
10	. Summary	36
11	. References	38
12	. Appendix A - Border Defense System Ada Source Code	39
13	Appendix B - Distributed Runtime Source Code	199

Table of Contents

Figure 1.	Software Subsystems		11
Figure 2.	Top Level BDS Desig	n	19

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1. Introduction

This paper describes the results of a project to investigate issues in distributed Ada programs. It is based on an earlier demonstration project done by the U.S. Army, Center for Software Engineering at Fort Monmouth, NJ[1]. It goes beyond that initial work in two important areas: using task arrays to expand parallelism; and improving the benefits of program distribution beyond performance gains by providing support for fault tolerance.

The earlier project had statically allocated tasks to a processor, and each task was unique, based on a uniprocessor design. The new approach of using task arrays supports the capability to spread the execution of independent iterative operations across a distributed system. The distributed runtime was re-implemented to provide much more flexible reconfiguration capabilities. This made it possible to measure performance gains over a wider set of configurations. The increased flexibility also made it possible for the system to reconfigure under fault conditions and continue operation in a degraded mode.

This paper briefly outlines the application and its key performance characteristics used to demonstrate use of the distributed Ada techniques. Improvements to the application are mentioned, as well as enhancements to the distributed runtime system. Performance benchmarks were done to assess the benefits obtained by distribution of a single Ada program over a network of three Intel 80386 processors connected by an Ethernet. The results of the benchmarks were analyzed to explain how the system reacted under various processing loads and system configurations. These findings were further analyzed to identify design considerations which can be used to improve performance for other distributed applications.

2. Complexity of Distributed Systems

Systems which are distributed tend to be much more difficult to develop than a single processor implementation of the same problem. This is due to several contributing factors. True parallelism creates new scheduling problems and new ways in which resource contention can occur within systems. Distribution introduces the significant new requirement of communication. This inter-processor communication is often at a much higher data rate and less predictable than typical communication with external devices. For this reason, buffering and synchronization become critical design considerations.

Adding to the problem of distributed real-time systems is the issue of maintaining a common sense of time among all of the processors. Application software often becomes involved in the process of keeping track of time and adjusting for time differences among processors. This burden generally does not exist in uniprocessor applications.

Finally, distributed systems generally are constructed in an ad-hoc manner, creating a vast number of dissimilar architectures. This effectively eliminates the mass market necessary to encourage tool developers to support distributed systems. The result is that tools for developing distributed systems are usually custom made and frequently lacking in capability. Horror stories of developers using dozens of separate in-circuit emulators, each with their own console to test distributed systems, are common. Thus the nature of distributed systems simply adds to the difficulties of understanding and solving the complex problems which arise during development.

The benefits of distributed systems are found in essentially three fundamental characteristics: performance, physical separation, and fault tolerance. It is nearly always the case that the price/performance ratio of computer hardware favors many lower cost

processors over a single higher performance processor. Furthermore, the ability to handle interrupts by distributed processors reduces the number of context switches (and associated overhead) required. This often results in better response time and overall performance by having several lower cost processors concentrate on a single task rather than having one high cost processor switching among many tasks.

The physical separation of processors provides the ability to have processing resources in close proximity to isolated system hardware. This is often necessary to reduce the wiring and provide a degree of autonomy between subsystems. This can be extremely important in situations which may suffer from localized damage. Similarly, the additional processors make it possible for continued operation in the presence of processor failures. Eliminating all situations where a single component failure will result in system failure is a common axiom of fault tolerant systems.

The growing number of distributed systems in use is evidence to the fact that the benefits of these systems are significant even though there is additional complexity. Techniques that reduce the complexity of these systems would further enhance their attractiveness and are likely to lead to more cost-effective systems and widespread use of fault tolerant computing.

3. Use of the Ada Tasking Model for Distributed Systems

Several approaches are used to support cooperative processing on distributed systems. Most frequently, a message-based mechanism of communication is defined to allow a program on one processor to interface with programs on other processors. These messages are generally developed in an application-specific way with a wide variety of characteristics and requirements. Other techniques include some type of formalized remote procedure call approach with surrogate tasks to execute the procedures that are remotely invoked.

The approach used on this demonstration was to utilize the Ada tasking model of concurrency for all local and remote communication between parallel threads of execution. This approach to concurrency is often referred to as "Distributed Ada" and has a number of advantages to other approaches. Among them are:

- 1) The ability of the compiler to check interfaces between physical processors.
- 2) A consistent approach to parallelism all concurrent activities are expressly stated with a consistent formal mechanism making the system less complex.
- 3) Re-configuration is facilitated, since the interface between communicating tasks on a processor is the same as that among separate processors, thus allowing tasks to be migrated more easily.
- 4) Consistency helps to make distributed testing and debugging more easily supported by compiler implementers. Ad hoc approaches make debugging tools prohibitively expensive and generally not as complete.

By utilizing the Ada tasking model, the underlying details for remote communication and maintaining a consistent sense of time are hidden. Since there is a stable model to support, this also creates the possibility for system vendors to provide hardware that is optimized to support distribution of Ada tasks.

The benefits of providing a well understood, uniform approach to concurrent programming should not be underestimated. The ability for developers to have a clear understanding of how their distributed system interacts is essential in lowering the costs and improving the reliability of these systems. From this point of view, using the Ada tasking model for distributed communication and synchronization provides the best opportunity for consistency when programming in the Ada language.

4. Responding to Failures in a Distributed System

The potential to tolerate processor failures is a side-effect of having a distributed system. Frequently this potential is not realized due to the complexity of supporting the detection, isolation, and recovery mechanisms required for fault-tolerant processing. However, safety or mission-critical applications require fault-tolerance and therefore must accept the additional complexity.

The requirements for fault-tolerance can vary from system to system, and the corresponding implementation to support those requirements is substantially different. One of the critical factors is the time in which operations are allowed to be interrupted. Systems that cannot tolerate any interruption in service must perform calculations redundantly and decide which results to accept. More typically, systems are allowed to fail for a few seconds providing that they can come back in service correctly. For these systems, migration of services from failed processors to operational ones is often sufficient to maintain acceptable performance.

The degree to which information is lost (and/or corrupted) during failures also impacts the architecture of the system. The use of stable storage techniques to prevent loss of data is a common approach to continue in the presence of processor failures. This approach checkpoints data to a stable storage area (usually made from redundant memory modules) which is accessible from other processors. If one of the processors fails, another processor can generally carry on from the last checkpoint made by the failing processor. Obviously the amount of time lost due to a failure has a direct relationship on how frequently checkpoints must be made. On the other hand, preventing corruption of data depends on detecting the faults early and preventing the errors from propagating into other portions of the system. This technique is often referred to as establishing "fire walls".

Most failures can be detected by comparing the results of redundant operations, the use of check codes in data, or by using timers to insure that operations complete in their required times. Depending on the type of fault and its detection scheme, the recovery may be as simple as selecting the most likely value based on a majority vote of redundant computations; or it may be a complex process of retries and judgments made on confidence levels in components associated with the failure. The diversity in fault-tolerance requirements and the associated techniques to support them precludes a standard approach to fault-tolerant applications. Instead, flexibility for designers is necessary to allow the method of support to closely match the requirements. For this reason, the demonstration system includes the ability to have application software interface to the configuration control software. (More information on how faults are detected and handled in the demonstration system is provided at the end of section 5.2.) It is clear that many applications will require the ability to have the logic to support fault-tolerance shared between application-specific software and general fault-tolerance software in the runtime.

5. Demonstration Application

To adequately demonstrate the effective use of distributed processing, a real-time application was required to provide a test case program. A synthetic application titled the "Border Defense System (BDS)" which combines target tracking, rocket guidance, and graphics was developed to provide a suitable real-time test. A simulator was included to provide rocket and target motion.

The main characteristics of the BDS are summarized below:

- Hard Deadline Driven application: failure to meet timing requirements will result in mission failure.
- "Processor in the Loop" flight control with dynamic target tracking.
- Complex problem, with interac. . . among several different functional areas:

Message Reception (from Sensor Interface and Airborne Rockets)

Multiple Target Tracking and Prediction

Multiple Rocket Tracking and Guidance

Real-Time Graphics Updates

Real-Time Operator Interface (peak data rate of 500Hz)

- Using current technology: 32-bit Microprocessors (80386-16MHz)
- Initially a separate program was designated for the simulator, however it was temporarily incorporated into the system as additional tasks and placed on a separate processor using the distribution technique.
- All application concurrency is expressed using the Ada Tasking Model (Rendezvous) exclusively.
- The program consists of approximately 6700 Ada LOC contained within 51 compilation units. A copy of the BDS source code is provided in Appendix A.
- The distributed runtime is implemented with 5242 assembly language statements (for compatibility with the vendor runtime) contained in 10 modules. A copy of the distributed runtime source code is provided in Appendix B.

All calculations for both the rockets and targets are done in three dimensional space, however the target simulator currently maintains a constant altitude (Z=0) for the target motions. Each of the aimpoint calculations are computed every 100ms for all of the rockets in flight. On the 16MHz Intel 386 processor the computations currently require approximately 6ms per rocket.

5.1 Enhancements to the Demonstration Application Software

In addition to the fault tolerance and distributed processing capabilities, the demonstration software was enhanced in two ways. First, the rocket simulation algorithms were made much more realistic (and therefore complex). Second, the flight control system was redesigned to be oriented towards a realistic feedback system, that is, the software adjusts the rocket flight based on the effect of previous flight control commands. Previously, the rocket simulator made instantaneous flight corrections rather than corrections based on normal accelerations. This allowed a guidance routine that simply aimed the rocket at the target. A side effect of the feedback approach is an increased sensitivity to (ie. lack of tolerance for) incorrect tracking of the rocket motion. Errors can occur during overload situations where rocket reports can be lost. When this does happen, the rockets become unstable and their flight paths become very erratic.

Accuracy was improved by utilizing 32-bit fixed point types throughout most of the trajectory calculations rather than 16-bit fixed point. Custom fixed point routines were developed that provided substantially better performance than those in the native runtime system which were designed for a 16-bit machine. The rocket guidance equations now utilize 3rd-order processing, which is required to provide the desired accuracy. To provide some insight into what processing is done for rocket guidance, the following computations are performed for each rocket update:

- 1) The relative (closing) velocities and accelerations of the rocket/target pairings are computed and an estimated impact point is predicted.
- 2) The rocket's desired velocity vector is then computed and compared to the current velocity vector.
- 3) Based on the velocity differentials, the desired acceleration is compared to the current rocket acceleration. This provides a desired change in acceleration which is then used to determine the adjustment required in the rocket's attitude.
- 4) The flight profile is smoothed by integrating the attitude adjustment over a period, which is computed as a function of estimated "time-to-impact". This reduces "overshoot" of the glide slope and had a major beneficial effect on the rocket accuracy.

5.2 Improvements to the Distributed Runtime

Several major changes were made to the underlying distributed runtime. In general, the changes can be classified as enhancements to the configuration flexibility of the system. In particular, the system now is capable of dynamically altering the configuration during system execution. Figure 1 (on the following page) shows the subsystems that make up the entire program which is replicated on each processor. It is shown as being layered from the top application code down through successive levels of abstraction. Conversely, control is passed up through the levels towards the application. Upon initialization, control is given to the System Configuration Setup & Control module which passes control to the distributed runtime. It in-turn transfers control to the vendor runtime which elaborates and activates the application software.

Software Subsystems

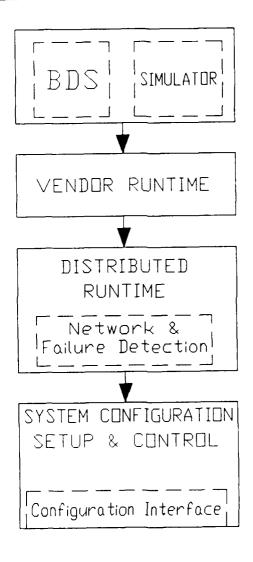


Figure 1. Software Subsystems

Application subprograms request service from the vendor runtime which passes the service request to the distributed runtime. Using a task directory built by the System Configuration Setup & Control module, the distributed runtime determines if the service involves distributed resources. If not, control is returned to the vendor runtime. If the service does involve distributed resources, the distributed runtime carries out the service using its own tasking primitives and the Network Services module.

The Network & Failure Detection module is capable of detecting communication errors or the apparent loss of a processor by using timers with acknowledgement messages and a "heartbeat" mechanism. This mechanism monitors activity from each of the processors. If no activity occurs within a specified period or an acknowledgement is not returned in time, a failure condition exists and failure recovery is initiated. Failure recovery essentially stops application processing and returns control to the System Configuration Setup & Control module.

5.2.1 Distribution Control Details

Each processor initializes the underlying hardware based on what is available in the machine. The Ethernet hardware contains a unique network station address (in Read-Only Memory) which is then used in a table look-up to determine the logical processor ID. The processor ID is then used to determine which processor is the Master (controlling) CPU and which are the Slaves. The Master is distinguished from the Slaves in that it is responsible for the distributed system Configuration Interface and the system-wide synchronization during start up.

The Master processor prompts the user through a menu system which allows configuration of system parameters. These parameters include:

the maximum number of rockets;
the maximum number targets;
which (of four possible) configurations to use; and
enabling automatic reconfiguration.

If automatic reconfiguration is enabled, a delay may be selected to allow a user to see the error condition for five seconds.

Most of this information is made available to the application via a distributed runtime interface package. For example, it is possible for an application routine to determine if it is running on the Master CPU by testing a boolean variable in the interface. This information can be used by the main program which is activated on all processors to perform machine specific operations. In the case of the BDS it is used to control initialization of the operator interface which only runs on the Master CPU. In addition, the number of Rockets and Targets, and the size of the configuration specific task array is made available. In some sense, these variables can be thought of as parameters to the main program, similar to many host-based Ada program invocations where the command line is provided to the application program as a parameter to the main subprogram. One important distinction from main program parameters is that the runtime package interface is available during the elaboration of other application packages. This allows the size of non-static objects, such as the constraints of the task array, to be based on configuration information.

The configuration selected during setup determines two important aspects of the distributed system: where the tasks are to be resident and how many tasks are to be allocated in application task arrays. Since the same code is present on all processors, a directory is used to determine where they are to be located. The tasks are effectively made remote by suspending them during their normal activation process. A future enhancement could be to

add a "self-sizing" mode that would have the master processor search for available processors on start up and after failures and utilize as many as are available.

6. Performance Characteristics

The BDS system has been tested to execute on one, two, and three processor configurations. When distributed onto two processors, the simulation tasks run on the second processor. When the third processor is added, the size of an unconstrained task array is increased from one to two, and the second task element of the array is located on the third processor. These tasks in the array divide up the work load of computing the rocket guidance equations.

The performance of the system under different processing loads was studied by collecting timings which reveal the ability of the system to meet the 100ms deadline for computing the next rocket guidance command. A significant portion of this computation is the time it takes to compute the individual aimpoints. This was measured to be approximately 6ms, but varies based on the actual values of the variables in the equations. The variation is due to the algorithm used for square-root which is iterative and will terminate when the current value is known to be within an error bound; and because the multiply and divide machine instructions vary in execution time.

Two important aspects of the timing analysis are the relationships among tasks and the ability to achieve performance gains even with these inter-dependencies. To illustrate this point, a brief description of the sequence of activities for rocket control must be presented:

1) Each rocket control cycle starts with the reception of new rocket flight information. This arrives during a rendezvous with a report buffer task which relays the information from the simulator. Normally, the control task is suspended while waiting for the buffer task to rendezvous, indicating the presence of a new rocket report. When the report arrives, it is provided to the control task and it begins the cycle of computation.

- 2) The first part of the computation is a correlation step where the current report is correlated to previous reports to create a tracking history. New rocket launches are detected and their histories are initialized. Also, rocket detonations are detected and they are marked as destroyed. Current counts of active and destroyed rockets are maintained and passed on to a status task which updates the screen statistic values. During the correlation processes, a "move" list is generated for updating the rocket symbol positions on the display.
- 3) Once the correlation has completed, the guidance tasks are given the rocket and target histories which are used to generate the new trajectory data. The guidance tasks are then allowed to run through the trajectory calculations to produce a new aimpoint for each rocket.
- 4) The control task continues to run in parallel with the guidance tasks after providing them with the information they need. It takes the "move" list generated during the correlation and provides it to the display task.
- 5) The control task then searches the target list to select the next ideal target if the automatic firing mode is selected and there is an available rocket. (The automatic firing mode indicates that the BDS is to select the next target rather than having the operator select the next target.)
- 6) The control task then awaits completion of all guidance computations. When the new aimpoints are provided to the control task, it then rendezvous with a guide buffer task which relays the guidance message to the rocket simulator. This completes the timed cycle of interest.

Note that the guidance tasks form a task array which is only one element in size for the one and two processor configurations, but expands to two tasks in the three processor configuration. This distributes the work load of aimpoint computations among two processors and allows the system to support additional simultaneous rocket flights without missing the measured 100ms deadline.

6.1 Benchmark Results

All measurements were taken with 40 active targets. During normal conditions, rocket accuracy was observed to be nearly 99 percent; that is, around one (1) target missed for every 100 rockets expended. In overload conditions where the deadline was missed, rocket accuracy dropped to nearly 0 percent resulting in every rocket missing. The BDS consisted of 11 conventional tasks, an unconstrained array of guidance tasks, and the main program. The entry calls made between the tasks are shown in Figure 2. For specific details of system operation, refer to documentation included in the application source code which is provided in Appendix A. A general description of the tasks are provided below (in decreasing priority order).

In_Char task Accepts input from the mouse device (mouse interrupt task).

Save task Buffers mouse data for controlling reticle updates.

Display task Performs all graphics display updates.

Track_Data task Buffers target position information between the target tracker and the rocket control task.

Report_Buf task Buffers rocket position reports from the simulator to the rocket control task (part of the simulator).

Guide_Buf task

Buffers rocket guidance commands from the rocket control task to
the simulator (part of the simulator).

Rock_Sup task Implements the rocket flight simulation (part of the simulator).

Targ_Sup task Generates and moves simulated targets (part of the simulator).

Control task Provides overall control for rocket monitoring and flight updates.

Guidance task

Called by the rocket control task to compute flight guidance aimpoints (this is an array of tasks).

Track task Produces target tracking information for the display and rocket control tasks.

Update task Updates the statistical status information on the screen.

Bds procedure Main program used to initialize system operation.

BDS Top Level Design Math Status Misc. Tan Data Traject Guide Arc Tan Sart Engage Grid_to_Pixel Machine Shapes Mouse Config Buffer Interrupt Target Track Rocket Control Graphics Display Rocket_ Simulate RDL Sensor Targ_Sup Rock_Sup Report_Buf 🚄 Datted Line Guide_Buf Indicates Entry Call

Figure 2. Top Level BDS Design

The measured time is the period that begins when the rocket report becomes available to the control task and ends when the guidance message is provided to the simulator. The allowable time for this has been established as 100ms based on the rocket update characteristics. Successive measurements were taken increasing the number of rockets until either the deadline was missed, or until the number of rockets reached 20. The tested configurations are as follows:

1 Processor : all tasks are resident

2 Processors : the simulator tasks are on the 2nd CPU

3 Processors : the simulator is on the 2nd CPU, the second

guidance task is on the 3rd CPU

# of Processors	# of Rockets	Time to complete 100ms Cycle
1	5	s ms د
1	10	105 ms
2	5	38 ms
2	10	77 ms
2	15	118 ms
3	5	41 ms
3	10	73 ms
3	15	79 ms
3	20	90 ms

Extrapolated Saturation Points for Each Configuration:

# Processors	# Rockets
1	9
2	13
3	22

Several observations on the benchmark results can be made. Because the simulator does not participate in the rocket control calculations, and the system is lightly loaded during the control of up to 5 rockets, there is no performance benefit in increasing from 1 to 2 CPUs

with only 5 rockets to control. When the number of rockets rises to 10, the dual-CPU system shows a substantial benefit over the single-CPU configuration because both the control computations and simulator computations increase as a function of the number of rockets.

The dual-CPU configuration misses its deadlines when more than 13 rockets become airborne. This is roughly a 44% gain in the number of rockets supported with the single-CPU configuration. The three-CPU configuration is expected to miss its deadlines when more than 22 rockets become airborne. This is roughly a 144% gain over a single-CPU and a 69% gain over the two-CPU configurations. The performance benefits are not linear because only segments of the application are being distributed, and because precedence relationships among the tasks restrict full processor utilization.

Not measured in the benchmarks is the performance of lower priority tasks which are much more substantially impacted during increased processing load. The low priority tasks are the first to relinquish the processor and therefore will suffer first in overload conditions. For example, the task responsible for updating the display statistics begins to starve when only 3 rockets are launched on the single-CPU configuration. In comparison, it continues to run (although at lower frequency) even during overload conditions on the dual and triple processor configurations. The extended life of the lower priority tasks is primarily due to the additional "background" cycles available on the multiple CPU configurations.

It is also due to the I/O blocking time during network communications. That is, while the high priority task waits for access to the network resource, it is blocked from execution which allows lower priority tasks to run. This is one interesting aspect of a distributed system that is not present in shared-memory multiprocessors or single processors. This I/O blocking time has the effect of transferring some execution time directly from high-priority tasks (performing network communications) to other tasks on the system. To the extent that the

I/O blocking time exceeds the processing time required by middle priority tasks, or if the middle and high priority tasks block in contention for the network, then very low priority tasks can be allowed to run. This phenomenon is observed in the demonstration project by the low priority task updating the status display even while high priority tasks miss their deadlines in the multiple processor configurations. The I/O blocking time during rocket "get_report" and "put_guidance" communications is estimated at under 2ms per 100ms cycle. This 2% CPU time is sufficient to prevent starvation in the low priority tasks.

6.2 Performance Gains

A common objective discussed for parallel systems is to achieve "scalable" increases in performance. This term implies that when processing resources are added, the "useful processing increases by a constant factor of the number of processors. For example, for a scale factor of 0.9, then if five times the processors are used, this will result in 4.5 times the useful processing that will be accomplished. This type of measurement is typically applied to computations where the time the computation takes place is only relevant because tomeone is waiting for the final output of a very large set of computations. However in real-time systems, consistent meeting of short-term deadlines is the measure of performance. For this reason, the conventional sense of "scalable" performance is not totally appropriate.

A real-time system that always meets its deadlines with one processor will not perform better when adding additional processors. It is much more typical that a basic accuracy specification must be met and no benefit is gained by exceeding it. In these cases just meeting the deadline is as good as meeting the deadline in half the required time. The only benefit is the excess capacity which allows future expansion. However, there is a class of algorithms that increase their accuracy based on the amount of time available to execute which might benefit from additional processing resources. So except for reliability concerns

and special purpose algorithms, performance can and often does get worse because of communication overhead.

On the other hand, when processing demands limit the capacities of an embedded system, there is potential for substantial benefits to be gained by using additional processors. This is because there are often fixed and variable components to the processing required for system operation. In the BDS, fixed components include the status update, rocket control and report message formatting and transmission, sensor message reception, graphics reticle motion, and runtime overhead (primarily context switching). All of these operations did not vary with the number of targets or rockets supported. Since the additional processing is applied to the variable portion, the increase in system capacity can greatly exceed the increase in processing resources. We refer to this increase in performance as "leveraged performance". In particular, if a system is 70% utilized due to fixed processing requirements, only 30% remains to perform the functions identified as the principal system mission. This 30% can be highly leveraged by adding an additional processor to achieve a nearly 300% increase in system capacity. This characteristic is not clearly evident in the demonstration benchmarks because the tests were taken without the user interface being active and because the target support processing is quite low. The user interface includes moving the reticle which can increase system load by up to 20%, but without a mouse simulator to provide constant simulated motion, it was decided to test the system without the effects of the user interface. The result is that the fixed processing for the demonstration benchmarks was rather low.

The benchmark results are also somewhat biased by a decision to locate only the simulator tasks on the second processor. This resulted in considerable idie time on the second processor which could have been used to do additional rocket calculations. The reason for

the decision was to insure the accuracy of the simulation by making sure that nothing interfered with the simulator's processing. A similar effect was created on the three-CPU configuration by choosing a processing balance between the two guidance tasks that was not optimal. Ideally, the two distributed computations would complete at roughly the same time, but the balance selected caused one to finish earlier than the other. The incorrect balance was made because of uncertainties in all of the timing factors that make up the processor loading. Further work is necessary to facilitate an automatic method of determining the optimal configuration for performance.

Thus this study identified two major beneficial factors when considering the use of additional processing resources:

- 1) When there is a large processing requirement for fixed overhead operations and the addition of processing resources can be applied to the mission-specific processing, which is otherwise limited by the available resources, there can be a leveraged benefit to the system capabilities.
- 2) When there is the potential for several independent sets of calculations to be performed, the performance increase can be effectively spread over a moderate number of CPUs, limited by the capacity of the network.

The ability to scale-up to a very large set of processors was not investigated by this project. It is believed that the current network architecture would severely restrict such a scale-up due to network contention.

7. Design of Systems Using Distributed Ada

7.1 Hardware Considerations

There are several aspects of system design that impact the utility of the system. Experience has shown that the implementation of distributed systems must be done with the expectation that the hardware will provide reasonable support for fundamental services. These services include:

- 1) The ability to transmit urgent information in a timely fashion. This requirement usually implies that messages can be prioritized and that the priority is observed in any situation where the potential delay exceeds the allowable allocated time.
- 2) The ability to broadcast, and later determine that all recipients obtained the message.
- 3) Sufficient hardware buffering support so that incoming messages will not overrun, resulting in the need for re-transmission.
- 4) Error detection (or correction) to provide indication of successful message transfer immediately (i.e. within 10 us) by the hardware.
- 5) The ability to synchronize among the processors.

To a large extent, the network topology has an influence on the real-time response and priority of services provided. The most common network topologies include: Rings, N-dimensional Hypercubes, and Buses. This demonstration project utilized a standard bus topology network - Ethernet, because of its availability and cost. As discussed in a prior report [1], Ethernet is not ideal for real-time use since there is no provision for hardware

generated acknowledgements and because access to the bus is granted on a contention basis. The contention-based access method is a poor choice for real-time because no provision is explicitly provided for priority, and two nodes trying to access the medium simultaneously result in a collision. When this occurs, they both wait a random period and retry later. The effect of this on congested networks is a queuing order that is often first-in, last-out. This results in very high worst-case response times which makes meeting fixed deadlines difficult. However, in the demonstration project contention was low since bus utilization was kept below 2%. A more appropriate network for real-time would be either a point-to-point interconnect or a star topology that provided guaranteed response time to all network nodes.

7.2 Software Considerations

The ability to use distributed Ada depends on being able to separate program execution into tasks that can execute in parallel. For embedded applications, usually a small set of independent tasks are naturally present due to their interaction with external objects. These objects (like a video display, operator keyboard, or rocket) are independent and operate concurrently in the real-world. Their control or monitoring therefore naturally maps to separate tasks. The order of execution for these tasks is often asynchronous and is dictated by external events. This type of concurrency is referred to as "natural parallelism". Parallel execution can also be performed on any independent set of operations that are not ordered. Typically these are done as sequential or iterative processes because treating each operation as a separate task would result in additional context switches. However, applying multiple processors to the computations can more than compensate for the small overhead of the additional context switches. This forced parallelism can help to increase the amount of processors which can be effectively used.

As a design goal, system designers should attempt to reduce interdependency of tasks as much as possible. Usually this requires detailed knowledge of the application and implies being able to partition system functions into tasks that have a high degree of autonomy. Tasks (other than monitors for shared access control) should not be used for activities that cannot be done in parallel.

Secondly, avoid serialization in the order of task synchronization if possible. For example, if task A must wait for both task B and task C to provide data, it should not enforce the order of which task it must rendezvous with first unless they are always guaranteed to arrive in a particular order. Instead, a conditional rendezvous should be used to prevent unnecessary serialization of events.

Third, tasks which must reside on a particular processor because of required access to hardware interfaces should provide the minimum service necessary to support efficient use of that hardware. This approach provides additional design freedom to locate a greater percentage of the required processing demands according to communication demands rather than specific hardware dependencies.

Finally, the software should be designed to operate correctly during overload conditions. This requires the ability to detect the overload condition, maintain consistency of data, and (ideally) to support the dynamic load shedding necessary to make good use of the available processing resources. This last provision is absolutely necessary to continue operation in the presence of hardware failures, since the loss of processing resources will almost certainly increase the likelihood of overload conditions.

7.3 Differences Between Distributed and Uniprocessor Implementations

There were two major unexpected differences identified between execution on a distributed system and a uniprocessor system. The first was mentioned in the performance section and involves the impact of I/O blocking on high priority tasks. This prevents starvation in low priority tasks and allows the status display and target tracker tasks to continue to run even under conditions of very high utilization. This effect was due to the desire to run tasks as often as possible during non-overload conditions and yet allow the more critical tasks to obtain the CPU during overload. This may have been avoidable using a more complex algorithm to schedule the status and target tracker tasks based on the available time for them to run.

The second difference was due to a design assumption that made the program erroneous. During a prototype enhancement of the application, a change was made in the program to use message sequence numbers in order to detect the loss of successive reports (due to buffer overwrite during overload conditions). Although the code was made obsolete by another function and was essentially removed prior to system integration, a seemingly harmless portion remained which examined the rocket report message. This message was a task entry "out" parameter from the simulator's report buffer task and it was examined to detect sequence numbers that changed from report to report. By convention, only the first "N" items in the message are considered valid, where "N" is provided at the beginning of the message. However the sequence monitoring code did not examine this count before testing the sequence numbers and assumed that the first rocket position was always valid. In the single-CPU configuration, the entry call "out" parameter was passed "by address", and the buffer task only updated those records that were active. In distributed configurations, entry call parameter passing must be done as "copy-in, copy-out" so the data can travel over a

network and it operates without regard to the contents of the objects being copied. The result is that all of the records are updated during each entry call. This had the effect that the sequence software became confused and rejected messages due to apparent bad sequence numbers. The root of the problem was due to a dependence on the parameter passing mechanism used for entry parameters. RM 9.5(6) states that:

"The parameter modes for parameters of the formal part of an entry declaration are the same as for a subprogram declaration and have the same meaning (see 6.2)."

RM 6.2(5) defines **out** mode parameters as:

The formal parameter is a variable and permits updating of the value of the associated actual parameter.

The value of a scalar parameter that is not updated by the call is undefined upon return; the same holds for the value of a scalar subcomponent, other than a discriminant. Reading the bounds and discriminants of the formal parameter and of its subcomponents is allowed, but no other reading.

RM 6.2(7) continues:

"For a parameter whose type is an array, record, or task type, an implementation may likewise achieve the above effects by copy, as for scalar types. In addition, if copy is used for a parameter of mode out, then copy-in is required at least for the bounds and discriminants of the actual parameter and of its subcomponents, and also for each subcomponent whose type is an access type. Alternatively, an implementation may achieve these effects by reference, that is, by arranging that every use of the formal parameter (to read or to update its value) be treated as a use of the associated actual parameter, throughout the execution of the subprogram call. The language does not define which of these two mechanisms is to be adopted for parameter passing, nor whether different calls to the same subprogram are to use the same mechanism. The execution of a program is erroneous if its effect depends on which mechanism is selected by the implementation."

The essential difference in implementation approach is that when call by reference is used, only those records that are explicitly assigned a value are altered by the entry call. When call by copy-in/copy-out, all of the values are altered. In either case it is considered erroneous to reference a value that is not updated, but in fact the single-CPU application was doing this. There was no effect in the call by reference implementation, but when the task became distributed and call by copy was used, the latent error in the software was

activated. The lesson is that erroneous programs are more likely to operate incorrectly on distributed systems because a different parameter passing mechanism is likely to be used for composite objects.

8. Limitations of the Distribution Support

8.1 Task Identification

The current mechanism for identifying tasks is the task's base priority. This was a convenient mechanism to use for a number of reasons. First, it is stored in the vendor's runtime task control block and is therefore available during any runtime call. It can be set via a **pragma** in the source code and therefore a unique ID can be associated with each task type. This approach was selected because it provided an expedient solution to identifying application tasks to the distributed runtime without modification to either the compiler or the vendor runtime.

Future versions would obviously use some other field in the control block because this technique is very limited. One complication is with the handling of task arrays. Since they are of the same task type, all tasks in an array have the same priority. This problem was circumvented by leaving sufficient space between adjacent priorities to change the priority of each task in the array during activation if necessary to make them unique.

Another approach was considered, but rejected because of development time. This approach was to use an intermediate file produced for a debugger to correlate the actual name of each task with the execution addresses where the task type is created and the task object is activated. This information would be combined with the designer's distributed configuration specification and loaded with the runtime. Since this specification would use the actual expanded name of each task, the limitation of having only as many tasks as there are priorities would be removed. During task type creation and task activation, the return address on the stack during the runtime call provides the execution address and could be used to identify the task type and object in the configuration specification. This approach

requires the development of a tool to process the compiler intermediate files, linker maps, and the distributed specification file. There may be other problems that are not obvious but the method appears suitable for some applications.

8.2 Code Replication

Currently all of the code for each task must be resident on each processor. In some applications this is unacceptable since there will not be sufficient memory to support the entire application on every processor. One approach to reducing this overhead may be to use subunits for all task bodies and then create a sublibrary with dummy bodies appropriate for each processor configuration. Linking the program with respect to each sublibrary will then produce a load image for that processor configuration. However, some compilation systems do not allow subunits to appear in any library other than the library in which the parent unit was compiled. In any case, it would probably be advisable to have a tool which automatically created the necessary sublibaries and subunit bodies and therefore reduce the chance of error in generating the individual load images.

8.3 Network Error Recovery

Each network message is acknowledged which allows transmission errors to be detected, however there is no provision for re-transmission. The system will simply shutdown and reconfigure on the first error. This is somewhat severe since re-transmission can usually be done without loosing real-time if the acknowledgements are prompt. (Measurements on the BDS indicated typical acknowledgement times of 400 to 800 microseconds.) The only complication is saving the data for re-transmission. Currently, transmissions transfer application variables directly to a single hardware transmit buffer. Once the message is sent, the buffer is reused and the application task is allowed to continue. There is no provision to

save the data for later re-transmission. This could be done with very little overhead by allocating additional space in the hardware buffer for transmissions, however the hardware in use is configured with only 8KB (kilobytes) of memory and therefore this is not practical. By expanding the memory to 32KB, or by using system memory and performing an additional copy, the data can be retained until the acknowledgment is received and the buffer can be freed.

9. Compiler and Runtime Problems

Considerable effort was spent isolating problems associated with the Ada implementation. The implementation was an upgrade from the version used on the previous demonstration project and was far more reliable than that earlier version. Nevertheless because code generator errors continued to appear, a decision was made to not use optimization for compilation of many of the units and to greatly restrict use of **pragma** inline. This noticeably improved the reliability of the generated code.

Even with these restrictions, two problems were identified during final integration testing. In one unit where inline was still used, the compiler failed to generate the same (correct) addresses for variables which were initialized during package elaboration. These variables happened to be pointers within a circular queue, and the error would generally go annoticed if the values of the memory locations happened to be the same. This was typically the case during testing since the system memory initialization routine would zero all of memory to prevent parity errors. However, depending on the contents of memory when the program was loaded the system could crash if the two values were very large or not equal to each other. While single stepping through the program it was noticed that the pointers were being initialized properly which was the obvious expected source of the problem. However, during program execution the system would still crash, and the pointers would have invalid ¹ata. Use of the processor's special debug registers to halt on references to data, much like an in-circuit emulator, helped to track down the problem and realize that two different locations were being used for the same variable.

The second problem was related to computation of 32-bit fixed point values. When a small value is divided by a large value resulting in zero, and exactly one of the operands is negative, the runtime would incorrectly assume that an overflow had occurred because the

result was not negative. Instead it was zero, which was also a legal value. This problem was fairly quickly resolved since it has been noticed in the earlier release of the runtime. It was fixed by changing the conditional branch instruction to allow for zero results.

10. Summary

This project demonstrated that the use of distributed Ada can provide increased performance benefits and fault tolerance for a reasonably complex real-time application. These benefits can take the form of simply using the parallelism natural in the application or by expanding the parallelism using task arrays to compute multiple independent calculations. In particular, the ability to distribute elements of a single Ada object was demonstrated by distributing an array of tasks to divide up the workload among several processors.

Task precedence relationships create considerable design difficulties when trying to analyze a system for optimal parallel operation. New tools and scheduling paradigms are required to assist designers in resolving these difficulties. However, techniques do exist to provide marginal improvements in parallel operation by reducing dependencies and encouraging the judicious use of synchronization primitives. An example of such a technique is the use of buffering schemes and control variables to de-couple tacks. This technique may require detailed knowledge of the application to insure proper execution with the buffering scheme.

The concept of "scalable performance" was discussed, and a more appropriate term for real-time embedded systems: "leveraged performance" was introduced. This concept recognizes the limiting factors in real-time systems, and emphasizes the potential of capacity increase factors greater than one (1) for applications with a substantial portion of processing dedicated to execution requirements of fixed duration.

A very important potential gain of distributed systems is the ability to utilize the natural redundancy in the hardware to achieve increased fault tolerance. Typical embedded systems have had, and will continue to have multiple processors. The problem of reconfiguring the

system during failure conditions has prevented widespread use of fault tolerance techniques. Distributed Ada appears to be a good candidate for reducing this problem to a manageable level for applications which should operate in the presence of failures.

Fault tolerance concerns were examined and a clear need arose to provide an interface between the fault tolerant runtime and the application. The application must be able to have some sense of the available processing resources in order to adapt to the configuration.

An example was shown of an erroneous program which failed on the distributed implementation when it had previously run correctly in a uniprocessor configuration. A conclusion was drawn that since distributed systems are likely to use both "pass by copy" and "pass by reference" mechanisms for parameters of composite types, programs which erroneously depend on the parameter passing mechanism are more likely to fail on distributed systems.

Finally, compiler reliability still poses a serious problem when trying to obtain the highest performance possible using complex optimizations and language features such as **pragma** inline. Mission and safety critical applications should consider the impact of having to operate without the use of these performance enhancements.

11. References

[1] CECOM Center for Software Engineering, "Real-Time Ada Demonstration Project", CIN # C02 092LA 000900, Final Report, delivered by LabTek Corp., May 31, 1989.

[2] Reference Manual for the Ada Programming Language, ANSI/MIL Standard 1815A-1983.

12 Appendix A - Border Defense System Ada Source Code

The source code for the BDS system follows in alphabetical order of the unit names (specifications precede bodies).

```
--% UNIT:
            Aim Data package spec.
--% Effects: Holds Rocket/Target history information for Guide.
--% Modifies: Rocket_Info is global data and is modified by Guide.
--% Requires: Initialization is required and performed by Guide.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: L. Griest.
-- | PACKAGE SPEC : Aim_Data
--|
-- | Aim_Data contains the information for Guide necessary to control the
--| rockets in flight. The data is initialized by Guide when the rocket
-- | is taking off from a launch position. Note that curr nnnn signifies the
--| most current postion of an object and that last_nnnn signifies the position
--| the object had immediately prior to this interval (assuming no overload
-- | condition). The prev_nnnn field exists only for rockets and represents
-- | the position the rocket had two intervals prior to this one. This field
-- is used to calculate the velocity of the rocket last interval in all three
-- axis. This information is not needed for targets.
-- RATE_REC_TYPE is necessary to provide the accuracy necessary when
--| calculating accelerations and velocities, particularly at launch times.
--|
-- Modifications Log
-- 89-11-09 : LJG => Original created.
with Types; use Types;
package Aim_Data is
 type RATE_REC_TYPE is record
   X : Types.RATE TYPE;
   Y : Types.RATE_TYPE;
   Z : Types.RATE_TYPE;
  end record;
  type ROCKET_INFO_TYPE is record
   LAST_TARG
                    : Types.POSITION_TYPE;
   CURR_TARG
                    : Types.POSITION_TYPE;
                    : Types.POSITION_TYPE;
   PREV_ROCK
   LAST_ROCK
                    : Types.POSITION_TYPE;
   CURR_ROCK
                     : Types.POSITION_TYPE;
   OLD_AIMPOINT
                      : Types.AIMPOINT_TYPE;
                      : BOOLEAN; -- rocket currently in boost phase?
   BOOST PHASE
```

end record;

type ROCKET_INFO_ARRAY is array(Types.ROCKET_INDEX_TYPE) of ROCKET_INFO_TYPE;
ROCKET_INFO : ROCKET_INFO_ARRAY;
end Aim_Data;

```
--% UNIT: Aimpoint function spec.
--% Effects: Compute new aimpoint based on acceleration requirements. --
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | FUNCTION SPEC : Aimpoint
--| Aimpoint is responsible for returning a new elevation and azimuth to
--| the caller based on the acceleration adjustment.
--|
-- Modifications Log
-- 89-11-6 : TEG => Original Created.
with Types; use Types; -- for operators on types only!
with Aim_Data; use Aim_Data;
function Aimpoint(OLD_AIMPOINT : Types.AIMPOINT_TYPE;
                 ACCEL_ADJUST : Aim_Data.RATE_REC_TYPE)
                 return Types.AIMPOINT_TYPE;
```

```
______
--% UNIT:
            Aimpoint function body.
--% Effects: Compute new aimpoint based on acceleration requirements. --
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | FUNCTION BODY : Aimpoint
--|
--| The selected aimpoint is a function of the desired change in acceleration
--| for each of three axis and the current aimpoint. The following rules are
      TO INCREASE Z ACCELERATION adjust elevation towards 16384 (straight up)
---
      TO INCREASE X ACCELERATION adjust azimuth towards 0
                                                         (straight right)
--| Obviously there is some interaction among these components. Z
--| is the controlling axis since its acceleration is not dependent on azimuth
--| and the accelerations in X and Y are dependent on elevation. Once the
--| change in elevation has been established, the impact on X and Y
--| accelerations are computed, then a proper azimuth is selected based
--| on the above rules.
-- | To implement the friendly fire suppressor only generate and process
--| elevations between -16384 (straight down) and 16384 (straight up).
-- and azimuths between 0 (straight right) and 32767 (straight
--| left).
--! When adjusting elevation, reduce negative impact since gravity will
-- have a compensating effect.
--1
-- Modifications Log
-- 89-11-03 : TEG => Original Created.
function Aimpoint(OLD_AIMPOINT
                              : Types.AIMPOINT_TYPE;
                ACCEL_ADJUST
                              : Aim_Data.RATE_REC_TYPE)
                return Types.AIMPOINT_TYPE is
  max climb : constant := 16384;
 max_descend : constant := -16384;
  left
            : constant := 32767; -- full left while going forward
                               0; -- full right while going forward
            : constant :=
  elev factor: constant:= 10000; -- controls flexibility in turning rocket
  az_factor : constant := 10000; -- controls flexibility in turning rocket
  NEW_AIMPOINT: Types.AIMPOINT TYPE;
  ADJUST_ELEV: Types.EXTENDED_BAM; -- use 32-bit values for intermediate
```

```
ADJUST_AZ : Types.EXTENDED_BAM;
  TEMP
             : Types.EXTENDED_BAM;
begin
-- Put("In Aimpoint: ADJUST_ELEVATION: ");
-- Change elevation to effect Z acceleration first.
  ADJUST ELEV := Types.EXTENDED_BAM(ACCEL_ADJUST.Z * elev_factor);
 if ACCEL_ADJUST.Z < 0.0 then
   ADJUST_ELEV := ADJUST_ELEV / 2; -- reduce descend angle because of gravity
 TEMP := Types.EXTENDED_BAM(OLD_AIMPOINT.ELEVATION) + ADJUST_ELEV;
-- Must perform limit check on climb/descend.
  if TEMP > max climb then
   NEW_AIMPOINT.ELEVATION := max_climb;
  elsif TEMP < max_descend then
   NEW_AIMPOINT.ELEVATION := max_descend;
   NEW_AIMPOINT.ELEVATION := Types.BAM(TEMP);
  end if;
-- NOW PROCESS AZIMUT4 (Using only X, let Y take care of itself!)
 ADJUST_AZ := Types.EXTENDED_BAM(-ACCEL_ADJUST.X * az_factor);
-- Do limit checks to make sure we don't start turning back towards FLOT
  TEMP := Types.EXTENDED BAM(OLD AIMPOINT.AZIMUTH) + ADJUST AZ;
  if TEMP > left then
   NEW AIMPOINT.AZIMUTH := left;
  elsif TEMP < right then
   NEW_AIMPOINT.AZIMUTH := right;
    NEW_AIMPOINT.AZIMUTH := Types.BAM(TEMP);
  end if;
  return NEW_AIMPOINT;
end AIMPOINT;
```

```
-----
--% UNIT:
             BDS Spec & Body.
--% Effects: Initiates main processing, loops recording idle time.
--% Modifies: No global data is modified.
--% Requires: Status.Initialize be called before Mouse.Initialize.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest
----- Distribution and Copyright -----
-- Derivation : LabTek Border Defense System V2.0
-- This Border Defense System Software inherits the LabTek copyright.
-- The following copyright must be included in all software utilizing
-- this application program.
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-- This software and its documentation are provided "AS IS" and
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-- for a particular purpose exist.
·· In no event shall any person or organization of people be
-- held responsible for any direct, indirect, consequential
-- or inconsequential damages or lost profits.
-----END-PROLOGUE-----
-- | TASK BODY : BDS main procedure
--1
-- | The BDS main procedure is used to synchronize the start of events within
··| the entire system. During elaboration until the start of the procedure,
--| the system will settle to a known state. Then when the call to Status
--| is performed, the statistics titles will be printed on the screen. After
--| this is performed the Mouse initialization is completed. Then two
```

```
--| successive entry calls are done. The first starts the Rocket.Control task
--| going. The second signals the Track task to begin processing target
--| The "--$TP(NNNN) ..." signifies a Time Point stamp location. There is a
-- | tool built by LabTek which transforms these comments to Ada code which
--| performs a call to a TimeStamp procedure. In order to keep from filling
--| memory too fast, a loop is used to force the main procedure to loop slower
--| than it normally would. This time stamp routine will enable approximations
--| of the amount of free time the processor has, since this procedure has the
-- | lowest priority.
--|
-- Modifications Log
-- 88-09-30 : TEG => Original created.
with Config;
                      -- global configuration parameters
                      -- updates statistics used
with Status;
                       -- global types definitions
with Types;
with Mouse;
                       -- mouse movement and rocket launching
                       -- rocket attitude and aimpoint calculations
with Rocket;
with Target;
                       -- generation of various targets
with Interrupt_Control; -- enabling and disabling of (all) interrupts
with Machine Dependent; -- individual pixel plotting for EGA
with Time Stamp:
                      -- run time profiler
with Distrib;
procedure BDS is
-- This is the main program for the Border Defense System. It has only
-- two calls which are of any importance, i.e., the other code is for
-- timing purposes only. The first call performs initialization of the screen
-- statistics descriptions and their initial values. The second cal! starts
-- the mouse.
                                           -- for visibility to "+"
use Types;
pragma PRIORITY(Config.bds_priority);
COUNT : Types.WORD;
                                           -- these two variables are for
SLOW : Types.WORD;
                                           -- slowing the time stamps
begin
  if Distrib.MASTER then
   Status.Initialize;
                                            -- print screen statistics
                                            -- must be done after status signal
   Mouse.Initialize:
   Rocket.Control.Start;
   Target.Track.Start;
  end if;
                                            -- done with initialization
  loop
```

```
Time_Stamp.Log(0001); --$TP(0001) BDS main time stamp
SLOW := 1;
for COUNT in 1..2000 loop
    SLOW := SLOW + 1;
end loop;
end loop;
end BDS;
```

```
--% UNIT: Config Spec. --
--% Effects: Provides system-wide configuration constants. --
--% Modifies: No global data is modified. --
--% Requires: No initialization is required. --
--% Raises: No explicitly raised exceptions are propagated. --
--% Engineer: T. Griest. --
```

-- | PACKAGE SPEC : Config

--1

The Config package (which currently has no body associated with it) can be contrasted to the Types package. While the Types package is responsible for declaring the various global types used throughout the BDS, the Config package is used to declare global constants. The rocket launch trajectory is used to determine the azimuth and elevation of the rocket before takeoff. The kill radius is used to determine the explosive power of the rocket as it hits near a target. If the target is within the radius determined below,

--| then it is considered to have been close enough to the rockets explosion to --| have caused damage severe enough to render it immobile and harmless. Note

--| that the BDS does not take into account the case of a rocket doing "some" --| damage on the target; every target is considered to be totally missed or

--| fully hit. There are two battle areas that can be considered in the BDS.

--| The first is the battlefield area which is the "real" area of conflict and
--| the other is the screen battlefield which is shown to the user. The "real"

--| battlefield is used by the Target Tracker, the Rocket Controller, and their --| respective data links to their sensors. In order to provide a proportional --| view of the "real" battlefield area, the number of pixels in X and Y was

--| calculated. The screen battlefield does not take up the entire screen;

--| some is left for the display of statistics. The calculations done in the --| "real" battlefield are three dimensional, those on the battlefield screen --| two dimensional.

--| The bytes per storage unit is used for transportability reasons. A count
--| of bytes required for each tasks' stack (including nested procedures) was
--| included so that the application could be less implementation dependant.
--| To leave defaults in place would require that the largest stack frame

--| be used for all tasks stacks regardless of the actual space needed. By
--| specifying the amount of stack needed on a per task basis, less memory
--| is used.

The interval constant declared below is the basic unit on which timing
In the BDS is performed. It specifies that an entire iteration (which
Includes a rocket update, a target update, and a possible mouse or
Istatistics update) all be performed in 100 millisecs. The delays specified
In the timed tasks (Target.Track, Rocket.Control, Simulate.RDL.Rock_Sup and
Includes.Sensor.Targ_Sup are the timed tasks currently) are calculated
Is so that they will wakeup once every interval (100 ms). The rest of the
Is system derives its timing from these drivers.

--| The priorities are grouped together here because priorities specified --| individually in each task declaration does not help anyone looking to --| determine priorities which are relative to each other. The Mouse_Buffer

```
-- task is not the highest priority task. Since there is a mouse associated
--| with the system, which uses an interrupt entry call and is treated as a
-- | task, it uses the hardware interrupts to determine its priority. Since
-- | a task must always be sitting at the accept to receive the interrupt tasks'
--| entry call, the Mouse_Buffer task (which is responsible for translating
-- the X-Y motion of the mouse (and any buttons pushed) into motion of the
-- | reticle) is defined at the highest software priority level. For the same
-- | reason, as well as to be able to keep the screen in real time, the Graphics
--| task is declared with the next available priority. In order to increase
--| throughput from the simulator to the BDS the buffers which route rocket
-- and target data are declared with the next highest available priority.
--| Because the simulator contains the two tasks which are scheduled according
-- to a deadline (RockSup and TargSup) these tasks are next in the priority
-- | line. Then the rocket controller for the BDS and the target controller
--| for the BDS are (respectively) assigned their priorities. Below these
-- tasks is the statistics task priority. It is allowed to be low because
--| of the liberal timing requirements placed on it by the requirements
--| documentation. Obviously, since the main program performs no function
-- | which is of use to the BDS, it is assigned the lowest priority.
--1
-- Modifications Log
-- 88-10-11 : TEG => Original created.
-- 89-11-16: MPS => Added launch attitudes and locations,
with System; use System;
package Config is
-- The following two constants allow the space needed for the various tasks to
-- be declared in bytes.
byte
                         : constant := 8;
                                               -- 8 bits
bytes_per_storage_unit := byte / System.STORAGE_UNIT;
-- Now define battlefield area perimeters
meters in battle area
                        : constant := 4_000.0; -- in X and Y direction
meters_per_X_pixel
                         : constant := 9.625; -- rounded up to nearest
meters_per_Y_pixel
                         : constant := 11.875; -- Types.METER.
max_pixels_in_battle_area : constant := meters_in_battle_area
                                                / meters per X pixel;
-- Task priorities in order of decreasing urgency.
-- NOTE: MOUSE IN CHAR has no priority because it runs
-- completely at the hardware interrupt level.
-- The idea implemented here is that all the Simulator information is
-- of higher priority than the actual Border Defense System code.
```

```
: constant PRIORITY := PRIORITY'last;
save_priority
                                                              -- Mouse Buffer
                   : constant PRIORITY := save_priority-1;
display_priority
                                                              -- unaphics
track data priority : constant PRIORITY := display_priority-1; -- Target
report_buf_priority : constant PRIORITY := track_data_priority-1;-- Sim.RDL
guide buf priority : constant PRIORITY := report_buf_priority-1;-- Sim.RDL
rock_sup_priority : constant PRIORITY := guide_buf_priority-1; -- Sim.RDL
targ_sup_priority : constant PRIORITY := rock_sup_priority-1; -- Sim.Sensor
control priority : constant PRIORITY := targ_sup_priority-1; -- Rocket
guidance priority : constant PRIORITY := control_priority-1; -- Rocket
track_priority : constant PRIORITY := guidance_priority-2; -- Target
update_priority
                   : constant PRIORITY := track_priority-1; -- Status
                   : constant PRIORITY := update_priority-1; -- Main
bds_priority
-- define entire hi-res screen display borders. The screen is divided into
-- two main sections. There is the battlefield area where the targets, rockets,
-- and reticle are allowed to move, and there is the statistics area where our
-- current statistics will be displayed. The maximum number of digits allowed
-- in any statistics displayed is statistics_length. Between the statistics and
-- the battlefield there is a border.
-- define entire screen constants
entire_screen_left
                      : constant := 0;
entire screen right
                      : constant := 639;
entire_screen_top
                      : constant := 0;
entire_screen_bottom
                       : constant := 349;
-- define battlefield display borders and center.
battlefield screen left : constant := 222;
                                              -- starting (left)
battlefield_screen_right : constant := 638;
                                              -- ending (in pixels)
battlefield_screen_top : constant := 1;
                                              -- starting (top)
battlefield_screen_bottom : constant := 338;
                                             -- ending (in pixels)
battlefield_center_x : constant := 430;
battlefield center y
                      : constant := 169;
-- define border between battlefield and statistics.
border_left
                       : constant := 221; -- starting (left)
                      : constant := 639;
                                             -- ending (in pixels)
border_right
border_top
                        : constant := 0;
                                              -- starting (top)
                        : constant := 339;
border bottom
                                              -- ending (in pixels)
-- define statistics display borders.
status left
                      : constant := 0;
                                            -- starting (left)
status right
                      : constant := 220;
                                              -- ending (in pixels)
status top
                      : constant := 0;
                                             -- starting (top)
status_bottom
                      : constant := 349;
                                              -- ending (in pixels)
```

```
-- statistics_length is the number of digits allowed in any status field, and
-- stats_title_max_length is the max number of letters any particular
-- statistics title may contain.
statistics_length
                 : constant := 4;
stats_title_max_length : constant := 11;
number_of_titles
                       : constant := 12;
max_targets
                       : constant := 50; -- total targets
                                            -- total rockets
max_rockets
                       : constant := 20;
                   : constant := 0.100; -- basic interval is 100ms
interval
gravity
                       : constant := 9.80665; -- meters/sec**2
-- launch attitude
launch_azimuth
                       : constant := 16384; -- straight ahead in BAMS
                       : constant := 15000; -- 7.6 degrees off straight up
launch_elevation
launch_x
                      : constant := 2000.0;
launch_y
                     : constant := 60.0;
launch_z
                       : constant := 10.0;
kill_radius
              : constant := 10.0; -- 10 meters x 10 meters
end Config;
```

```
--% UNIT:
             Control task subunit.
--% Effects: Provides overall control for rocket flight and display.
--% Modifies: Updates rocket data base in Rocket body.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | TASK BODY : Rocket.Control
--| The Rocket.Control task controls the information coming in from the rocket
-- | support task and the target support task. With this information it
--| develops a list for the guidance task to work on (the guidance task being
-- in charge of developing new aimpoints for each rocket), updates the
--| statistics, launches a new rocket if necessary, sends the new positions of
--| the rockets to graphics for displaying, receives from guidance the new
--| aimpoints, and delivers those to the Rocket Support task in the simulator.
--| The purpose of the disengaged pointers, the engage flag, and the rocket
--| launch flag is to support the specification that only one target can be
--| marked destroyed each interval and that only one rocket can be launched
-- | per interval. Also going along with this is that targets can only be
-- | created one per interval. This helps to maintain a better average
-- | response time, thus predictibility of the amount of time this routine will
--| take is enhanced. If a graph was drawn of CPU utilization versus time, and
--| the targets and rockets were all allowed to be created and destroyed in
--| one interval as necessary, then several destroyed rockets and consequently
--| several created targets (the next interval) would appear on the graph as
--| spikes. It is necessary to eliminate "spikes" from the BDS because it is
-- a deadline driven mission. For this reason, the Simulate.RDL.Rock_Sup task
--| and the Simulate.Sensor.Targ_Sup task have timing loops surrounding their
-- | executable code. This technique allows for better fault tolerance; if one
-- of the buffer tasks or even one of the four main tasks mentioned above were
-- to be disabled because of an error, the rest of the system would still be
-- able to function properly.
--| The rendezvous mechanism with the guidance task is done as if there were
--| an array of guidance tasks. Although there is only one guidance task at
--| present, if more were added and they were on separate processors, this
-- | design would facilitate the distribution of those tasks.
--|
-- Modifications Log
-- 88-11-10 : TEG => Original Created.
-- 89-11-22 : MPS => History information moved from rocket package body
                   to rocket.control task body.
with Interrupt_Control;
```

with Grid to Pixel;

```
with Simulate;
with Target;
with Sync;
with Calendar;
with Engage;
with Time_Stamp;
 pragma ELABORATE(Interrupt_Control, Grid_to_Pixel, Sync,
                  Simulate, Target, Calendar, Engage, Time_Stamp);
separate(Rocket)
task body Control_Type is
                        -- for operators
 use Calendar;
                        -- for operators
 use Types;
                         -- for operators
 use Sync;
 package RDL renames Simulate.RDL; -- make simulator transparent
 dis list size
                  : constant := Config.max_rockets;
  type HISTORY_REC_TYPE is record
   ROCKET_OLD
                 : Types.POSITION_TYPE;
   TARGET OLD
                   : Types.POSITION TYPE;
   TARGET_AIMED_AT : Types.WORD_INDEX;
  end record;
  type HISTORY_LIST_TYPE is array(Types.ROCKET_INDEX_TYPE) of HISTORY_REC_TYPE;
                  : HISTORY_LIST_TYPE; -- :olds old rocket/target positions
 POS HISTORY
 MOVE_NUMBER
                  : Types.WORD_INDEX;
                                            -- to update display
 NEXT_ROCKET_MSG : ROCKET_MSG_TYPE;
                                            -- local copy of input msg
 NEXT_TARGET_LIST : Target.TARGET_DATA_LIST_TYPE; -- local copy of input data
 GUIDE_MSG
                : ROCKET_GUIDE_MSG_TYPE; -- local copy of output msg
 AIMPOINT_LIST : AIMPOINT_LIST_TYPE(Types.ROCKET_INDEX_TYPE);
                                              -- local copy
 MOVE_ROCKETS
                  : Graphics.MOVE_LIST_TYPE(Types.ROCKET_INDEX_TYPE);
 MOVE_INDEX
                 : Types.WORD_INDEX;
 PIXEL POINT
                  : Shapes.PIXEL;
                  : Types.WORD_INDEX;
 MSG_INDEX
                                            -- used to index incoming report
 OLD_SEQ_TAG : Sync.SEQ_TYPE;
                                            -- to filter stale reports out
                                             -- used to update OLD_SEQ_TAG
 ANY ACTIVE ROCKETS : BOOLEAN;
 ACTIVE_ROCKETS_ID : Types.ROCKET_INDEX_TYPE; -- holds an active rockets ID
                  : Target.TARGET_ID_TYPE;
 NEXT_ENGAGED
 NEXT DISENGAGED : Target.TARGET_ID_TYPE; -- keep track of all disengagements
 DISENGAGED_LIST : array(Types.ROCKET_INDEX_TYPE) of Target.TARGET_ID_TYPE;
```

```
DISENGAGED_ON_PTR : Types.WORD_INDEX;
 DISENGAGED OFF PTR : Types.WORD_INDEX;
 DISENCAGED ACK PTR : Types.WORD_INDEX;
                                              -- possible rocket to launch
 AVAILABLE_ROCKET : Types.WORD_INDEX;
 LAUNCH_PENDING : BOOLEAN := FALSE;
 LAUNCH_TARGET : Target.TARGET_ID_TYPE;
 LAUNCH_ROCKET : Types.ROCKET_INDEX_TYPE;
 ROCKET_DESTROYED : BOOLEAN;
 ROCKET LAUNCHED : BOOLEAN;
begin
 accept Start;
 for I in AIMPOINT_INFO'range loop
                                       -- initialize track data
   AIMPOINT_INFO(I).ACTIVE := FALSE;
   DISENGAGED_LIST(I) := 0;
  end loop;
 NEXT ENGAGED := 0;
                                      -- initialize disengage circle queue
 DISENGAGED ON PTR := 1;
 DISENGAGED OFF PTR := 1;
 DISENGAGED_ACK_PTR := 1;
 OLD_SEQ_TAG := 0;
                                        -- Main processing loop
  goo J
                                          -- exception block
   begin
                              --$TP(0002) Control task start time
      Time_Stamp.Log(0002);
      ROCKET DESTROYED := FALSE;
      ROCKET_LAUNCHED := FALSE;
      ANY_ACTIVE_ROCKETS := FALSE;
-- Rendezvous with buffer task to get next rocket message from sensor
      Time Stamp.Log(0003); -- $TP(0003) Control rendezvous with Report_Buf start
      RDL.Report_Buf.Get_Report(NEXT_ROCKET_MSG);
     Time Stamp.Log(0004); --$TP(0004) Control rendezvous with Report_Buf end
-- If there are more on circular disengage queue, send another to tracker
      if DISENGAGED OFF_PTR /= DISENGAGED_ON_PTR and then
        not NEXT TARGET LIST(DISENGAGED_LIST(DISENGAGED_OFF_PTR)).STATUS.ENGAGED
      then
        DISENGAGED OFF PTR := DISENGAGED_OFF PTR rem dis_list_size + 1;
      if DISENGAGED OFF PTR = DISENGAGED_ON_PTR then
        NEXT_DISENGAGED := 0;
        NEXT DISENGAGED := DISENGAGED_LIST(DISENGAGED_OFF_PTR);
```

```
end if;
-- Rendezvous to Get target list from target tracker, and provide it
-- with information on which targets have been engaged and disengaged.
     Time_Stamp.Log(0005); -- $TP(0005) Control rendezvous with Track_Dat start
     Target.Track_Data.Get(NEXT_TARGET_LIST, NEXT_ENGAGED, NEXT_DISENGAGED);
     Time_Stamp.Log(0006); --$TP(0006) Control rendezvous with Track_Dat end
-- Check if Track task has recognized the engage request, if so then
-- it is safe to clear it, and possibly engage another.
      if NEXT_ENGAGED /= 0 and then
         NEXT_TARGET_LIST(NEXT_ENGAGED).STATUS.ENGAGED
       NEXT_ENGAGED := 0;
     end if;
   Check to see if last disengage request was acknowledged
      if DISENGAGED_ACK_PTR /= DISENGAGED_OFF_PTR and then
        not NEXT_TARGET_LIST(DISENGAGED_LIST(DISENGAGED_ACK_PTR)).STATUS.ENGAGED
      then
       DISENGAGED_ACK_PTR := DISENGAGED_ACK_PTR rem dis_list_size + 1;
     end if;
-- determine which rockets have been expended, and delete them from screen
-- (previously active, but no longer in report list)
     MOVE_INDEX := 0;
     MSG_INDEX := 1;
      for ROCKET_ID in Types.ROCKET_INDEX_TYPE loop
        if AIMPOINT_INFO(ROCKET_ID).ACTIVE then
          if NEXT_ROCKET_MSG.ROCKET_LIST(MSG_INDEX).SYNC_TAG = OLD_SEQ_TAG then
            ANY_ACTIVE_ROCKETS := TRUE; -- need an active rockets time tag
            ACTIVE ROCKETS ID := ROCKET ID;
            exit;
                                                 -- old rocket report
          end if;
   look at most recent rocket report message to make sure rocket is still alive
          if MSG_INDEX <= NEXT_ROCKET_MSG.NUM_ROCKETS and then
             ROCKET_ID = NEXT_ROCKET_MSG.ROCKET_LIST(MSG_INDEX).ROCKET_ID
          then
            POS_HISTORY(ROCKET_ID).ROCKET_OLD :=
                            AIMPOINT_INFO(ROCKET_ID).ROCKET_POS;
            AIMPOINT INFO(ROCKET ID).ROCKET POS :=
```

```
NEXT_ROCKET_MSG.ROCKET_LIST(MSG_INDEX).POSITION;
         POS_HISTORY(ROCKET_ID).TARGET_OLD :=
                          AIMPOINT_INFO(ROCKET_ID).TARGET_POS;
         AIMPOINT_INFO(ROCKET_ID).TARGET_POS :=
       NEXT_TARGET_LIST(POS_HISTORY(ROCKET_ID).TARGET_AIMED_AT).POSITION_NEW;
         MOVE_INDEX := MOVE_INDEX + 1;
        MOVE_ROCKETS(MOVE_INDEX) :=
               (XY_OLD => Grid to Pixel(POS_HISTORY(ROCKET ID).ROCKET OLD),
               XY_NEW => Grid_to_Pixel(AIMPOINT_INFO(ROCKET_ID).ROCKET_POS),
               OBJECT => Shapes.ROCKET,
               COLOR => Graphics.ROCKET_COLOR);
        MSG_INDEX := MSG_INDEX + 1;
      else
the rocket has deceased, put it in the list for erasure.
         PIXEL_POINT := Grid_to_Pixel(
                                           -- get last point in pixel value
                     AIMPOINT_INFO(ROCKET_ID).ROCKET_POS);
         AIMPOINT_INFO(ROCKET_ID).ACTIVE := FALSE; -- mark as inactive
         MOVE_INDEX := MOVE_INDEX + 1;
         MOVE_ROCKETS(MOVE_INDEX) :=
           (PIXEL_POINT,
           PIXEL_POINT,
           Shapes.ROCKET,
            Graphics.background color);
         AVAILABLE_ROCKET := ROCKET_ID;
                                              -- save if decide to launch
         DISENGAGED_LIST(DISENGAGED_ON_PTR):=
                       POS_HISTORY(ROCKET_ID).TARGET_AIMED_AT;
         DISENGAGED_ON_PTR := DISENGAGED_ON_PTR rem dis list size + 1;
         Interrupt_Control.Disable;
         Status.STATUS CONTROL(Status.AIRBORNE).DATA :=
            Status.STATUS_CONTROL(Status.AIRBORNE).DATA - 1;
         Status.STATUS_CONTROL(Status.EXPENDED).DATA :=
            Status.STATUS_CONTROL(Status.EXPENDED).DATA + 1;
         Interrupt_Control.Enable;
         ROCKET DESTROYED := TRUE;
      end if; -- found
  rocket slot previously inactive, see if rocket has launched
       if MSG INDEX <= NEXT ROCKET MSG.NUM ROCKETS and then
         NEXT_ROCKET_MSG.ROCKET_LIST(MSG_INDEX).ROCKET_ID =
                                                     ROCKET ID
      then
  ROCKET HAS BEEN LAUNCHED, UPDATE DATA BASES
         AIMPOINT_INFO(ROCKET_ID) :=
            ( TRUE,
                                           -- ACTIVE
             NEXT_ROCKET_MSG.ROCKET_LIST(MSG_INDEX).POSITION, -- NEW
```

```
NEXT TARGET LIST(LAUNCH TARGET).POSITION_NEW); -- NEW
           POS HISTORY(ROCKET ID) :=
                  (NEXT_ROCKET_MSG.ROCKET_LIST(MSG_INDEX).POSITION, -- OLD
                  NEXT_TARGET_LIST(LAUNCH_TARGET).POSITION_NEW,
                                             -- TARGET AIMED AT
                  LAUNCH_TARGET);
           LAUNCH_PENDING := FALSE;
                                                   -- all accounted for
           MSG_INDEX := MSG_INDEX + 1;
           Interrupt_Control.Disable;
           Status.STATUS CONTROL(Status.AIRBORNE).DATA :=
              Status.STATUS_CONTROL(Status.AIRBORNE).DATA + 1;
           Interrupt_Control.Enable;
           ROCKET_LAUNCHED := TRUE;
         else
           AVAILABLE ROCKET := ROCKET ID;
         end if; -- new rocket test
       end if: -- active test
     end loop;
                -- rocket-id loop (scan of all rockets)
-- Update Time tag for next message.
     if ANY_ACTIVE_ROCKETS then
       OLD_SEQ_TAG := NEXT_ROCKET_MSG.ROCKET_LIST(ACTIVE_ROCKETS_ID).SYNC_TAG;
     end if:
                            -- if no active rockets, don't change OLD_SEQ_TAG.
-- Get guidance task(s) working on finding new aimpoint for guidance msg
     for I in Types.WORD_INDEX range 1..Distrib.num_guide_tasks loop
       Time_Stamp.Log(0007);
                                 -- $TP(0007) Control rendezvous with Guidance(1) start
       Rocket Guide(I).History(
          AIMPOINT_INFO(Distrib.guide_low(I)..Distrib.guide_high(I)));
       Time_Stamp.Log(0008); --$TP(0008) Control rendezvous with Guidance(1) end
     end loop;
-- update status information
     Interrupt_Control.Disable;
     if ROCKET LAUNCHED then
       Status.STATUS_CONTROL(Status.AIRBORNE).DISPLAYED := FALSE;
     end if:
     if ROCKET_LAUNCHED or ROCKET_DESTROYED then
       Status.STATUS_CONTROL(Status.AIRBORNE).DISPLAYED := FALSE;
       Status.STATUS CONTROL(Status.EXPENDED).DISPLAYED := FALSE;
       Status.REQ_COUNT := Status.REQ_COUNT + 1;
       if Status.REQ COUNT = 1 then
         Time_Stamp.Log(0009);
                                   -- $TP(0009) Control rendezvous with Status start
         Status.Update.Signal;
         Time_Stamp.Log(0010);
                                  --$TP(0010) Control rendezvous with Status end
       end if;
     end if;
     Interrupt Control.Enable;
```

```
MSG INDEX := 0;
                                  -- zero index for creating guidance message
-- Now, check if we should try to create a new ROCKET. Note that
-- if a rocket has just been destroyed, don't try to fire a new one
-- before the rocket tracker knows that it has been disengaged. Otherwise
-- it is likely to choose a target other than one that is closest.
      if not LAUNCH PENDING and
        DISENGAGED_ACK_PTR    DISENGAGED_ON_PTR and -- all have been ack'ed
        NEXT_ENGAGED = 0
                                                     -- engage has been ack'ed
     then
       NEXT_ENGAGED := Engage(NEXT_TARGET_LIST);
       if NEXT_ENGAGED > 0 then
         LAUNCH ROCKET := AVAILABLE ROCKET;
         LAUNCH_TARGET := NEXT_ENGAGED;
         LAUNCH_PENDING := TRUE;
       end if; -- ready to launch
     ena if; -- not pending check
-- get graphics task working on displaying rockets
     Time_Stamp.Log(0011);
                               -- $TP(0011) Control rendezvous with Graphics start
     Graphics.Display.Move(Graphics.LOW, MOVE_ROCKETS(1..MOVE_INDEX));
     Time_Stamp.Log(0012);
                               --$TP(0012) Control rendezvous with Graphics end
-- now get results of guidance information
     for I in Types.WORD_INDEX range 1..Distrib.num_yuide_tasks loop
       Time_Stamp.Log(0013);
                                  --$TP(0013) Control rendezvous with Guidance(2) start
       Rocket_Guide(1).Next_Guidance(
         AIMPOINT_LIST(Distrib.guide_low(I)..Distrib.guide_high(I)) );
       Time_Stamp.Log(0014); --$TP(0014) Control rendezvous with Guidance(2) end
     end loop;
   Now generate new guidance message and send to Guide Buf
      for ROCKET_ID in AIMPOINT_INFO'range loop
       if AIMPOINT_INFO(ROCKET_ID).ACTIVE then
          MSG_INDEX := MSG_INDEX + 1;
          GUIDE_MSG.ROCKET_GUIDE_LIST(MSG_INDEX) :=
                                       (ROCKET_ID, AIMPOINT_LIST(ROCKET_ID));
        elsif LAUNCH PENDING and then
              ROCKET_ID = LAUNCH_ROCKET then
          MSG_INDEX := MSG_INDEX + 1;
-- initiate launch
          GUIDE_MSG.ROCKET_GUIDE_LIST(MSG_INDEX) := (ROCKET_ID,
                                  (Config.launch azimuth,
                                   Config.launch_elevation));
```

```
end if;
end loop;
GUIDE_MSG.NUM_ROCKETS := MSG_INDEX;
Time_Stamp.Log(0015); --$TP(0015) Control rendezvous with Guide_Buf start
RDL.Guide_Buf.Put_Guide(GUIDE_MSG); -- send new guidance message
Time_Stamp.Log(0016); --$TP(0016) Control rendezvous with Guide_Buf end

exception
when others =>
Debug_IO.Put_Line("Exception in Control task");
end; -- exception block
end loop; -- main processing loop
end Control_Type; -- Rocket.Control task body
```

```
--% UNIT: Debug IO Spec.
--% Effects: Provides non-intrusive trace output to secondary port.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
.....
-- | PACKAGE SPEC : Debug_IO
--| This package is used to provide visibility to the character (and string)
--| input and output procedures. Currently, because the screen memory is
--| written directly to, all text input and output is done via a serial port.
--| These routines are needed to signal to the user (via the serial port) that
--| an exception has occurred.
--|
-- Modifications Log
-- 88-09-01 : TEG => Original created.
package Debug IO is
 procedure Put(CHAR : CHARACTER);
 procedure Get(CHAR : out CHARACTER);
 procedure Put(STR : STRING);
 procedure Get(STR : out STRING);
 procedure Put Line(STR : STRING);
 procedure Get_Line(STR : out STRING; LENGTH : out INTEGER);
 procedure Skip_Line;
end Debug_10;
```

```
--% UNIT: Debug IO body.
--% Effects: Provides non-intrusive trace output to secondary port.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
.......
-- | PACKAGE BODY : Debug_IO
--| The Debug_IO package is used to provide a means of communication from
--| the BDS to the user. Since the terminal (the EGA screen in this case) is
--| being written to directly, output cannot take place there, and therefore
--| Text_IO cannot be used. See the hardware configuration file for more
-- details on the input and output modes.
---
-- Modifications Log
-- 88-09-01 : TEG => Original created.
with Terminal Driver;
 pragma ELABORATE(Terminal_Driver);
package body Debug_IO is
pragma SUPPRESS(storage_check);
 procedure Put(CHAR : CHARACTER) is
   Terminal_Driver.Put_Character(CHAR);
  end Put; -- character
  procedure Get(CHAR : out CHARACTER) is
  begin
   Terminal_Driver.Get_Character(CHAR);
  end Get; -- character
  procedure Put(STR : STRING) is
  begin
   for I in STR'range loop
     Terminal_Driver.Put_Character(STR(I));
   end loop;
  end Put; -- String
  procedure Get(STR : out STRING) is
  begin
   for I in STR'range loop
```

```
Terminal_Driver.Get_Character(STR(1));
  end loop;
end Get; -- String
procedure Put_Line(STR : STRING) is
begin
  for I in STR'range loop
   Terminal_Driver.Put_Character(STR(I));
 end loop;
 Terminal_Driver.Put_Character(ASCII.CR);
  Terminal_Driver.Put_Character(ASCII.LF);
end Put_Line;
procedure Get_Line(STR : out STRING; LENGTH : out INTEGER) is
  CHAR : CHARACTER := ASCII.NUL;
  LEN : INTEGER := STR'first;
begin
  while CHAR /= ASCII.CR and LEN <= STR'last loop
    Terminal_Driver.Get_Character(CHAR);
    STR(LEN) := CHAR;
   LEN := LEN + 1;
  end loop;
end Get_Line;
procedure Skip_Line is
  CHAR : CHARACTER := ASCII.NUL;
begin
  while CHAR /= ASCII.CR loop
    Terminal_Driver.Get_Character(CHAR);
  end loop;
end Skip_Line;
```

end Debug_IO;

```
--% UNIT: Distrib Package Spec.
--% Effects: Provides parameters to control task arrays and work lists.--
--% Modifies: No global data is modified other than in this spec.
--% Requires: Depends on presence of Distrbuted Runtime for # of tasks. --
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
_____
-- | PACKAGE SPEC : Distrib
-- | OPERATION :
--| This package controls the parameters for automatically performing a
--| division of the guidance workload. In this case, a large array can be
-- | broken down so that two or more tasks can perform their operations on the
--| array at the same time (if true multi-processing is in effect).
--|
-- Modifications Log
-- 88-12-05 : TEG => Original Created.
-- 89-12-06 : TEG => Enhanced to support dynamic configuration/reconfiguration
with Types;
......
-- DISTRIBUTION CONTROL PARAMETERS
.....
package Distrib is
-- Configuration Setting for number of Rockets and Targets
-- These are set during package body elaboration.
NUM_TARGETS : Types.WORD_INDEX;
NUM_ROCKETS : Types.WORD_INDEX;
-- Max num guide tasks is used to determine the maximum number of guide
-- tasks which could be created. It is used simply to define the size of
-- the index arrays.
 Max_guide_tasks : constant := 2;
-- NUM_GUIDE_TASKS contains the ACTUAL number of guide tasks in the current
-- configuration. It is initialized by a call to the distributed runtime
-- during package elaboration.
 NUM_GUIDE_TASKS : Types.WORD_INDEX;
```

```
-- MASTER is TRUE iff this processor has been configured as the master
   processor.
 MASTER
                      : BOOLEAN;
-- The following two "index" arrays are used by the Congrol task to
-- divide work among the possible guidance tasks. These values are
-- also intialized according to the configuration control tables in
-- in the Distrib package body during elaboration.
 GUIDE_LOW
                  : array(Types.WORD_INDEX range 1..Max_guide_tasks)
                    of Types.WORD_INDEX;
 GUIDE_HIGH
                  : array(Types.WORD_INDEX range 1..Max_guide_tasks)
                    of Types.WORD_INDEX;
-- RESTART is used to stop operation of the BDS and allow the operator
   setup a different configuration. It is only called when the MODE
-- button is pressed while the RESET button is held down on the mouse.
 procedure Restart; -- DOES NOT RETURN TO CALLER!
 pragma INTERFACE(ASM86, Restart);
 pragma INTERFACE_SPELLING(Restart, "D1DRTE?RESTART");
end Distrib;
```

```
--% UNIT: Engage Procedure Spec.

--% Effects: Determines if Rocket is to be launched, and at what target.

--% Modifies: No global data is modified.

--% Requires: Status package must set mode and airborne counts.

--% Raises: No explicitly raised exceptions are propagated.

--% Engineer: M. Sperry.

--|

--|

--| SUBPROGRAM SPEC : Engage

--|

--| This function determines which target will be selected when it is

--| determined that a rocket needs a target to aim at.

--|

--- Modifications Log

---

--- 88-11-10 : MPS => Original Created.

---

with Target;

function Engage(TARGET_INFO : in Target.TARGET_DATA_LIST_TYPE) return

Target.TARGET_ID_TYPE;
```

```
--% UNIT: Engage Procedure Body.
--% Effects: Determines if Rocket is to be launched, and at what target.-
--% Modifies: No global data is modified.
--% Requires: Status package must set mode and airborne counts.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: M. Sperry.
-- | SUBROGRAM BODY : Engage
--1
--| The Engage procedures performs two functions based on the MODE. The
-- | MODE is either MANUAL or AUTOMATIC. In MANUAL mode the engage procedure
--| first determines if a rocket can be launched and not exceed the maximum
-- allowable rockets. It then reads the shared variables of the reticle's
--| position and the LAUNCH button on the mouse and determines if the reticle
-- is in proximity to a target. If so, that target is chosen unless there is
--| one closer. In AUTOMATIC mode, if there are not too many active rockets,
--| then the target closest to the bottom of the screen is chosen. This routine
-- is called during every rocket control task iteration. The returned
--| parameter TARGET is zero if no target should be engaged, otherwise it
-- | indicates the selected targets id.
---
-- Modifications Log
-- 88-11-20 : MPS => Original Created.
with Interrupt_Control;
with Status;
with Mouse_Buffer;
with Types;
with Config;
with Shapes;
with Time_Stamp;
with Distrib;
 pragma ELABORATE(Interrupt_Control, Status, Mouse_Buffer, Distrib);
function Engage(TARGET_INFO : in Target.TARGET_DATA_LIST_TYPE) return
               Target.TARGET_ID_TYPE is
use Types;
             -- for operators
use Status;
             -- for operators
                                        -- reticle in PIXEL coordinates
RETICLE_X_PIXEL : Types.WORD;
RETICLE_Y_PIXEL : Types.WORD;
                                        -- reticle in PIXEL coordinates
                                         -- reticle in GRID coordinates
RETICLE_X_GRID : Types.METERS;
RETICLE_Y_GRID : Types.METERS;
                                          -- reticle in GRID coordinates
```

PREV DISTANCE : Types.METERS;

```
DISTANCE X
                : Types.METERS;
DISTANCE_Y
                : Types.METERS := Config.meters_in_battle_area;
TOTAL_DISTANCE : Types.METERS;
TARGET ID
               : Target.TARGET_ID_TYPE;
begin
  Time_Stamp.Log(0018);
                           --$TP(0018) Engage start
  TARGET_ID := 0;
                                                -- default
  if Status.STATUS_CONTROL(Status.AIRBORNE).DATA <
     Types.WORD(Distrib.NUM_ROCKETS)
  then
    if Status.MODE = Status.MANUAL then
      if Mouse_Buffer.LAUNCH then
        -- read ABS_X and ABS_Y in Mouse_Buffer, then convert to METERS types.
        -- Then, find closest target in list to reticle, and give it back.
        Interrupt Control.Disable;
                                             -- go atomic while reading
        RETICLE_X_PIXEL := Mouse_Buffer.NEW_ABS_X;
        RETICLE Y PIXEL := Mouse Buffer.NEW_ABS_Y;
        Mouse_Buffer.LAUNCH := FALSE;
        Interrupt_Control.Enable;
        RETICLE_X_GRID :=
        Types.METERS(Types.METERS(RETICLE X PIXEL -
                     Config.battlefield_screen_left) *
                     Types.METERS(Config.meters_per_X_pixet));
        RETICLE_Y_GRID :=
        Types.METERS(Types.METERS(Config.battlefield_screen_bottom -
                     RETICLE_Y_PIXEL) *
                     Types.METERS(Config.meters per Y pixel)):
        -- This loop locates the closest target to the reticle center
        for ID in Types.TARGET_INDEX_TYPE loop
          if TARGET_INFO(ID).STATUS.ACTIVE and then
             not TARGET_INFO(ID).STATUS.ENGAGED then
            DISTANCE_X := abs(RETICLE_X_GRID - Types.METERS(
                          TARGET INFO(ID).POSITION NEW.X));
            DISTANCE_Y := abs(RETICLE_Y_GRID - Types.METERS(
                          TARGET_INFO(ID).POSITION_NEW.Y));
            if DISTANCE_X <= Shapes.reticle_X_error and
               DISTANCE_Y <= Shapes.reticle_Y_error</pre>
            then
              TOTAL_DISTANCE := Types.METERS(DISTANCE_X * DISTANCE_X) +
                                Types.METERS(DISTANCE_Y * DISTANCE_Y);
              if TARGET_ID = 0 or else TOTAL_DISTANCE < PREV_DISTANCE then
                PREV DISTANCE := TOTAL DISTANCE;
                TARGET_ID := ID;
              end if;
                                             -- distance/target check
            end if;
                                             -- x and y reticle distance check
          end if:
                                             -- active/not engaged check
        end loop;
      end if;
                                        -- launch check
    else
                                -- automatic mode, search for closest Y value
      for ID in Types.TARGET_INDEX_TYPE loop
```

```
if TARGET_INFO(ID).STATUS.ACTIVE and then
          (not TARGET_INFO(ID).STATUS.ENGAGED and
          Types.METERS(TARGET_INFO(ID).POSITION_NEW.Y) <= DISTANCE_Y)</pre>
       then
         DISTANCE_Y := Types.METERS(TARGET_INFO(ID).POSITION_NEW.Y);
         TARGET_ID := ID;
                             -- active/not engaged/closest y check
       end if;
     end loop;
   end if;
                            -- mode check
 end if;
                            -- number of rockets check
 Time_Stamp.Log(0019);
                           --$TP(0019) Engage end
 return TARGET_ID;
end Engage;
```

```
--% UNIT:
            Graphics Package Spec.
--% Effects: Performs all updates to graphics display.
--% Modifies: No global data is modified.
--% Requires: Screen must be put in graphics mode by runtime initialize.--
--% Raises: QUEUE_ERROR is raised if no room for move list.
--% Engineer: T. Griest / M. Sperry.
______
---
-- PACKAGE SPEC : Graphics
--1
--| The Graphics package provides the interface for all screen display
--| operations. All activity is performed by the Display task which insures
--| that the display is updated in a consistent and timely fashion. The shapes
--| that the graphics displays are all defined in the Shapes package. The
-- | MOVE_RECORD is defined as all the elements needed in order to perform a
--| draw or erase of an image. A MOVE_LIST is an array of MOVE_RECORDs and
--| it is used as a parameter when one of the routines responsible for moving
-- an image across the screen needs to rendezvous with Graphics.Display. An
--| entire list is enqueued onto one of the priority queues and each element
-- is dequeued separately in order to continuously check for more arrivals.
-- | The high priority queue is currently reserved for the reticle motion.
--|
-- Modifications Log
-- 88-08-25 : TEG => Original created.
with Types;
with Config;
with Shapes;
package Graphics is
stack_size : constant := 8192; -- in bytes
-- define screen and graphics constants
subtype COLOR_TYPE is Types.WORD; -- range 0..63; -- 64 colors on EGA
background_color : constant COLOR_TYPE := 0;
                                              -- black
reticle_color : constant COLOR_TYPE := 14; -- bright yellow
border_color : constant COLOR_TYPE := 9;
                                              -- bright blue
status_color : constant COLOR_TYPE := 15;
                                              -- bright white
status_box_color : constant COLOR_TYPE := 9;
                                              -- bright blue
rocket_color : constant COLOR_TYPE := 12;
                                              -- bright red
target_color : constant array(Types.TARGET_CLASS_TYPE, BOOLEAN) of
```

```
COLOR_TYPE := ((6, 14), (3, 11), (2, 10), (5, 13));
        -- different color for engage = false/true and target type
no_process
               : constant COLOR_TYPE := 16; -- don't process object color
-- define graphics data structures
type MOVE_RECORD is record
 XY_OLD
                 : Shapes.PIXEL;
                                         -- previous position object held
  XY_NEW
                 : Shapes.PIXEL;
                                        -- new position
                  : Shapes.SYMBOL_TYPE; -- list of relative offsets
  OBJECT
  COLOR
                  : COLOR_TYPE;
                                         -- color for that object
end record;
type MOVE_LIST_TYPE is array (Types.WORD_INDEX range <>) of MOVE_RECORD;
type PRIORITY_TYPE is (HIGH, LOW);
QUEUE_ERROR
                                                  if queue over/underflow
                  : exception;
task type Display_Type is
  entry Print_Titles(X,Y : Types.WORD;
                     TITLE : STRING;
                     COLOR : COLOR_TYPE);
  entry Move(PRIORITY : PRIORITY_TYPE; WORK_LIST : MOVE_LIST_TYPE);
  pragma PRIORITY(Config.display_priority);
end Display Type;
for Display_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                                               stack_size);
                  : Display_Type;
Display
end Graphics;
```

```
--% UNIT:
             Graphics Package Body
--% Effects: Performs all updates to graphics display.
--% Modifies: No global data is modified.
--% Requires: A method of access to the EGA BIOS calls.
--% Raises: QUEUE ERROR is raised if no room for move list.
--% Engineer: T. Griest / M. Sperry.
-- PACKAGE BODY : Graphics
--|
     The purpose of the graphics package body is the implementation of the
-- | display task.
--1
-- | TASK BODY : Graphics.Display
     The display task is responsible for buffering the various tasks that want
--| to draw their particular symbol on the screen. The task begins by placing
--| the screen (via BIOS calls) into high resolution mode 10h. When this
-- is done, the screen will be in write mode 0 - the BIOS default. In this
--- mode it is possible to print characters easily by calling the appropriate
--| BIOS routine. After the statistics have been printed, a change to write
--) mode 2 is accomplished. This mode permits quick drawing of pixels in the
-- | color needed, and the battlefield border is drawn this way. The rest of
--| the graphics are also done in this mode. The display task then waits
-- ] for a work request to draw a symbol. When a request comes in, it is put
--- on a prioritized queue. The queue used is a function of the callers'
-- | priority. Now, since there is work to do, the task processes one symbol
-- at a time, checks to see if other tasks are waiting to queue any requests,
-- and continues processing until no requests are left in any of the queues.
-- When a request is processed, it's old position is erased, and it's new
-- | position is drawn. No attempt is made to synchronize with the vertical
-- | retrace since it would slow down the task too much. The penalty associated
-- | with this is a slight flicker of some of the images (especially when the
--| reticle is being slowly dragged across the screen). When checking if there
-- is more work to do, using 'count instead of a select statement was used
--| because the code generated for 'count was significantly smaller.
--1
-- Modifications Log
-- 88-08-25 : MPS => Original Created
with Machine Dependent;
with Interrupt_Control;
with Debug_10;
with Time_Stamp;
pragma ELABORATE(Machine_Dependent, Interrupt_Control, Debug_IO, Time_Stamp);
```

```
package body Graphics is
task body Display_Type is
                                       -- needed for visibility to "+" operator
use Types;
buffer_size
               : constant := 256;
initialize screen : constant := 0;
                                       -- for Int 10 BIOS call, fnctn 0
          : constant := 0;
                                      -- dummy parameter
dummy_1
               : constant := 0;
                                     -- dummy parameter
dummy 2
position_cursor : constant := 2; -- position function is int 10, fnct 2
                : constant := 14; -- write char is int 10, fnct 16#0E#
write
type CIRCULAR BUFFER is array(Types.WORD_INDEX range 0 .. buffer_size - 1) of
                                                           MOVE_RECORD;
type BUFFER_TYPE is record
 ON : Types.WORD_INDEX := 0;
 OFF : Types.WORD_INDEX := 0;
 DATA : CIRCULAR BUFFER;
end record;
SET_PRIORITY : PRIORITY_TYPE := PRIORITY_TYPE'FIRST;
            : array(PRIORITY_TYPE'FIRST..PRIORITY_TYPE'LAST) of BUFFER_TYPE; -- set up queues
BUFFER
NO WORK
            : BOOLEAN;
                                     -- all queues empty?
                                      -- for individual processing
WORK_REQUEST : MOVE_RECORD;
           : Shapes.OBJECT_PTR;
                                     -- current object to move
TEXT_MODE : BOOLEAN;
                                       -- printing stats titles?
          : Types.WORD;
                                      -- temp for holding string slices
                                       -- index into TITLE string
COUNTER
            : Types.WORD;
procedure Erase_Image(BASE : Shapes.PIXEL;
                     ITEM : Shapes.OBJECT_PTR) is
-- | SUBPROGRAM BODY : Graphics.Display.Erase_Image
--1
--| A procedure designed to calculate absolute coordinates for the routine
-- | Machine_Dependant.Put_Pixel given a shape(OBJECT_PTR) and an absolute
-- | reference point where the object is to be placed. No color is specified
-- | because the intent of this procedure is to erase, which is actually
--| drawing over the old image in the background color.
--1
begin
 Time_Stamp.Log(0020);
                           -- $TP(0020) Graphics.Erase_Image start
 for I in ITEM.all'range loop
   Machine_Dependent.Put_Pixel(BASE.X + ITEM.all(I).X_OFFSET,
                               BASE.Y + ITEM.all(I).Y_OFFSET,
                               background_color);
  end loop;
```

```
-- $TP(0021) Graphics. Erase_Image end
 Time_Stamp.Log(0021);
end Erase_Image;
pragma INLINE(ERASE_IMAGE);
procedure Draw_image(BASE : Shapes.PIXEL;
                     ITEM : Shapes.OBJECT_PTR;
                     COLOR : COLOR_TYPE) is
--1
--| SUBPROGRAM BODY : Graphics.Display.Draw_Image
--|
--|
    This procedure is functionally the same as Erase_Image except that a
--| color is passed to it so that the object can be drawn in that color.
begin
 Time_Stamp.Log(0022);
                            -- $TP(0022) Graphics.Draw_Image start
 for I in ITEM.all'range loop
   Machine_Dependent.Put_Pixel(BASE.X + ITEM.all(1).X_OFFSET,
                                BASE.Y + ITEM.all(I).Y_OFFSET,
                                COLOR);
  end loop;
  Time_Stamp.Log(0023);
                            -- $TP(0023) Graphics.Draw_Image end
end Draw_Image;
pragma INLINE(DRAW_IMAGE);
procedure Initialize_Border is
-- | SUBPROGRAM BODY : Graphics.Display.Initialize_Border
    A procedure which utilizes the Shapes package to place a color border
--1
-- | around the screen thus defining the battlefield area. The reticle never
-- | leaves the battlefield area and statistics are never displayed inside
-- | the battlefield area.
---
BORDER : MOVE_RECORD;
begin
 BORDER.OBJECT := Shapes.DOT;
 OBJECT := Shapes.OBJECT PTR TABLE(BORDER.OBJECT);
 BORDER.COLOR := border_color;
-- draw top and bottom border
  for I in Config.border_left..Config.border_right loop
    BORDER.XY_NEW := (Types.COORDINATE(1),Config.border_top);
    Draw_image(BORDER.XY_NEW,OBJECT,BORDER.COLOR);
```

```
BORDER.XY_NEW := (Types.COORDINATE(I),Config.border_bottom);
   Draw_Image(BORDER.XY_NEW,OBJECT,BORDER.COLOR);
 end loop;
-- draw left side and right side border
 for J in Config.border_top..Config.border_bottom loop
   BORDEP.XY_NEW := (Config.border_left,Types.COORDINATE(J));
   Draw Image(BORDER.XY NEW, OBJECT, BORDER.COLOR);
   BORDER.XY_NEW := (Config.border_right, Types.COORDINATE(J));
   Draw_Image(BORDER.XY_NEW,OBJECT,BORDER.COLOR);
 end loop;
except ion
 when others => Debug_IO.Put_Line("Exception raised in Graphics.Initialize");
end Initialize Border;
procedure Enqueue(PRIORITY : PRIORITY_TYPE; MOVE_REQUEST : MOVE_RECORD) is
--1
--| SUBPROGRAM BODY : Graphics.Display.Enqueue
--| A procedure which enqueues a MOVE RECORD (a record containing all the
--| information needed to draw a symbol) onto the proper priority queue for
-- | later processing. May raise QUEUE_ERROR.
--1
ON_NEW
       : Types.WORD_INDEX;
begin
 Time_Stamp.Log(0024);
                           --$TP(0024) Graphics.Enqueue start
 ON NEW := (BUFFER(PRIORITY).ON + 1) rem buffer size;
 if ON_NEW = BUFFER(PRIORITY).OFF then
   raise QUEUE_ERROR;
 end if;
 Interrupt_Control.Disable;
                                                 -- compiler bug
 BUFFER(PRIORITY).DATA(ON NEW) := MOVE REQUEST;
 Interrupt Control Enable;
 BUFFER(PRIORITY).ON := ON NEW;
 Time_Stamp.Log(0025);
                           --$TP(0025) Graphics.Enqueue end
end Enqueue;
pragma INLINE(Enqueue);
procedure Dequeue(PRIORITY : PRIORITY TYPE; MOVE REQUEST : out MOVE_RECORD) is
-- | SUBPROGRAM BODY : Graphics.Display.Dequeue
--|
-- A procedure which is given the priority of the queue it needs to access
-- in order to pop the MOVE_RECORD (a record containing drawing information)
```

```
--| off that queue. If there are no items on that queue, QUEUE_ERROR is raised.
--1
OFF_NEW : Types.WORD_INDEX;
begin
 Time Stamp.Log(0026);
                         --$TP(0026) Graphics.Dequeue start
 if BUFFER(PRIORITY).OFF = BUFFER(PRIORITY).ON then
   raise QUEUE_ERROR;
 end if;
 OFF_NEW := (BUFFER(PRIORITY).OFF + 1) rem buffer_size;
  Interrupt_Control.Disable;
                                               -- compiler bug
 MOVE_REQUEST := BUFFER(PRIORITY).DATA(OFF_NEW);
 Interrupt_Control.Enable;
 BUFFER(PRIORITY).OFF := OFF_NEW;
  Time_Stamp.Log(0027); --$TP(0027) Graphics.Dequeue end
end Dequeue;
pragma INLINE(Dequeue);
-- Body of DISPLAY TASK
...........
begin
 NO_WORK := TRUE;
 TEXT_MODE := TRUE;
 Machine_Dependent.Int10(initialize_screen,
                         dummy_1,
                                              -- dummy variables are unused
                                             -- hi-res graphics mode
                         dummy_2);
  Machine Dependent.Write Mode 0;
  while TEXT_MODE loop
    accept Print_Titles(X,Y : Types.WORD;
                       TITLE : STRING;
                       COLOR : COLOR_TYPE) do
     if TITLE'length > 0 then
       Machine_Dependent.Int10(position_cursor,X,Y);
       COUNTER := 1;
       while COUNTER <= TITLE'length loop
         CHAR := Types.WORD(CHARACTER'pos(TITLE(INTEGER(COUNTER))));
         Machine_Dependent.Int10(write,
                                CHAR,
                                COLOR);
         COUNTER := COUNTER + 1;
       end loop;
     else
       TEXT_MODE := FALSE;
     end if;
    end Print_Titles;
  Machine_Dependent.Write_Mode_2;
                                              -- go to write mode 2
  Initialize_Border;
                                               -- draw battlefield border
```

```
loop
   begin
                                                 -- exception block
     Time_Stamp.Log(0028); --$TP(0028) Graphics task start
     if NO_WORK or Move'COUNT > 0 then
       Time Stamp.Log(0112);
                                --$TP(0112) Graphics accept Move start
       accept Move(PRIORITY : PRIORITY_TYPE; WORK_LIST : MOVE_LIST_TYPE) do
         for I in WORK LIST'range loop
           Enqueue(PRIORITY, WORK_LIST(I));
         end loop;
       end Move;
       Time_Stamp.Log(0113); --$TP(0113) Graphics accept Move end
       NO_WORK := FALSE;
     end if;
-- Now there is some work to do, see if any left on highest priority
     SET_PRIORITY := PRIORITY_TYPE'FIRST;
     loop
       if BUFFER(SET PRIORITY).ON /= BUFFER(SET_PRIORITY).OFF then
         Dequeue(SET_PRIORITY,WORK_REQUEST); -- at this point, requests real
         OBJECT := Shapes.OBJECT PTR_TABLE(WORK_REQUEST.GBJECT);
         Erase_Image(WORK_REQUEST.XY_OLD, OBJECT);
         Draw_Image (WORK_REQUEST.XY_NEW, OBJECT, WORK_REQUEST.COLOR);
         NO_WORK := FALSE;
         exit;
                                        -- leave loop if we processed a request
       else
                                        -- default
         NO_WORK := TRUE;
         exit when SET_PRIORITY = PRIORITY_TYPE'LAST;
         SET_PRIGRITY := PRIORITY_TYPE'SUCC(SET_PRIORITY);
       end if;
     end loop;
   exception
     when QUEUE ERROR => null;
                                      -- since error is propagated to caller
     when others =>
       Debug_IO.Put_Line("Error in Display Task");
                                        -- exception block
   end;
 Time Stamp.Log(0029); --$TP(0029) Graphics task end
 end loop;
end Display_Type;
end Graphics;
```

```
--% UNIT: Grid_to_Pixel Function Spec.
--% Effects: Converts battlefield meters X-Y to graphics Pixel X-Y.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | SUBPROGRAM SPEC : Grid_To_Pixel
--|
--| This function provides a translation to go from the "real" battlefield
-- to the screen battlefield. Note that the screen battlefield has the Y
-- component at 0 at the top of the screen and increasing positively down
-- the screen. A diagram in hwconfig.as shows the complete screen.
--1
-- Modifications Log
-- 88-U9-26 : TEG => Original created.
with Shapes;
with Types;
function Grid_to_Pixel(GRID : in Types.POSITION_TYPE) return Shapes.Pixel;
pragma INLINE(Grid_to_Pixel);
```

```
--% UNIT:
           Grid to Pixel Function Spec.
--% Effects: Converts battlefield meters X-Y to graphics Pixel X-Y.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
--| SUBPROGRAM BODY : Grid_To_Pixel
--1
--| Translate from Battlefield Grid coordinates in meters to pixels
-- on the screen. This means applying scale factors for x/y and
--| providing offsets to battlefield area on screen. NOTE: since
--| battlefield coordinates have 0,0 in lower left; and graphics
-- coordinates have 0,0 in upper left, this involves a transpose of
-- | the Y axis (thus the '-').
- - [
-- Modifications Log
-- 88-10-20 : TEG => Original created.
-- 89-01-04 : MPS => Changed Time Stamp to properly time the routine.
with Config;
with Time_Stamp;
with Math;
  pragma ELABORATE(Time_Stamp, Math);
function Grid to Pixel(GRID: in Types.POSITION_TYPE) return Shapes.Pixel is
  use Types;
  use Math;
  TEMP : Types.LONG_FIXED;
  PIX : Shapes.PIXEL;
begin
                          --$TP(0030) Grid_To_Pixel start
  Time Stamp.Log(0030);
  TEMP := GR!D.X / Types.LONG FIXED(Config.meters_per_x_pixel);
  PIX.X := Config.battlefield screen_left + Types.COORDINATE(TEMP);
  TEMP := GRID.Y / Types.LONG_FIXED(Config.meters_per_y_pixel);
  Fi': := Config.battlefield_screen_left + Types.COORDINATE(TEMP);
  Time_Stamp.Log(0031); -- $TP(0031) Grid_To_Pixet end
  return PIX;
end Grid_to_Pixet;
```

```
--% UNIT: Guidance Task Subunit
--% Effects: Calls "Guide" to compute next rocket aimpoint for every --
       active rocket in the input list.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
--| TASK BODY : Rocket.Guidance
--|
--| Task Guidance is used as a template for an array of tasks which compute
--| guidance information for a specified number of rockets. The first thing
-- it does is it gets the history information for the rocket/target list
--| and makes a local copy. The index of the history array (containing previous
--| positions and which rockets were previously active) is ROCKET_ID. The
--| entire guide_list array is passed, even though many of the entries may be
-- | inactive. Only active rockets (those that are in the air or taking off)
-- are given guidance. The entire array however is again passed back to the
-- | caller, the rocket control task.
--|
-- Modifications Log
-- 88-10-12 : TEG => Original created.
-- 89-11-22 : MPS => Adjusted to work with new Guide procedure.
with Guide:
with Time Stamp;
with Interrupt_Control;
 pragma ELABORATE(Guide, Time_Stamp, Interrupt_Control);
separate(Rocket)
task body Guidance_Type is
                        -- for operator visibility
 use Types;
 NEXT_GUIDE_LIST : AIMPOINT_LIST_TYPE(1..Config.max_rockets);
 NEXT_HISTORY LIST : POSITION LIST TYPE(1..Config.max_rockets);
 FIRST_ROCKET_ID : Types.WORD_INDEX;
 LAST_ROCKET_ID : Types.WORD_INDEX;
begin
 Loop
                           -- main processing loop
   begin
                               -- exception block
   Time_Stamp.Log(0032); --$TP(0032) Guidance task start
                            --$TP(0033) Guidance accept History start
     Time_Stamp.Log(0033);
     accept History(AIM_DATA : in POSITION_LIST_TYPE) do
```

```
FIRST_ROCKET_ID := AIM_DATA'first;
       LAST_ROCKET_ID := AIM_DATA'last;
        Interrupt_Control.Disable;
                                    --BUGFIX for compiler bug (direction flag)
        NEXT_HISTORY_LIST(FIRST_ROCKET_ID..LAST_ROCKET_ID) := AIM_DATA;
        Interrupt_Control.Enable; --BUGFIX for compiler bug
      end History;
                            --$TP(0034) Guidance accept History end
      Time_Stamp.Log(0034);
-- process list to create guidance information
      for ROCKET_ID in FIRST_ROCKET_ID..LAST_ROCKET_ID loop
       if NEXT_HISTORY_LIST(ROCKET_ID).ACTIVE then
         Guide(ROCKET_ID, NEXT_HISTORY_LIST(ROCKET_ID).ROCKET_POS,
               NEXT HISTORY LIST(ROCKET ID). TARGET POS,
               NEXT_GUIDE_LIST(ROCKET_ID));
        end if;
      end loop;
                               --$TP(0035) Guidance accept Next Guidance start
      Time Stamp.Log(0035);
      accept Next_Guidance(AIMPOINT_LIST : out AIMPOINT_LIST TYPE) do
        if AIMPOINT_LIST'first /= FIRST_ROCKET_ID or -- make sure list hasn't
          AIMPOINT_LIST'last /= LAST_ROCKET_ID
                                                 -- changed from History
        then
         raise GUIDANCE_LIST_ERROR;
        else
         Interrupt_Control.Disable; --BUGFIX for compiler bug (direction flag)
         AIMPOINT_LIST := NEXT_GUIDE_LIST(FIRST_ROCKET_ID..LAST_ROCKET_ID);
         Interrupt_Control.Enable; --BUGFIX for compiler bug (direction flag)
       end if;
      end Next_Guidance;
      Time Stamp.Log(0036); --$TP(0036) Guidance accept Next Guidance end
    exception
      when others =>
        Debug IO.Put Line("Error in GUIDANCE TASK");
                                 -- exception block
    end;
                          --$TP(0037) Guidance task end
  Time_Stamp.Log(0037);
  end loop;
                           -- main processing loop
end Guidance_Type;
```

```
--% UNIT: Guide Function Spec.
--% Effects: Computes a new aimpoint based on rocket/target positions. --
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | SUBPROGRAM SPEC : Guide
--|
--| The Guide function is used to find an aimpoint for the rocket to fly at
-- | when it is in flight. This includes guidance for the rocket during it's
--| launch phase. It takes as parameters the rocket_index, the latest positions
--| of both the rocket and target and returns two Binary Angle Measuements,
-- one Azimuth and one Elevation per cal.
--|
-- Modifications Log
-- 88-12-05 : TEG => Original created.
-- 89-11-07 : TEG => Changed from a function to a procedure call.
with Types;
procedure Guide(ROCKET_ID : Types.ROCKET_INDEX_TYPE;
               ROCKET_POS : Types.POSITION_TYPE;
               TARGET_POS : Types.POSITION_TYPE;
               NEW_AIMPOINT : out Types.AIMPOINT_TYPE);
--pragma INLINE(Guide);
```

```
--% UNIT: Guide Function Body.
--% Effects: Computes a new aimpoint based on rocket/target positions. --
--% Modifies: Aim Data Rocket Info is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | SUBPROGRAM BODY : Guide
--|
--| The Guide function takes the most recent two postions of a rocket/target
--! pair, and computes an aimpoint for the rocket to intercept.
--| Because the target is assumed to be moving, a process which
--| extrapolates the target's position forward is used. However, this section
-- is only called upon when the rocket is close to the target (TIME_TO_TARGET).
--| The basic theory of operation is to control the rocket attitude by changing
 -| the previous aimpoint incrementally according to the amount of change
-- | desired in the acceleration from the last interval.
--1
-- Modifications Log
-- 88-11-09 : TEG => Original created.
-- 89-11-14 : TEG => Equations were improved upon to be more realistic.
with Config;
with Types:
with Math:
with Rocket;
with Aim_Data;
with Aimpoint; -- function
  pragma ELABORATE(Math, Aimpoint);
procedure Guide(ROCKET_ID : Types.ROCKET_INDEX_TYPE;
               ROCKET POS : Types.POSITION_TYPE;
                TARGET POS : Types.POSITION_TYPE;
               NEW AIMPOINT : out Types.AIMPOINT_TYPE) is
                -- for operators
  use Types;
  use Aim Data; -- for enumeration types AXIS (x,y,z)
               -- for speedy fixed point math
  use Math;
                      : constant := 1.0; -- resolution on TIME_TO_TARGET
  accuracy
                     : constant := 6; -- boost done when z >= 1/6 (dist x_y)
  height factor
  integration_interval : constant := 4.0; -- periods to integration acceleration
  integration_int_sq : constant := (integration_interval-1.0) ** 2;
  furthest_extrapolate : constant := 300.0; -- don't bother going beyond
                    : constant := 3.0; -- maximum change to acceleration
  max_change
```

```
: constant Types.LONG_FIXED := Types.LONG_FIXED(
  limit_rock_extrap
                    (Config.meters in battle area + Types.LONG FIXED(1000.0)));
 ROCKET VELOC 1
                     : Aim Data.RATE REC TYPE;
 ROCKET_VELOC_2
                     : Aim Data.RATE REC_TYPE;
 TARGET_VELOC_1
                     : Aim Data.RATE_REC_TYPE;
 ROCKET_ACCEL
                     : Aim_Data.RATE_REC_TYPE;
 ROCK TARG DELTA
                     : Types.POSITION_TYPE;
 BOOST_LIMIT
                     : Types.LONG_FIXED;
 ROCK_TARG_DSQ_X
                     : Types.LONG_FIXED;
 ROCK_TARG_DSQ_Y
                     : Types.LONG_FIXED;
 ROCK TARG DSQ Z
                     : Types.LONG FIXED;
 ROCK TARG DIST
                     : Types.LONG FIXED;
 ROCK_TARG_XY_DIST : Types.LONG_FIXED;
 ROCK_SQ_X
                   : Types.RATE_TYPE;
 ROCK_SQ_Y
                   : Types.RATE_TYPE;
 ROCK_SQ_Z
                     : Types.RATE_TYPE;
 ROCK_VELOC_VECT : Types.RATE_TYPE;
 ROCK_XY_VELOC_VECT : Types.RATE_TYPE;
 TIME_TO_TARGET
                    : Types.LONG_FIXED;
  EXTRAP_TARG
                     : Types.POSITION_TYPE;
  EXTRAP_ROCK
                     : Types.POSITION_TYPE;
 DESIRED_VELOC
                   : Aim_Data.RATE_REC_TYPE;
 DESIRED_ACCEL
                   : Aim_Data.RATE_REC_TYPE;
  CHANGE_ACCEL
                     : Aim_Data.RATE_REC_TYPE;
 SUM
                     : Types.LONG_FIXED;
  SUM VELOCITY
                     : Types.LONG FIXED;
 AZIMUTH
                     : Types.BAM;
 ELEVATION
                     : Types.BAM;
  INTEGRATION_PERIOD : Types.LONG_FIXED;
  INTEGRATION_SQ
                     : Types.LONG_FIXED;
begin
-- If a new launch is taking place, initialize the Aim Data data base.
  if ROCKET_POS.Y = Config.launch_y and ROCKET_POS.X = Config.launch_x then
   Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_TARG := TARGET_POS;
   Aim_Data.ROCKET_INFO(ROCKET_ID).CURR_TARG := TARGET_POS;
   Aim_Data.ROCKET_INFO/ROCKET_ID).PREV_ROCK := ROCKET_POS;
   Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK := ROCKET_POS;
   Aim_Data.ROCKET_INFO(ROCKET_ID).CURR_ROCK := ROCKET_POS;
   Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT :=
                           (Config.launch_elevation,Config.launch_azimuth);
   Aim_Data.ROCKET_INFO(ROCKET_ID).BOOST_PHASE := TRUE;
  end if;
  First update history of data.
  Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_TARG :=
                              Aim_Data.ROCKET_INFO(ROCKET_ID).CURR_TARG;
  Aim_Data.ROCKET_INFO(ROCKET_ID).CURR_TARG := TARGET_POS;
```

```
Aim_Data.ROCKET_INFO(ROCKET_ID).PREV_ROCK :=
                              Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK;
 Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK :=
                              Aim_Data.ROCKET_INFO(ROCKET_ID).CURR_ROCK;
 Aim_Data.ROCKET_INFO(ROCKET_ID).CURR_ROCK := ROCKET_POS;
-- First check Target's Y coordinate to avoid friendly fire.
-- IF ROCKET IS GOING OVER TARGET, SIMPLY SET AIMPOINT STRAIGHT DOWN.
 if TARGET_POS.Y < ROCKET_POS.Y then
   Aim_Data.ROCKET INFO(ROCKET_ID).OLD_AIMPOINT := (ELEVATION => -16384,
           AZIMUTH => Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT.AZIMUTH);
   NEW_AIMPOINT := Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT;
   return;
 end if:
  Compute Rocket Velocity in all three axes.
 ROCKET_VELOC_1.X := Types.RATE_TYPE(ROCKET_POS.X -
             Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK.X); -- rocket change X
 ROCKET_VELOC_1.Y := Types.RATE_TYPE(ROCKET_POS.Y -
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK.Y); -- rocket change Y
 ROCKET_VELOC_1.2 := Types.RATE_TYPE(ROCKET_POS.Z -
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK.Z); -- rocket change Z
 ROCKET_VELOC_2.X := Types.RATE TYPE(
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK.X -
            Aim_Data.ROCKET_INFO(ROCKET_ID).PREV_ROCK.X); -- rocket change X
 ROCKET_VELOC_2.Y := Types.RATE_TYPE(
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK.Y -
            Aim_Data.ROCKET_INFO(ROCKET_ID).PREV_ROCK.Y); -- rocket change Y
 ROCKET VELOC 2.Z := Types.RATE TYPE(
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_ROCK.Z -
            Aim_Data.ROCKEr_INFO(ROCKET_ID).PREV_ROCK.Z); -- rocket change Z
  Compute Target Velocity in all three axes.
 TARGET_VELOC_1.X := Types.RATE_TYPE(TARGET_POS.X -
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_TARG.X); -- target change X
 TARGET_VELOC_1.Y := Types.RATE_TYPE(TARGET_POS.Y -
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_TARG.Y); -- target change Y
 TARGET_VELOC_1.Z := Types.RATE_TYPE(TARGET_POS.Z -
            Aim_Data.ROCKET_INFO(ROCKET_ID).LAST_TARG.Z); -- target change Z
  Compute Acceleration for Rocket in all three axes.
 ROCKET_ACCEL.X := ROCKET_VELOC_1.X - ROCKET_VELOC_2.X;
 ROCKET_ACCEL.Y := ROCKET_VELOC_1.Y - ROCKET_VELOC_2.Y;
 ROCKET_ACCEL.Z := ROCKET_VELOC_1.Z - ROCKET_VELOC_2.Z;
-- Compute velocity vector for rocket using the formula
```

```
v = sqrt(curr_rock.X**2 + curr_rock.y**2 + curr_rock.z**2)
 ROCK_SQ_X := ROCKET_VELOC_1.X * ROCKET_VELOC_1.X;
 ROCK_SQ_Y := ROCKET_VELOC_1.Y * ROCKET_VELOC_1.Y;
 ROCK_SQ_Z := ROCKET_VELOC_1.Z * ROCKET_VELOC_1.Z;
 SUM_VELOCITY := Types.LONG_FIXED(ROCK_SQ_X) + Types.LONG_FIXED(ROCK_SQ_Y);
 ROCK_XY_VELOC_VECT := Types.RATE_TYPE(Math.Sqrt(SUM_VELOCITY));
 ROCK_VELOC_VECT := Types.RATE_TYPE(Math.Sqrt(SUM_VELOCITY +
                    Types.LONG_FIXED(ROCK_SQ_Z)));
-- Compute distance between rocket and target using the formula
       d = sqrt(d(X)**2 + d.Y**2 + d.Z**2)
       where d(i) = curr_rock(i) - curr_targ(i)
 ROCK_TARG_DELTA.X := TARGET_POS.X - ROCKET_POS.X;
 ROCK_TARG_DELTA.Y := TARGET_POS.Y - ROCKET_POS.Y;
 ROCK_TARG_DELTA.Z := TARGET_POS.Z - ROCKET_POS.Z;
 ROCK TARG DSQ X := ROCK TARG DELTA.X * ROCK TARG DELTA.X;
 ROCK TARG DSQ Y := ROCK TARG DELTA.Y * ROCK TARG DELTA.Y;
 ROCK_TARG_DSQ_Z := ROCK_TARG_DELTA.Z * ROCK_TARG_DELTA.Z;
 SUM := ROCK_TARG_DSQ_X + ROCK_TARG_DSQ_Y + ROCK_TARG_DSQ_Z;
 ROCK_TARG_DIST := Math.Sqrt(SUM);
-- Compute rocket time to target, ITERATION TAKES INTO ACCOUNT
-- changes in rocket velocity and target motion (NOTE: change in

    rocket acceleration is NOT included)

 if ROCK VELOC VECT > 0.01 then
   TIME_TO_TARGET := ROCK_TARG_DIST / ROCK_VELOC_VECT;
-- Extrapolate target position based on TIME_TO_TARGET.
-- Since TIME TO TARGET does not take into account rocket accelaration,
-- it tends to be way off during low rocket velocities. To reduce the
-- effect of this, limit the extrapolation to a reasonable period.
    if TIME_TO_TARGET > furthest_extrapolate then
     TIME_TO_TARGET := furthest_extrapolate;
   end if;
   EXTRAP TAPG.X := TARGET POS.X + TARGET_VELOC_1.X * TIME TO TARGET;
   EXTRAP_TARG.Y := TARGET_POS.Y + TARGET_VELOC_1.Y * TIME_TO_TARGET;
  prevent from extrapolating the target behind the rocket
    if EXTRAP_TARG.Y < ROCKET_POS.Y then
     EXTRAP_TARG.Y := ROCKET_POS.Y;
   EXTRAP_TARG.Z := TARGET_POS.Z + TARGET_VELOC_1.Z * TIME_TO_TARGET;
```

```
else
   TIME_TO_TARGET := integration_interval + 1.0;
   EXTRAP_TARG.X := TARGET POS.X;
   EXTRAP_TARG.Y := TARGET_POS.Y;
   EXTRAP_TARG.Z := TARGET_POS.Z;
 end if;
 if TIME_TO_TARGET < integration_interval then</pre>
   INTEGRATION_PERIOD := TIME_TO_TARGET / Types.WORD(2);
   if INTEGRATION_PERIOD < 1.0 then
     INTEGRATION_PERIOD := 1.0;
   end if;
   INTEGRATION SQ := (INTEGRATION PERIOD-1.0) * (INTEGRATION_PERIOD-1.0);
   INTEGRATION_PERIOD := integration_interval;
   INTEGRATION_SQ
                    := integration_int_sq;
 end if;
-- Compute where the ROCKET will be at the end of the INTEGRATION period.
-- All velocities will be calculated for that point to target. Limit the
-- extrapolations to reasonable values.
 EXTRAP_ROCK.X := ROCKET_POS.X + ROCKET_VELOC_1.X * (INTEGRATION_PERIOD-1.0) +
                  (ROCKET_ACCEL.X / Types.WORD(2)) * INTEGRATION_SQ;
 if EXTRAP_ROCK.X > limit_rock_extrap then
   EXTRAP_ROCK.X := limit_rock_excrap;
 end if;
 EXTRAP_ROCK.Y := ROCKET_POS.Y + ROCKET_VELOC_1.Y * (INTEGRATION_PERIOD-1.0) +
                  (ROCKET_ACCEL.X / Types.WORD(2)) * INTEGRATION_SQ;
 if EXTRAP_ROCK.Y limit_rock_extrap then
   EXTRAP_ROCK.Y := limit_rock_extrap;
 end if;
 EXTRAP_ROCK.Z := ROCKET_POS.Z + ROCKET_VELOC_1.Z * (INTEGRATION_PERIOD-1.0) +
                  (ROCKET_ACCEL.X / Types.WORD(2)) * INTEGRATION_SQ;
 if EXTRAP_ROCK.Z > limit_rock_extrap then
   EXTRAP_ROCK.Z := limit_rock_extrap;
 end if;
 ROCK_TARG_DELTA.X := EXTRAP_TARG.X - EXTRAP_ROCK.X;
 RUCK_TARG_DELTA.Y := EXTRAP_TARG.Y - EXTRAP_ROCK.Y;
 ROCK_TARG_DELTA.Z := EXTRAP_TARG.Z - EXTRAP_ROCK.Z;
 ROCK_TARG_DSQ_X := ROCK_TARG_DELTA.X * ROCK_TARG_DELTA.X;
 ROCK_TARG_DSQ_Y := ROCK_TARG_DELTA.Y * POCK_TARG_DELTA.Y;
 ROCK_TARG_DSQ_Z := ROCK_TARG_DELTA.Z * ROCK_TARG_DELTA.Z;
 SUM := ROCK_TARG_DSQ_X + ROCK_TARG_DSQ_Y + ROCK_TARG_DSQ_Z;
 ROCK TARG DIST := Math.Sqrt(SUM);
 ROCK_TARG_XY_DIST := Math.sqrt(ROCK_TARG_DSQ_X + ROCK_TARG_DSQ_Y);
```

```
-- Compute Desired Velocities in each axis for the end of INTEGRATION period.
-- If distance to target is too small to measure, then don't bother to find a
-- new desired velocity or acceleration because the rocket has already hit
-- the target by now!
  if ROCK_TARG_XY_DIST /= 0.0 then
   DESTRED_VELOC.X := ROCK_XY_VELOC_VECT *
                       (ROCK_TARG_DELTA.X / ROCK_TARG_XY_DIST);
   DESIRED_VELOC.Y := ROCK_XY_VELOC_VECT *
                       (ROCK_TARG_DELTA.Y / ROCK_TARG_XY_DIST);
   DESIRED_VELOC.Z := ROCK_VELOC_VECT * (ROCK_TARG_DELTA.Z / ROCK_TARG_DIST);
-- Compute Desired Accelerations
    DESIRED_ACCEL.X := (DESIRED_VELOC.X - ROCKET_VELOC_1.X) / INTEGRATION_PERIOD;
    DESIRED_ACCEL.Y := (DESIRED_VELOC.Y - ROCKET_VELOC_1.Y) / INTEGRATION_PERIOD;
    DESIRED_ACCEL.Z := (DESIRED_VELOC.Z - ROCKET_VELOC_1.Z) / INTEGRATION_PERIOD;
-- Compare Current Rocket Acceleration to Desired Rocket Acceleration
-- to produce Change in Acceleration
  end if;
  CHANGE_ACCEL.X := DESIRED_ACCEL.X - ROCKET_ACCEL.X *
               Math.SIN(Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT.AZIMUTH);
-- LIMIT THE CHANGE IN ACCELERATION
  if abs CHANGE_ACCEL.X > max_change then
    if CHANGE ACCEL.X < 0.0 then
      CHANGE_ACCEL.X := -max_change;
      CHANGE_ACCEL.X := max_change;
    end if;
  end if;
  CHANGE_ACCEL.Z := DESIRED_ACCEL.Z - ROCKET_ACCEL.Z;
  if abs CHANGE_ACCEL.Z > max_change then
    if CHANGE_ACCEL.Z < 0.0 then
      CHANGE_ACCEL.Z := -max_change;
      CHANGE_ACCEL.Z := max_change;
    end if;
  end if;
-- Now translate from acceleration change requests to new aimpoint
  Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT :=
```

```
AIMPOINT(Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT,CHANGE_ACCEL);

-- Now check if BOOST PHASE, if so go UP by adjusting ELEVATION. Do not adjust
-- AZIMUTH because it is already pointing in the correct direction.

-- if Aim_Data.ROCKET_INFO(ROCKET_ID).BOOST_PHASE then

BOOST_LIMIT := ROCK_TARG_XY_DIST / Types.WORD(height_factor);

if ROCKET_POS.Z > BOOST_LIMIT then

Aim_Data.ROCKET_INFO(ROCKET_ID).BOOST_PHASE := FALSE;

end if;

Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT.ELEVATION :=

Config.launch_elevation;

end if;

-- boost_phase check

NEW_AIMPOINT := Aim_Data.ROCKET_INFO(ROCKET_ID).OLD_AIMPOINT;

end Guide;
```

```
--% UNIT:
             Guide Buf Task Subunit
--% Effects: Provides asynchronous comm. between simulator and Control.--
-- % Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
--| TASK BODY : Simulate.RDL.Guide_Buf
---
--| The Guide_Buf task acts as a buffer between the rocket data link
--| support task Rock_Sup and the Rocket.Control task which processes
-- the rocket data. The direction flow is from Rocket.Control to the
-- Pock Sup task, even though there are only accept statements here. This is
-- to ease timing constraints. The purpose of MSG_COUNT is to allow Rock_Sup
-- | to use previous guidance messages if Rocket.Control is late sending it's
-- new guidance message. However, it is set to zero at the start so
--| that before the main procedure gets a chance to run, Rock_Sup will
--| wait at the accept for at least one current guidance message, the
--| first one. After the first guidance message is received, because
-- | timing of the system is derived from Rock_Sup, Rock Sup no longer will
-- | need to wait for a new guidance message from Control. This operation
--| reflects the fact that the rockets will continue to travel through space
--- regardless of whether there is guidance for them or not.
--|
-- Modifications Log
- 88-10-20 : TEG => Original created.
with Debug_IO;
with Time_Stamp;
separate (Simulate.RDL)
task body Guide Buf_Type is
 use Types;
 start
             : constant Types.WORD_INDEX := 1; -- start of arrays
 GUIDE MSG : Rocket.ROCKET_GUIDE MSG_TYPE;
 MSG_COUNT
             : Types.WORD := 0; -- if a message has been buffered
begin
 loop
                           -- $TP(0040) Guidebuf task start
   Time_Stamp.Log(0040);
   select
     accept Put_Guide(DATA : in Rocket.ROCKET_GUIDE_MSG TYPE) do
       Time_Stamp.Log(0041);
                               ---$TP(0041) Guidebuf accept Put_Guide start
       GUIDE_MSG.NUM_ROCKETS := DATA.NUM_ROCKETS;
                                                        -- copy data
```

```
GUIDE_MSG.ROCKET_GUIDE_LIST(start..DATA.NUM_ROCKETS) :=
                        DATA.ROCKET_GUIDE_LIST(start..DATA.NUM_ROCKETS);
       MSG_COUNT := 1;
                               -- only meaningful that it is > 0
       Time_Stamp.Log(0042); --$TP(0042) Guidebuf accept Put_Guide end
     end Put_Guide;
   or
     when MSG_COUNT > 0 =>
     accept Get_Guide(DATA : out Rocket.ROCKET_GUIDE_MSG_TYPE) do
       Time_Stamp.Log(0043);
                                 --$TP(0043) Guidebuf accept Get_Guide start
       DATA.NUM_ROCKETS := GUIDE_MSG.NUM_ROCKETS;
       DATA.ROCKET_GUIDE_LIST(start..GUIDE_MSG.NUM_ROCKETS) :=
            GUIDE_MSG.ROCKET_GUIDE_LIST(start..GUIDE_MSG.NUM_ROCKETS);
       MSG_COUNT := 1;
                                   -- do keep multiple copies
                               --$TP(0044) Guidebuf accept Get_Guide end
       Time_Stamp.Log(0044);
     end Get_Guide;
    end select;
 Time_Stamp.Log(0045); --$TP(0045) Guidebuf task end
 end loop;
exception
 when others =>
   Debug_IO.Put_Line("GUIDE_BUF termination due to exception.");
end Guide_Buf_Type;
```

```
--% UNIT
               : Hardware Configuration Spec.
--% Effects
            : None.
-- % Modifies : Nothing.
--% Requires
              : The hardware defined below.
--% Raises
               : No exceptions.
--% Engineer : M. Sperry.
-- | PACKAGE SPEC : HW_Config
    This package is designed to familiarize the user with the hardware that
--| the BDS was originally implemented upon. It is implemented on a TANDY 4000
--| with an EGA screen, utilizing a Logitech C7 Serial Mouse on serial port COM2
--| as a pointing device. The timer chip addresses and values are defined.
-- Note: Some machine addresses (for the EGA especially) are in the package
-- | Machine_Dependent.
--1
-- Modifications Log
-- 89-08-08 : MPS => Original created.
-- 89-11-19 : MPS => Added timer chip addresses and constants
with Types;
with Low_Level_IO;
package HW_Config is
-- The following addresses are used for this machine.
COM2 data
                 : constant Low_Level_IO.PORT_ADDRESS := 16#2F8#;
COM2_int_enable : constant Low_Level_IO.PORT_ADDRESS := 16#2F9#;
COM2_int_ident : constant Low_Level_IO.PORT_ADDRESS := 16#2FA#;
                : constant Low_Level_IO.PORT_ADDRESS := 16#2FB#;
COM2_control
COM2_modem_control : constant Low_Level_10.PORT_ADDRESS := 16#2FC#;
                  : constant Low_Level_IO.PORT_ADDRESS := 16#2FD#;
COM2_status
pic_8259
                  : constant Low_Level_IO.PORT_ADDRESS := 16#20#;
pic_8259_mr
                  : constant Low_Level_IO.PORT_ADDRESS := 16#21#;
counter_two_addr : constant := 16#42#;
timer control addr : constant := 16#43#;
end HW_Config;
```

```
Interrupt_Control Package Spec. and Body.
-- X Effects: Provides control over interrupt flags.
-- % Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: M. Sperry.
-- PACKAGE SPEC : Interrupt_Control
---
--| The purpose of the Interrupt_Control package is to provide Ada level
--| semantics for disabling and enabling interrupts on the 80x86 family of
-- | processors. Also for clearing the direction flag because of an RTE bug
-- | which does not always clear it.
---
-- Modifications Log
-- 88-11-20 : MPS => Original created.
with Machine_Code;
use Machine_Code;
pragma ELABORATE(Machine_Code);
package Interrupt_Control is
pragma SUPPRESS(Elaboration_Check);
procedure Disable;
pragma INLINE(Disable);
procedure Enable;
pragma INLINE(Enable);
procedure Clear_Direction_flag;
pragma INLINE(Clear_Direction_Flag);
end Interrupt_Control;
-- | PACKAGE BODY : Interrupt_Control
-- [
-- Interrupt_Control is implemented with machine code statments. The
-- | suppression of the elaboration check is used to make the inlining of
-- | these machine instructions as short as possible.
--|
package body Interrupt_Control is
```

```
procedure Disable is
begin
    MACHINE_INSTRUCTION'(none,m_CLI);
end Disable;

procedure Enable is
begin
    MACHINE_INSTRUCTION'(none,m_STI);
end Enable;

procedure Clear_Direction_Flag is
begin
    MACHINE_INSTRUCTION'(none,m_CLD);
end Clear_Direction_Flag;
end Interrupt_Control;
```

```
Machine Dependent Package Spec.
--% Effects: Provides machine dependent operations for enhanced speed. --
-- % Modifies: No global data is modified.
--% Requires: Graphics mode, and initialization of timer channel two. --
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: M. Sperry.
______
-- PACKAGE SPEC : Machine_Dependent
--|
--| Package Machine Dependent contains machine code statements to perform
--| low level graphics functions, including an interface to the BIOS routines
-- | found on the EGA (for text processing). Note that these instructions
-- | are inlined to enhance speed.
--| Also implemented are routines which perform fixed point multiplications
-- and divisions in machine code for speed enhancements.
--| And, a procedure which returns the value in the channel two counter.
--|
-- Modifications Log
-- 88-11-04 : MPS => Original created.
-- 89-08-24 : MPS => Specifications for fixed math routines incorporated.
-- 89-11-21 : MPS => Next_Random created.
with Machine Code;
with Graphics;
with Types;
use Machine Code;
pragma ELABORATE(Machine Code);
package Machine_Dependent is
                                           -- mode 2, channel 2
-- channel 2 LSB divisor
start_countdown : constant := 16#82#;
max timer value : constant := 256;
procedure Put_Pixel(ABS_X, ABS_Y : Types.COORDINATE;
                            : Graphics.COLOR_TYPE);
                   COLOR
pragma INLINE(Put_Pixel);
procedure Write_Mode_0;
pragma INLINE(Write_Mode_0);
-- Provide a mechanism to call ROM located routine to initialize screen
procedure Int10(BIOS_FUNCTION : Types.WORD; -- spec to BIOS graphics call
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PARAM_1
                             : Types.WORD;
                PARAM_2
                             : Types.WORD);
pragma INTERFACE(ASM86, Int10);
pragma INTERFACE_SPELLING(Int10, "D18IOS?GRAPHICSCALL");
procedure Write Mode 2;
pragma INLINE(Write_Mode_2);
procedure Long_Long_Mul(LEFT,RIGHT : Types.LONG_FIXED;
                        RESULT
                                   : out Types.LONG FIXED);
pragma INLINE(Long_Long_Mul);
procedure Long_Long_Div(LEFT,RIGHT : Types.LONG_FIXED;
                        RESULT
                                   : out Types.LONG_FIXED);
pragma INLINE(Long_Long_Div);
procedure Long_Word_Div(LEFT : Types.LONG_FIXED;
                        RIGHT : Types.WORD;
                        RESULT : out Types.LONG_FIXED);
pragma INLINE(Long_Word_Div);
procedure Meters Meters Div(LEFT, RIGHT: Types.METERS;
                            RESULT
                                      : out Types.METERS);
pragma INLINE(Meters_Meters_Div);
procedure Meters_Word_Div(LEFT : Types.METERS;
                          RIGHT : Types.WORD;
                          RESULT : out Types.METERS);
pragma INLINE(Meters_Word_Div);
procedure Meters_Meters_Mul(LEFT,RIGHT : Types.METERS;
                            RESULT
                                      : out Types.METERS);
pragma INLINE(Meters_Meters_Mul);
procedure Rate_Rate_Mul(LEFT,RIGHT : Types.RATE_TYPE;
                        RESULT
                                   : out Types.RATE_TYPE);
pragma INLINE(Rate_Rate_Mul);
procedure Rate_Rate_Div(LEFT,RIGHT : Types.RATE_TYPE;
                        RESULT
                                   : out Types.RATE TYPE);
pragma INLINE(Rate_Rate_Div);
procedure Rate_Word_Div(LEFT : Types.RATE_TYPE;
                        RIGHT : Types.WORD;
                        RESULT : out Types.RATE_TYPE);
pragma INLINE(Rate_Word_Div);
procedure Long_Rate_Div(LEFT : Types.LONG_FIXED;
                        RIGHT : Types.RATE_TYPE;
                        RESULT : out Types.LONG_FIXED);
pragma INLINE(Long_Rate_Div);
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--% UNIT:
            Machine_Dependent Package Body.
--% Effects: Provides graphics machine dependencies.
--% Modifies: No global data is modified.
--% Requires: No initialization is required (other than graphics mode). --
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: M. Sperry.
_____
-- | PACKAGE BODY : Machine_Dependent
--1
--| A package which makes use of the functionality of the BIOS routines
--| found in an EGA card to perform some graphics processing. Note that
-- | some register level EGA programming is performed.
--| Also, the timer chip channel two functions are utilized to generate
-- pseudo random numbers.
- - |
-- Modifications Log
-- 88-08-25 : MPS => Original created.
-- 89-08-24 : MPS => Incorporated fixed math routine bodies for speed.
-- 89-11-28 : MPS => developed Next_Random procedure.
with HW_Config;
package body Machine Dependent is
hi res_graphics : constant := 16#10#;
                                              -- graphics mode
             : constant := 16#0200#;
                                               -- set cursor function
set_cursor
page zero
               : constant := 16#00#;
                                               -- set cursor to active page
write function : constant := 16#0E#;
index_register : constant := 16#3CE#;
                                              -- port address
access_register : constant := 16#3CF#;
                                              -- port address
                                               -- index register 5
mode_register : constant := 5;
write_mode_2_val : constant := 2;
write_mode_0_val : constant := 0;
procedure Put_Pixel(ABS_X, ABS_Y : Types.COORDINATE;
                   COLOR
                              : Graphics.COLOR_TYPE) is
-- | SUBPROGRAM BODY : Machine_Dependent.Put_Pixel
--| An assembly level procedure (for enhanced speed) to place a dot on the EGA
-- | screen. Write mode two is used here (again, for enhanced speed). It is
--! important to note that this routine could be called up to 1235 times per
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-- interval.
--1
begin
-- The first thing to do is find out which bit must be turned on. This is
-- done by taking SHR( 80h, ABS X mod 8 ). The bit ordering goes from 7 -> 0.
  MACHINE_INSTRUCTION'(register_register, m_MOV, CX, CX); -- defeat compiler bug
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, DX, 16#3CE#); -- select bit
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, AL, 8); -- mask register
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL); -- in graphics chip
-- Determine which bit must be turned on. This is
-- done by taking SHR( 80h, ABS_X rem 8 ), reversing the bit ordering.
  MACHINE_INSTRUCTION'(register_system_address. m_MOV, CX, ABS_X'address); --X
  MACHINE_INSTRUCTION'(register_register, m_MOV, BX, CX); -- make copy of X
  MACHINE_INSTRUCTION'(register_immediate, m_AND, CL, 7); -- mask for bit #
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, AL, 16#80#); -- most significant bit is
  MACHINE_INSTRUCTION'(register_register, m_SHR, AL, CL); -- bit zero, do bit reversal.
-- AL now holds the bit mask. Now give it to the bit mask register located
-- at 16#3CF#.
  MACHINE_INSTRUCTION'(register, m_INC, DX); -- increment port address to 3CF
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
-- Now, latch the byte of graphics memory. The byte to latch
-- is defined as (ABS_Y * 80) + (ABS_X / 8). Then, when giving
-- it back, place the color in AL. Note that only four bits of the color are
-- significant and that the color placed in AL is not actually a color, but a
-- palette register selection (from 0 to 15). The color in the palette
-- register is the color displayed.-16#6000# is loaded (= A000H)
-- to point to the EGA graphics page zero memory address.
  MACHINE_INSTRUCTION'(register_system_address, m_MOV, AX, ABS_Y'address);--Y
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, CX, 80); -- bytes/line
  MACHINE_INSTRUCTION'(register, m_MUL, CX); -- ABS_Y * 80 in AX
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, CL, 3); -- Shift Count
  MACHINE INSTRUCTION'(register_register, m_SHR, BX, CL); -- ABS_X / 8 in BX
  MACHINE_INSTRUCTION'(register_register, m_ADD, BX, AX); -- BX is offset
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, AX, -16#6000#); -- base of RAM
  MACHINE_INSTRUCTION'(register_register, m_MOV, ES, AX);
-- Latch the palette selection. Note that the contents of AL upon return are
-- meaningless, and that the color is latched internally to the EGA's four bit
-- planes.
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-- mov AL,ES:[BX]
  MACHINE_INSTRUCTION'(register_address, m_MOV, AL, ES, BX, nil, SCALE_1, 0);
  MACHINE_INSTRUCTION'(register_system_address, m_MOV, AX, COLOR'address);
-- Finally, give the palette selection (color) to the four bit planes.
-- mov ES:[BX].AL
  MACHINE_INSTRUCTION'(address_register, m_MOV, ES, BX, nil, SCALE_1, 0, AL);
end Put_Pixel;
procedure Write_Mode_0 is
-- | SUBPROGRAM BODY : Machine_Dependent.Write_Mode_0
---
    A procedure used to change the write mode of the screen to mode 0,
-- for text writing. This procedure is called before writing any text.
--1
begin
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, DX, index_register);
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, AL, mode_register);
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, DX, access_register);
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, AL, write_mode_0_val);
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
end Write_Mode_0;
procedure Write_Mode_2 is
--1
--| SUBPRCORAM BODY : Machine_Dependent.Write_Mode_2
--| A procedure used to change the write mode of the screen to mode 2, which
-- | facilitates the process of pixel plotting. This routine is called after
-- | writing the necessary statistics titles, etc.
---
begin
  MACHINE INSTRUCTION'(register immediate, m MOV, DX, index register);
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, AL, mode_register);
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
  MACHINE_INSTRUCTION'(register_immediate, m_MOV, DX, access_register);
 MACHINE_INSTRUCTION'(register_immediate, m_MOV, AL, write_mode_2_val);
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
end Write_Mode_2;
-- | SUBPROGRAM BODY : Machine_Dependent.fixed_Math routines
--|
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--! These routines are written in 80386 32-bit code optimized for the
--| types on which they operate for maximum speed. They are treated as a
-- | resource.
--1
procedure Long_Long_Mul(LEFT,RIGHT : Types.LONG_FIXED;
                                 : out Types.LONG_FIXED) is
                        RESULT
begin
  MACHINE_INSTRUCTION'(none,m_CLI);
  MACHINE_INSTRUCTION'(register_system_address,m_LEA,BX,LEFT'address);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHIME_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
  MACHINE_INSTRUCTION'(immediate,m DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address,m_IMUL,RIGHT'address);
  MACHINE_INSTRUCTION'(register immediate, m_MOV, CX, 6);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(immediate,m DATAB,16#66#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHRD EAX,EDX,CL
  MACHINE_INSTRUCTION'(immediate,m DATAB,16#AD#);
  MACHINE_INSTRUCTION'(immediate,m DATAB, 16#D0#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE_INSTRUCTION'(none,m_STI);
end Long_Long_Mul;
procedure Long_Long_Div(LEFT,RIGHT : Types.LONG_FIXED;
                        RESULT
                                  : out Types.LONG_FIXED) is
begin
  MACHINE_INSTRUCTION'(none,m_CLI);
  MACHINE_INSTRUCTION'(register_system_address,m_LEA,BX,LEFT'address);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE INSTRUCTION'(none,m CWD); -- CDQ
  MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,6);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHLD EDX,EAX,CL
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#A5#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#C2#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_register,m_SHL,AX,CL);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address,m_IDIV,RIGHT'address);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE_INSTRUCTION'(none,m_STI);
end Long_Long_Div;
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procedure Long_Word_Div(LEFT : Types.LONG_FIXED;
                        RIGHT : Types.WORD;
                        RESULT : out Types.LONG_FIXED) is
begin
  MACHINE_INSTRUCTION'(none,m_CLI);
  MACHINE_INSTRUCTION'(register_system_address,m_LEA,BX,RIGHT'address);
  MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(none,m_CBW);
                                       -- this instruction performs a CLIDE
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_register,m_MOV, 3X, AX);
  MACHINE_INSTRUCTION'(register system address,m LEA,SI,LEFT'address);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,SI,nil,scale_1,0);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(none,m_CWD);
                                      -- this instruction performs a CDQ
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register,m_IDIV,8X);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE_INSTRUCTION'(none,m_STI);
end Long_Word_Div;
procedure Meters_Meters_Mul(LEFT,RIGHT : Types.METERS;
                            RESULT
                                     : out Types.METERS) is
  MACHINE_INSTRUCTION'(register_register, m_MOV, CX, CX); -- defeat compiler bug
  MACHINE_INSTRUCTION'(register_system_address,m_MOV,AX,LEFT'address);
  MACHINE_INSTRUCTION'(system_address,m_IMUL,RIGHT'address);
  MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,3);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHRD AX,DX,CL
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#AD#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#D0#);
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
end Meters_Meters_Mul;
procedure Meters_Meters_Div(LEFT,RIGHT : Types.METERS;
                            RESULT
                                    : out Types.METERS) is
begin
  MACHINE_INSTRUCTION'(register_register, m_MOV, CX, CX); -- defeat compiler bug
  MACHINE_INSTRUCTION'(register_system_address, m_MOV, AX, LEFT'address);
  MACHINE_INSTRUCTION'(none,m_CWD);
  MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,3);
  MACHINE_INSTRUCTION'(register_register,m_SHL,AX,CL);
  MACHINE_INSTRUCTION'(system_address,m_IDIV,RIGHT'address);
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
end Meters_Meters_Div;
procedure Meters_Word_Div(LEFT : Types.METERS;
                          RIGHT : Types.WORD;
                          RESULT : out Types.METERS) is
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begin
  MACHINE INSTRUCTION'(register_register, m_MOV, CX, CX); -- defeat compiler bug
  MACHINE INSTRUCTION'(register system address,m MOV,AX,LEFT'address):
 MACHINE INSTRUCTION'(none, m CWD);
  MACHINE INSTRUCTION'(register_system_address,m_MOV,BX,RIGHT'address);
  MACHINE INSTRUCTION'(register, m_IDIV, BX);
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
end Meters Word Div:
procedure Rate Rate Mul(LEFT, RIGHT: Types.RATE TYPE;
                                   : out Types.RATE TYPE) is
                        RESULT
begin
  MACHINE INSTRUCTION'(none.m CLI):
  MACHINE INSTRUCTION'(register system address,m LEA, BX, LEFT'address);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
  MACHINE INSTRUCTION'(immediate,m DATAB,16#66#); -- 32 bit override
  MACHINE INSTRUCTION'(system address, m IMUL, RIGHT'address);
  MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,16);
  MACHINE INSTRUCTION'(immediate,m DATAB, 16#66#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHRD EAX,EDX,CL
  MACHINE INSTRUCTION'(immediate, m DATAB, 16#AD#);
  MACHINE INSTRUCTION'(immediate,m DATAB, 16#00#);
  MACHINE INSTRUCTION'(immediate, m DATAB, 16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE_INSTRUCTION'(none,m_STI);
end Rate Rate Mul;
procedure Rate Rate Div(LEFT,RIGHT : Types.RATE_TYPE;
                        RESULT
                                   : out Types.RATE_TYPE) is
begin
  MACHINE INSTRUCTION'(none, m_CLI);
  MACHINE INSTRUCTION'(register system address, m LEA, BX, LEFT'address);
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale 1,0);
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE INSTRUCTION'(none, m_CWD);
                                                   -- performs a CDQ
  MACHINE INSTRUCTION'(register_immediate,m_MOV,CX,16);
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHLD EDX,EAX,CL
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#A5#);
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#C2#);
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE INSTRUCTION'(register_register,m_SHL,AX,CL);
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE INSTRUCTION'(system_address,m_IDIV,RIGHT'address);
  MACHINE INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE INSTRUCTION'(none, m_STI);
end Rate_Rate_Div;
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procedure Rate_Word_Div(LEFT : Types.RATE_TYPE;
                        RIGHT : Types.WORD;
                        RESULT : out Types.RATE TYPE) is
begin
  MACHINE INSTRUCTION'(none,m CLI):
  MACHINE_INSTRUCTION'(register_system address,m_LEA,BX,RIGHT'address);
  MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(none,m_CBW);
                                       -- this instruction performs a CWDE
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_register,m_MOV,BX,AX);
  MACHINE_INSTRUCTION'(register_system_address,m_LEA,SI,LEFT'address);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,SI,nil,scale_1,0);
  MACHINE_INSTRUCTION'(immediate,m DATAB, 16#66#);
  MACHINE_INSTRUCTION'(none,m_CWD);
                                      -- this instruction performs a CDQ
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register,m_IDIV,BX);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE_INSTRUCTION'(none,m_STI);
end Rate_Word_Div;
procedure Rate_Long_Long_Mul(LEFT : Types.RATE_TYPE;
                             RIGHT : Types.LONG_FIXED;
                             RESULT : out Types.LONG FIXED) is
begin
  MACHINE_INSTRUCTION'(none,m_CLI);
  MACHINE_INSTRUCTION'(register_system_address,m_LEA,BX,LEFT'address);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address,m_IMUL,RIGHT'address);
  MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,16);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHRD EAX,EDX,CL
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#AD#);
  MACHINE INSTRUCTION'(immediate,m DATAB, 16#00#);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE_INSTRUCTION'(none,m_STI);
end Rate_Long_Long_Mul;
procedure Rate_Long_Rate_Mul(LEFT : Types.RATE_TYPE;
                             RIGHT : Types.LONG FIXED:
                             RESULT : out Types.RATE_TYPE) is
begin
 MACHINE INSTRUCTION'(none, m CLI);
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MACHINE_INSTRUCTION'(register_system_address,m_LEA,BX,LEFT'address);
 MACHINE INSTRUCTION'(immediate,m DATAB, 16#66#);
 MACHINE INSTRUCTION'(register address, m MOV, AX, SS, BX, nil, scale 1,0);
 MACHINE INSTRUCTION'(immediate, m DATAB, 16#66#); -- 32 bit override
 MACHINE_INSTRUCTION'(system_address,m_IMUL,RIGHT'address);
 MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,6);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
 MACHINE INSTRUCTION'(immediate, m_DATAB, 16#0F#); -- SHRD EAX, EDX, CL
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#AD#);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#D0#);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
 MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
 MACHINE_INSTRUCTION'(none,m_STI);
end Rate_Long_Rate_Mul;
procedure Long_Rate_Div(LEFT : Types.LONG_FIXED;
                        RIGHT : Types.RATE TYPE;
                        RESULT : out Types.LONG_FIXED) is
begin
 MACHINE INSTRUCTION'(none,m CLI);
 MACHINE INSTRUCTION'(register_system address,m LEA,BX,LEFT'address);
 MACHINE INSTRUCTION'(immediate, m_DATAB, 16#66#);
 MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
  MACHINE_INSTRUCTION'(none,m_CWD);
                                                  -- performs a CDQ
 MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,16);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHLD EDX,EAX,CL
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#A5#);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#C2#);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
 MACHINE INSTRUCTION'(register_register,m SHL,AX,CL);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
 MACHINE INSTRUCTION'(system address, m IDIV, RIGHT'address);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
 MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
 MACHINE INSTRUCTION'(none, m STI);
end Long_Rate_Div;
procedure Rate_Long_Div(LEFT : Types.RATE_TYPE;
                        RIGHT : Types.LONG_FIXED;
                        RESULT : out Types.RATE_TYPE) is
begin
 MACHINE_INSTRUCTION'(none,m_CLI);
 MACHINE INSTRUCTION'(register system address, m LEA, BX, LEFT'address);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
 MACHINE_INSTRUCTION'(register_address,m_MOV,AX,SS,BX,nil,scale_1,0);
 MACHINE INSTRUCTION'(immediate,m DATAB, 16#66#); -- 32 bit override
 MACHINE INSTRUCTION'(none, m CWD);
                                                  -- performs a CDQ
 MACHINE_INSTRUCTION'(register_immediate,m_MOV,CX,6);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
```

```
MACHINE_INSTRUCTION'(immediate,m_DATAB,16#0F#); -- SHLD EDX,EAX,CL
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#A5#);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#C2#);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
 MACHINE INSTRUCTION'(register_register,m_SHL,AX,CL);
  MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#);
  MACHINE_INSTRUCTION'(system_address,m_IDIV,RIGHT'address);
 MACHINE_INSTRUCTION'(immediate,m_DATAB,16#66#); -- 32 bit override
 MACHINE_INSTRUCTION'(system_address_register,m_MOV,RESULT'address,AX);
  MACHINE_INSTRUCTION'(none,m_STI);
end Rate_Long_Div;
procedure Next Random(CHANNEL TWO VALUE : out Types.WORD INDEX) is
-- | SUBPROGRAM BODY : Machine_Dependent.Next_Random
     This function returns the value of the number at the address of the
- - 1
--| channel two counter. It is assumed that the initialization of that channel
-- is previously performed. Therfore it returns a value between 0 and 255.
--1
begin
  MACHINE_INSTRUCTION'(register_register, m_MOV, CX, CX); -- defeat compiler bug
  MACHINE_INSTRUCTION'(register_immediate,m_MOV,DX,HW_Config.counter_two_addr);
 MACHINE_INSTRUCTION'(register_register,m_IN,AL,DX);
  MACHINE_INSTRUCTION'(register_register,m_XOR,AH,AH);
  MACHINE_INSTRUCTION'(system_address_register,m_MOV,CHANNEL_TWO_VALUE'address,
                       AX);
end Next_Random;
procedure Initialize_Timer_Two is
-- | SUBPROGRAM BODY : Machine_Dependent.Initialize_Timer_Two
-- A procedure used to start the channel 2 counter counting down from
-- | max_timer_value to zero over and over to generate numbers.
- - |
begin
  MACHINE INSTRUCTION'(register immediate,m MOV,DX,
                       HW_Config.timer_control_addr);
  MACHINE INSTRUCTION'(register_immediate,m_MOV,AX,start_countdown);
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
 MACHINE INSTRUCTION'(register_immediate,m_MOV,DX,HW_Config.counter_two_addr);
 MACHINE_INSTRUCTION'(register_immediate,m_MOV,AX,max_timer_value);
  MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
-- The first out byte was the LSB. Now take care of MSB.
```

```
MACHINE_INSTRUCTION'(register_immediate,m_MOV,DX,HW_Config.counter_two_addr);
MACHINE_INSTRUCTION'(register_immediate,m_MOV,AX,max_timer_value);
MACHINE_INSTRUCTION'(register_register, m_OUT, DX, AL);
end Initialize_Timer_Two;
pragma INLINE(Initialize_Timer_Two);

begin
   Initialize_Timer_Two;
end Machine_Dependent;
```

```
--% UNIT:
             Math Package Spec.
-- % Effects: Compute various functions: Tan, Arc Tan, and Sqrt.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: Various.
------
-- | PACKAGE SPEC : Math
--|
--| Math is responsible for implementing the necessary math functions of
-- | the BDS. Some of the transcedental functions are approximations using
--| table look-ups. The fixed point routines which perform basic multiply
--| and divide operations are made visible to replace the runtime supplied
-- | routines which are too general (and thus too slow) for the BDS's needs.
-- The Get_Random_Num function uses the channel two timer to return a
-- | number from 0 to the limit specified.
--1
-- Modifications Log
-- 88-10-09 : TEG => Original created.
-- 89-08-24: MPS => fixed point routines were made visible.
-- 89-09-11 : MPS => Sin, Cos added from intern code.
-- 89-11-08 : MPS => General Power routine added.
-- 89-11-08 : MPS => Arcsin routine added.
-- 89-11-08 : LJG => Arctan routine was made more precise.
-- 89-11-20 : MPS => Get_Random_Num routine moved from Targ_Sup to Math.
with Types:
package Math is
function "*"(LEFT,RIGHT: Types.LONG_FIXED) return Types.LONG_FIXED;
--pragma INLINE("*");
function "/"(LEFT,RIGHT: Types.LONG_FIXED) return Types.LONG_FIXED;
--pragma INLINE("/");
function "/"(LEFT : Types.LONG_FIXED;
            RIGHT: Types.WORD) return Types.LONG_FIXED;
--pragma iNLINE("/");
function "*"(LEFT,RIGHT: Types.METERS) return Types.METERS;
--pragma INLINE(***);
function "/"(LEFT,RIGHT: Types.METERS) return Types.METERS;
```

```
--pragma INLINE("/");
function "/"(LEFT : Types.METERS;
            RIGHT : Types.WORD) return Types.METERS;
--pragma INLINE("/");
function "*"(LEFT,RIGHT: Types.RATE_TYPE) return Types.RATE TYPE;
--pragma INLINE("*");
function "/"(LEFT,RIGHT : Types.RATE_TYPE) return Types.RATE_TYPE;
--pragma INLINE("/");
function "/"(LEFT : Types.RATE_TYPE;
            RIGHT : Types.WORD) return Types.RATE_TYPE;
--pragma INLINE("/");
function "*"(LEFT : Types.RATE_TYPE;
             RIGHT : Types.LONG_FIXED) return Types.RATE_TYPE;
--pragma INLINE("*");
function "*"(LEFT : Types.RATE_TYPE;
            RIGHT : Types.LONG_FIXED) return Types.LONG_FIXED;
--pragma INLINE("*");
function "/"(LEFT : Types.LONG_FIXED;
            RIGHT : Types.RATE_TYPE) return Types.LONG FIXED;
--pragma INLINE("/");
function "/"(LEFT : Types.RATE_TYPE;
            RIGHT : Types.LONG_FIXED) return Types.RATE TYPE;
--pragma INLINE("/");
function Power (BASE, RAISED_TO : Types.LONG_FIXED) return Types.LONG FIXED;
function Sin (ANGLE : Types.BAM) return Types.LONG_FIXED;
function Cos (ANGLE : Types.BAM) return Types.LONG_FIXED;
function Tan (ANGLE : Types.BAM) return Types.LONG_FIXED;
function Arcsin (THETA: Types.LONG_FIXED) return Types.LONG_FIXED; -- in degrees
function Arctan (Z_INPUT : Types.LONG_FIXED) return Types.BAM;
function Sqrt (X : in Types.METERS) return Types.METERS;
function Sqrt (X : in Types.LONG_FIXED) return Types.LONG_FIXED;
function Sqrt (X : in Types.RATE_TYPE) return Types.RATE_TYPE;
function Get_Random_Num (LIMIT : Types.WORD_INDEX) return Types.WORD_INDEX;
```

function Get_Random_Num (LIMIT : Types.METERS) return Types.METERS;
function Get_Random_Num (LIMIT : Types.LONG_FIXED) return Types.LONG_FIXED;
end Math;

```
--% UNIT:
              Math Package Body.
--% Effects: Compute various functions: Tan, Arc Tan, and Sqrt.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: L. Griest.
-- | PACKAGE BODY : Math
--1
     The math package contains the various math routines needed by the BDS.
--| Some of these routines contain simplifications to increase performance.
--| The routines (provided to do fixed point math) are all functions which allow
--| for overloading. Since machine code statements can only be procedures, a
-- call to the appropriate procedure is contained within each function. Since
-- each function and procedure is inlined, the end result should not generate
-- any overhead.
-- Modifications Log
-- 88-10-10 : TEG => Original created.
-- 89-08-24 : MPS => Bodies of fixed point functions created.
-- 89-11-08 : MPS => Arcsin and Power functions added.
-- 89-11-09 : LJG => Arctan function was given greater accuracy.
-- 89-11-20 : MPS => Get Random Num function created.
with Machine Dependent;
with Time_Stamp;
  pragma ELABORATE(Time Stamp, Machine Dependent);
package body Math is
  use Types;
  type FUNC NAME is ( SINE, COSINE);
  subtype FUNC RANGE is Types.WORD range 0..100;
  type FUNC_TABLE is array (FUNC_RANGE, FUNC_NAME) of Types.LONG_FIXED;
  TRIG_FUNC : FUNC_TABLE := ((0.000000,1.000000),(0.015625,1.000000),
                              (0.031250,1.000000),(0.046875,1.000000),
                              (0.062500, 1.000000), (0.078125, 1.000000),
                              (0.093750,1.000000),(0.109375,1.000000),
                              (0.125000, 1.000000), (0.140625, 1.000000),
                               (0.156250, 1.000000), (0.171875, 1.000000),
                              (0.187500,0.984375),(0.203125,0.984375),
                              (0.218750, 0.984375), (0.234375, 0.984375),
                              (0.250000, 0.968750), (0.265625, 0.968750),
                              (0.281250, 0.968750), (0.296875, 0.968750),
                              (0.312500, 0.953125), (0.328125, 0.953125),
```

(0.343750, 0.953125), (0.359375, 0.937500),

```
(0.375000,0.937500),(0.390625,0.937500),
                             (0.406250, 0.921875), (0.421875, 0.921875),
                             (0.437500,0.906250),(0.453125,0.906250),
                             (0.453125, 0.890625), (0.468750, 0.890625),
                             (0.484375,0.875000),(0.500000,0.875000),
                             (0.515625, 0.859375), (0.531250, 0.859375),
                             (0.546875,0.843750),(0.546875,0.843750),
                             (0.562500, 0.828125), (0.578125, 0.828125),
                             (0.593750,0.812500),(0.609375,0.796875),
                             (0.625000, 0.796875), (0.625000, 0.781250),
                             (0.640625, 0.765625), (0.656250, 0.765625),
                             (0.671875, 0.750000), (0.671875, 0.734375),
                             (0.687500, 0.718750), (0.703125, 0.718750),
                             (0.703125,0.703125),(0.718750,0.703125),
                             (0.734375,0.687500),(0.750000,0.687500),
                             (0.750000, 0.671875), (0.765625, 0.656250),
                             (0.765625, 0.640625), (0.781250, 0.625000),
                             (0.796875,0.609375),(0.796875,0.593750),
                             (0.812500, 0.593750), (0.812500, 0.578125),
                             (0.828125, 0.562500), (0.828125, 0.546875),
                             (0.843750, 0.531250), (0.843750, 0.515625),
                             (0.859375, 0.515625), (0.859375, 0.500000),
                             (0.875000,0.484375),(0.875000,0.468750),
                             (0.890625, 0.453125), (0.890625, 0.437500),
                             (0.890625, 0.437500), (0.906250, 0.421875),
                             (0.921875,0.406250),(0.937500,0.390625),
                             (0.937500,0.375000),(0.937500,0.359375),
                             (0.937500,0.343750),(0.953125,0.343750),
                             (0.953125,0.312500),(0.953125,0.296875),
                             (0.953125,0.296875),(0.953125,0.281250),
                             (0.968750,0.250000),(0.984375,0.250000),
                             (0.984375,0.234375),(0.984375,0.218750),
                             (0.984375,0.203125),(0.984375,0.187500),
                             (0.984375, 0.171875), (1.000000, 0.156250),
                             (1.000000,0.140625),(1.000000,0.125000),
                             (1.000000, 0.109375), (1.000000, 0.093750),
                             (1.000000,0.078125),(1.000000,0.062500),
                             (1.000000,0.031250),(1.000000,0.015625),
                             (1.000000,0.000000));
 TAN_TABLE : array(Types.WORD range 0..90) of Types.LONG_FIXED :=
0.00000, 0.01746, 0.03492, 0.05241, 0.06993, 0.08749, 0.10510, 0.12278,
0.14054, 0.15838, 0.17633, 0.19438, 0.21256, 0.23087, 0.24933, C.26795,
0.28675, 0.30573, 0.32492, 0.34433, 0.36397, 0.38386, 0.40403, 0.42447,
0.44523, 0.46631, 0.48773, 0.50953, 0.53171, 0.55431, 0.57735, 0.60086,
0.62487, 0.64941, 0.67451, 0.70021, 0.72654, 0.75356, 0.78129, 0.80978,
0.83910, 0.86929, 0.90040, 0.93252, 0.96569, 1.00000, 1.03553, 1.07237,
1.11061, 1.15037, 1.19175, 1.23490, 1.27994, 1.32704, 1.37638, 1.42815,
1.48256, 1.53986, 1.60033, 1.66428, 1.73205, 1.80405, 1.88073, 1.96261,
```

```
2.05030, 2.14451, 2.24604, 2.35585, 2.47509, 2.60509, 2.74748, 2.90421,
3.07768, 3.27085, 3.48741, 3.73205, 4.01078, 4.33148, 4.70463, 5.14455,
5.67128, 6.31375, 7.11536, 8.14434, 9.51436, 11.43005, 14.30067, 19.08114,
28.63625, 57.28996, Types.sqrt_large_number);
function "*"(LEFT, RIGHT: Types.LONG FIXED) return Types.LONG FIXED is
                : Types.LONG_FIXED;
 MULTIPLICAND 1 : Types.LONG FIXED := LEFT;
  MULTIPLICAND_2 : Types.LONG_FIXED := RIGHT;
  Machine_Dependent.Long_Long_Mul(MULTIPLICAND_1,MULTIPLICAND_2,RESULT);
  return RESULT;
end "*":
function "/"(LEFT,RIGHT: Types.LONG_FIXED) return Types.LONG_FIXED is
 QUOTIENT : Types.LONG FIXED;
 DIVIDEND : Types.LONG_FIXED := LEFT;
  DIVISOR : Types.LONG_FIXED := RIGHT;
  Machine_Dependent.Long_Long_Div(DIVIDEND,DIVISOR,QUOTIENT);
  return QUOTIENT;
end "/";
function "/"(LEFT : Types.LONG_FIXED;
             RIGHT: Types.WORD) return Types.LONG_FIXED is
  QUOTIENT : Types.LONG_FIXED;
 DIVIDEND : Types.LONG FIXED := LEFT;
 DIVISOR : Types.WORD
                            := RIGHT;
begin
  Machine_Dependent.Long_Word_Div(DIVIDEND,DIVISOR,QUQTIENT);
  return QUOTIENT;
end "/";
function "*"(LEFT, RIGHT: Types.METERS) return Types.METERS is
                : Types.METERS;
  MULTIPLICAND 1 : Types.METERS := LEFT;
  MULTIPLICAND_2 : Types.METERS := RIGHT;
  Machine_Dependent.Meters_Meters_Mul(MULTIPLICAND_1,MULTIPLICAND_2,RESULT);
  return RESULT;
end "*":
function "/"(LEFT,RIGHT: Types.METERS) return Types.METERS is
  QUOTIENT : Types.METERS;
 DIVIDEND : Types.METERS := LEFT;
 DIVISOR : Types.METERS := RIGHT;
  Machine Dependent.Meters_Meters_Div(DIVIDEND,DIVISOR,QUOTIENT);
  return QUOTIENT;
end "/";
```

```
function "/"(LEFT : Types.METERS;
            RIGHT: Types.WORD) return Types.METERS is
 QUOTIENT : Types.METERS;
 DIVIDEND : Types METERS := LEFT;
 DIVISOR : Types.WORD := RIGHT;
begin
 Machine Dependent.Meters Word Div(DIVIDEND, DIVISOR, QUOTIENT);
  return QUOTIENT:
end "/";
function "*"(LEFT,RIGHT: Types.RATE TYPE) return Types.RATE TYPE is
               : Types.RATE_TYPE;
 MULTIPLICAND 1 : Types.RATE TYPE := LEFT;
  MULTIPLICAND_2 : Types.RATE_TYPE := RIGHT;
  Machine Dependent.Rate Rate Mul(MULTIPLICAND 1, MULTIPLICAND 2, RESULT);
  return RESULT;
end "*":
function "/"(LEFT,RIGHT : Types.RATE_TYPE) return Types.RATE_TYPE is
 QUOTIENT : Types.RATE TYPE;
 DIVIDEND : Types.RATE_TYPE := LEFT;
 DIVISOR : Types.RATE_TYPE := RIGHT;
  Machine Dependent.Rate Rate Div(DIVIDEND, DIVISOR, QUOTIENT);
 return QUOTIENT:
end "/":
function "/"(LEFT : Types.RATE_TYPE;
            RIGHT: Types.WORD) return Types.RATE_TYPE is
 QUOTIENT : Types.RATE_TYPE;
 DIVIDEND : Types.RATE_TYPE := LEFT;
 DIVISOR : Types.WORD
                           := RIGHT;
begin
  Machine Dependent.Rate Word Div(DIVIDEND, DIVISOR, QUOTIENT);
  return QUOTIENT;
end "/";
function "/"(LEFT : Types.LONG FIXED;
            RIGHT: Types.RATE_TYPE) return Types.LONG FIXED is
 QUOTIENT : Types.LONG FIXED:
 DIVIDEND : Types.LONG FIXED := LEFT;
 DIVISOR : Types.RATE_TYPE := RIGHT;
begin
 Machine_Dependent.Long_Rate_Div(DIVIDEND,DIVISOR,QUOTIENT);
  return QUOTIENT;
end "/";
function "/"(LEFT : Types.RATE_TYPE;
             RIGHT : Types.LONG_FIXED) return Types.RATE_TYPE is
 QUOTIENT : Types.RATE_TYPE;
```

```
DIVIDEND : Types.RATE_TYPE := LEFT;
 DIVISOR : Types.LONG_FIXED := RIGHT;
begin
  Machine_Dependent.Rate_Long_Div(DIVIDEND,DIVISOR,QUOTIENT);
  return QUOTIENT;
end "/";
function "*"(LEFT : Types.RATE TYPE;
             RIGHT: Types.LONG_FIXED) return Types.LONG_FIXED is
  RESULT
                : Types.LONG_FIXED;
 MULTIPLICAND_1 : Types.RATE_TYPE := LEFT;
  MULTIPLICAND 2 : Types.LONG_FIXED := RIGHT;
begin
  Machine_Dependent.Rate_Long_Long_Mul(MULTIPLICAND_1,MULTIPLICAND_2,RESULT);
  return RESULT:
end "*";
function "*"(LEFT : Types.RATE_TYPE;
             RIGHT: Types.LONG_FIXED) return Types.RATE TYPE is
  RESULT
                : Types.RATE_TYPE;
  MULTIPLICAND_1 : Types.RATE_TYPE := LEFT;
  MULTIPLICAND_2 : Types.LONG_FIXED := RIGHT;
  Machine_Dependent.Rate_Long_Rate_Mul(MULTIPLICAND_1,MULTIPLICAND_2,RESULT);
  return RESULT;
end "#";
function Power(BASE, RAISED_TO: Types.LONG_FIXED) return Types.LONG_FIXED is
            : Types.LONG FIXED := 1.0;
  OLD_RESULT : Types.LONG_FIXED;
  for I in 1..Types.WORD(RAISED_TO) loop
    OLD_RESULT := RESULT;
    RESULT := RESULT * BASE;
    if RESULT = OLD RESULT then
                                          -- if no change, don't waste time
      exit;
    end if;
  end loop;
  return RESULT;
end Power;
function Sin (ANGLE : Types.BAM) return Types.LONG_FIXED is
-- | SUBPROGRAM BODY : Math.Sin
--| Sin is a function which takes an angle in Binary Angle
--! Measurements and uses a table lookup to find the corresponding result.
-- It returns the sin of the ANGLE in the Types.LONG_FIXED type.
--1
```

```
NEGAT : Types.WORD := 1;
ANGLE2 : Types.WORD := Types.WORD(ANGLE);
TEMP : Types.LONG_FIXED;
begin
  if ANGLE2 < 0 then
     ANGLE2 := abs(ANGLE2);
            := - 1:
     NEGAT
  end if;
  if ANGLE2 > 16384 then
     TEMP := TRIG_FUNC(Types.WORD((32767 - ANGLE2)/163),SINE);
     return TEMP * Types.LONG_FIXED(NEGAT);
  else
     TEMP := TRIG_FUNC(Types.WORD(ANGLE2/163),SINE);
     return TEMP * Types.LONG_FIXED(NEGAT);
  end if:
end Sin;
function Cos (ANGLE : Types.BAM) return Types.LONG_FIXED is
--1
-- | SUBPRCGRAM BODY : Math.Cos
--{
--| Cos is a function which takes an angle in Binary Angle
-- | Measurements and uses a table lookup to find the corresponding result.
-- | The result is returned in the Types.LONG_FIXED type.
--1
ANGLE2 : Types.WORD := Types.WORD(ANGLE);
begin
  ANGLE2 := abs(ANGLE2);
  if ANGLE2 > 16384 then
     return (-1) * TRIG .UNC(Types.WORD((32767-ANGLE2)/163),COSINE):
  else
     return TRIG_FUNC(Types.WORD(Angle2/163),COSINE);
  end if;
end Cos;
function Tan (ANGLE : Types.BAM) return Types.LONG_FIXED is
-- | SUBPROGRAM BODY : Math.Tan
--| Tan is the tangent function which takes an angle in Binary Angle
--| Measurements and uses a table lookup to find the corresponding result.
  TANGENT
             : Types.LONG FIXED;
             : Types.WORD;
  THETA
```

```
begin
  Time_Stamp.Log(0048);
                          --$TP(0048) Math.Tan start
  THETA := Types.WORD(ANGLE/182);
                                      -- approx. 182 bams per degree
  if THETA >= -90 and THETA <= 90 then
    if THETA >= 0 then
     TANGENT := TAN_TABLE(THETA);
     TANGENT := -TAN_TABLE(-THETA);
    end if;
  elsif THETA < -90 then
    TANGENT := TAN_TABLE(THETA + 180);
    TANGENT := -TAN_TABLE(180-THETA);
  end if;
  Time_Stamp.Log(0049);
                         --$TP(0049) Math.Tan end
  return TANGENT;
end Tan;
function Sqrt(X : in Types.METERS) return Types.METERS is
--1
-- | SUBPROGRAM BODY : Math.Sqrt
--| Sqrt returns the square root of a number. This routine will exit when
-- the approximation of the square is close to the previous result. This
--| prevents unneeded looping for accuracy.
--|
  use Types; -- import operators
  F : Types.METERS := X;
  Y : Types.METERS := 1.0;
  OLD_Y : Types.METERS := Y;
begin
  Time_Stamp.Log(0050); --$TP(0050) Math.Sqrt start (METERS)
  if X = 0.0 then
    return F;
  end if;
  for I in 1..15 loop
    exit when Y = 0.0;
    Y := (Y + F/Y) / Types.WORD(2);
    if Y = OLD_Y then
     exit;
    end if:
    OLD_Y := Y;
  end loop;
                         --$TP(0051) Math.Sqrt end (METERS)
  Time_Stamp.Log(0051);
  return Y;
end Sqrt;
function Sqrt(X : in Types.LONG_FIXED) return Types.LONG_FIXED is
```

```
use Types; -- import operators
  F : Types.LONG_FIXED := X;
  Y : Types.LONG_FIXED := 1.0;
  OLD_Y : Types.LONG_FIXED := Y;
begin
  Time_Stamp.Log(0052); --$TP(0052) Math.Sqrt start (LONG_FIXED)
  if X = 0.0 then
    return F;
  end if;
  for I in 1..15 loop
    exit when Y = 0.0;
    Y := (Y + F/Y) / Types.WORD(2);
   if Y = OLD_Y then
     exit;
    end if;
   OLD_Y := Y;
  end loop;
  Time_Stamp.Log(0053); --$TP(0053) Math.Sqrt end (LONG_FIXED)
  return Y;
exception
  when NUMERIC_ERROR => Y := OLD_Y;
                       return Y;
end Sqrt;
-- for RATE TYPE
function Sqrt(X : in Types.RATE_TYPE) return Types.RATE_TYPE is
  use Types; -- import operators
  F : Types.RATE_TYPE := X;
  Y : Types.RATE_TYPE := 1.0;
  OLD_Y : Types.RATE_TYPE := Y;
begin
  Time_Stamp.Log(0116); --$TP(0116) Math.Sqrt start (RATE_TYPE)
  if X = 0.0 then
   return F;
  end if;
  for I in 1..15 loop
    exit when Y = 0.0;
   Y := (Y + F/Y) / Types.WORD(2);
    if Y = OLD_Y then
      exit;
    end if;
    OLD_Y := Y;
  end loop;
  Time_Stamp.Log(0117); --$TP(0117) Math.Sqrt end (RATE_TYPE)
  return Y;
exception
  when NUMERIC_ERROR => Y := OLD_Y;
                       return Y;
end Sqrt;
```

```
--|
-- | SUBPROGRAM BODY : Math.Arcsin
---
--| Arcsin uses an appoximation method of series expansion in order to achieve
--| it's results. Simple constants are named to improve readability. The
--- constants are not fully divided out beforehand to increase accuracy, but
--! to improve speed, the constants could be divided beforehand and constants
-- | used to multiply the TERMs.
function Arcsin(THETA: Types.LONG_FIXED) return Types.LONG_FIXED is
  rad to deg : constant Types.LONG_FIXED := 57.296875; -- 180 / pi
           : constant Types.LONG_FIXED := 3.0;
           : constant Types.LONG_FIXED := 6.0;
  six
            : constant Types.LONG_FIXED := 5.0;
  five
  seven
            : constant Types.LONG FIXED := 7.0;
  fifteen : constant Types.LONG_FIXED := 15.0;
            : constant Types.LONG FIXED := 40.0;
  three_thirty_six : constant Types.LONG_FIXED := 336.0;
            : Types.LONG_FIXED;
  TERM1
  TERM2
            : Types.LONG_FIXED;
            : Types.LONG_FIXED;
  TERM3
           : Types.LONG_FIXED;
  TERM4
  RESULT : Types.LONG_FIXED;
begin
  TERM1 := THETA;
  TERM2 := Power(THETA, three);
  TERM2 := TERM2 / six;
  TERM3 := three * Power(THETA, five);
  TERM3 := TERM3 / forty;
  TERM4 := fifteen * Power(THETA, seven);
  TERM4 := TERM4 / three_thirty_six;
  RESULT := (TERM1 + TERM2 + TERM3 + TERM4) * rad_to_deg;
  return RESULT;
end Arcsin;
function Arctan(Z INPUT : Types.LONG_FIXED) return Types.BAM is
--|
-- | SUBPROGRAM BODY : Math.Arctan
--|
-- A function used to return an approximation of the arctangent function.
-- | Using the Taylor series expansion:
--| arctan z = z - (z**3/3) + (z**5/5) - (z**7/7) + ... (|z| <= 1 and z**2 /= -1)
-- | carried out for two terms (initially).
--1
            : Types.LONG_FIXED;
Z_CUBED
```

```
QUOTIENT
            : Types.LONG_FIXED;
ARCTAN_Z
            : Types.LONG_FIXED;
CONV_FACTOR : constant Types.LONG_FIXED := 10430.38; -- Radians to BAMS
         : Types.LONG_FIXED;
ARCTAN_Z_BAMS : Types.BAM;
begin
 Z_CUBED := Z_INPUT * Z_INPUT; --actually z**2
 Z_CUBED := Z_CUBED * Z_INPUT; -- z**3
 QUOTIENT := Z_CUBED / Types.WORD(3);
 ARCTAN_Z := Z_INPUT - QUOTIENT;
 TEMP := ARCTAN_Z * CONV_FACTOR;
 ARCTAN_Z_BAMS := Types.BAM(TEMP);
  return ARCTAN_Z_BAMS;
end Arctan;
function Get Random Num(LIMIT : Types.METERS) return Types.METERS is
--| SUBPROGRAM BODY : Math.Get_Random_Num
--| This function returns a psuedo random number to the caller. It has
-- | three forms returning three different types for the convenience of
-- | Simulate. Sensor. The random number is received from the channel two
--| counter. Therefore the number returned from the
--| Machine_Dependent.Next_Random call is between 0 and Machine Dependent
-- | max_timer_value. This number is then multiplied by the parameter
--| LIMIT, and, returned in the type of LIMIT, is a random number from
-- | 0 to LIMIT.
--1
  VALUE : Types.RATE_TYPE;
  TEMP : Types.WORD_INDEX;
begin
  Machine_Dependent.Next_Random(TEMP);
  VALUE := Types.RATE_TYPE(TEMP) / Types.WORD(
                                   Machine_Dependent.max_timer_value);
  VALUE := VALUE * Types.RATE_TYPE(LIMIT);
  return Types.METERS(VALUE);
end Get_Random_Num;
function Get_Random_Num(LIMIT : Types.LONG_FIXED) return Types.LONG_FIXED is
  VALUE : Types.RATE_TYPE;
  TEMP : Types.WORD_INDEX;
  Machine_Dependent.Next_Random(TEMP);
  VALUE := Types.RATE_TYPE(TEMP) / Types.WORD(
                                   Machine_Dependent.max_timer_value);
  VALUE := VALUE * LIMIT;
  return Types.LONG_FIXED(VALUE);
end Get_Random_Num;
```

```
--% UNIT:
            Mouse Package Spec.
--% Effects: Provides graphics pointing device interrupt handling.
-- X Modifies: Status Mode, and Mouse_Buffer X-Y positions are updated. --
--% Requires: Runtime initialization of interrupt vector.
-- % Raises: Task will terminate on MOUSE_ERROR.
--% Engineer: M. Sperry.
-- | PACKAGE SPEC : Mouse
--|
--| In addition to establishing communications with the mouse, a task is
-- provided which handles the receive interrupt generated by the mouse at
-- | COM2. This task has the pragma INTERRUPT_HANDLER and special restrictions
-- apply to it's communication facilities in order to guarantee a good response
-- | time.
--|
-- Modifications Log
-- 88-09-30 : MP$ => Original created.
with System;
package Mouse is
procedure Initialize;
task Char_In is
 pragma INTERRUPT_HANDLER;
 entry REPORT;
 for REPORT use at (16#83#,0);
                                           -- COM2 8250 serial port vector
end Char_in;
end Mouse;
```

```
Mouse Package Body.
--% Effects: Provides graphics pointing device interrupt handling.
--% Modifies: Status Mode, and Mouse_Buffer X-Y positions are updated. --
--% Requires: Runtime initialization of interrupt vector.
--% Raises: Task will terminate on MOUSE ERROR.
--% Engineer: M. Sperry.
------
-- PACKAGE BODY : Mouse
--|
--| The Mouse package implements the routines needed for control of a
-- | mouse. There is an initialization procedure which sets up the mouse
-- | for 4800 baud, no parity, 7 data bits, and two stop bits. There is
-- also an interrupt entry task which takes data from the mouse and
-- if a complete report is generated, gives that data to the Mouse_Buffer
-- task.
--|
-- Modifications Log
-- 88-09-30 : MPS => Original created.
-- 89-08-08 : MPS => Made all references to hardware in one package.
-- 89-12-06 : TEG => Shut off mouse interrupts during init. Added support
                   for Restarting the system using RESET&MODE buttons.
with Types;
with Low_Level_10;
with Debug_IO;
with Mouse_Buffer;
                             -- provides constants and data structures
with Mouse Data;
with Status;
with Interrupt Control;
with Time Stamp;
with HW Config;
with Distrib;
use Low_Level_IO;
use Mouse Data;
                              -- visibility to "and" function
pragma ELABORATE(Low Level_10, Debug_10, Mouse_Buffer, Status, Time Stamp);
package body Mouse is
DATA
                : Low_Level_IO.BYTE;
                                              -- char from mouse
BUTTON PUSHED : Mouse_Data.BIT_FIELD;
                                              -- array representing keys
STATUS BYTE
               : Mouse_Data.BIT_FIELD;
                                               -- represents status errors
PREV BUTTON PUSH : Mouse_Data.BIT_FIELD := (others => FALSE); --previous buttons
MOUSE INPUT
               : Mouse_Data.RAW_MOUSE_WORD := (0,0,0);-- transform to 12-bit
```

```
MOUSE REPORT
                : Mouse_Data.SIGNED_MOUSE_WORD; -- transformation to signed
               : Types.WORD range 0..5 := 0; -- counts byte in report
REPORT_COUNT
CHANGE_REQUESTED : BOOLEAN := FALSE;
                                                -- rendezvous with status?
MOUSE ERROR
              : EXCEPTION;
TEMP X
                : Types.WORD;
                                                -- local copy of X motion
TEMP Y
                : Types.WORD;
                                               -- local copy of Y motion
procedure Initialize is
·-| SUBPROGRAM BODY : Mouse.Initialize
--1
--| Initialize sets up the mouse at 4800 baud, no parity, 7 data bits, and
-- two stop bits. The number of stop bits is insignificant. There should
-- only be two formats that the mouse can be in, either relative bit pad one
-- or Microoft Mouse. The default on power up for the mouse is MM at 4800.
--- The mouse must be commanded in the following order: BAUD (which is set to
-- default to 4800 so it is not necessary to reprogram it), # of reports/sec.,
--| and then format of the reports. The mouse used is a Logitech Serial Mouse
--| as described in hwconfig.as. The mouse is programmed with Relative Bit
-- | Pad One format which has five bytes of data associated with it.
--|
 INTERRUPTS : Low_Level_IO.BYTE;
                                             -- for input of 8259 ints
  RESPONSE : Low_Level_IO.BYTE;
                                             -- for mouse responses
 TIME_OUT : INTEGER := 30000;
                                             -- time out for mouse response
begin
-- Disable receive interrupts
 Send_Control(HW_Config.COM2_int_enable, Mouse_Data.specific_int_disable);
 Receive Control(HW Config.COM2 status, RESPONSE); -- clean out junk in status
 Receive_Control(HW_Config.COM2_data,RESPONSE); -- clean out junk in data
 Send_Control(HW_Config.COM2_control,Mouse_Data.access_baud);
 Send Control(HW_Config.COM2 data, Mouse Data.host baud); -- set BAUD = 4800
-- set COM2 serial parameters
 Send Control(HW_Config.COM2 control, Mouse Data.host format);
 Send_Control(HW_Config.COM2_data,Mouse_Data.acknowledge); -- wakeup mouse
   Receive Control(HW Config.COM2 status, RESPONSE); -- wait for response
   if RESPONSE = Mouse Data.data new then
     Receive_Control(HW_Config.COM2_data, RESPONSE); -- clear out byte
     exit;
   else
     TIME OUT := TIME_OUT - 1;
   end if:
   if TIME OUT = 0 then
     exit;
   end if;
 end loop;
 if TIME_OUT = 0 then
   Debug_IO.Put_Line("Unable to establish communications with mouse.");
```

```
end if:
 Send_Contro.(HW_Config.COM2_data,Mouse_Data.mouse_char_speed);
                                                 -- slow for mouse input buffer
 Send_Control(HW_Config.COM2_data,Mouse_Data.mouse_format);
 Send_Control(HW_Config.COM2_modem_control, Mouse_Data.general_int_enable);
 Send_Control(HW Config.COM2 int_enable, Mouse_Data.specific int enable);
 Receive_Control(HW_Config.pic_8259_mr,INTERRUPTS);
-- enable COM2 in PIC in line below
 INTERRUPTS := Mouse_Data.Bits_to_Byte
             (Mouse_Data.Byte_to_Bits(INTERRUPTS) and Mouse Data.pic and mask);
 Send_Control(HW_Config.pic_8259_mr,INTERRUPTS);
end Initialize;
task body Char_In is
-- TASK BODY : Mouse.Char In
--| One of the main tasks used to move the reticle around the battlefield
--| screen. The task rendezvous with the graphics task reporting positions
--| every 28 milliseconds, unless the middle button is pressed (MODE) changing
-- the mode to AUTOMATIC. In this event, the mouse simply waits for a change
--- to MANUAL, since automatic mode is controlled by the rocket task. The mouse
--- task will not rendezvous with the graphics task until set to MANUAL. When
--| in MANUAL mode, the task (upon completion of one report) will rendezvous
-- with the graphics task at high priority to report it's position. It will
-- | then change the status task's shared variables if any need to be changed.
-- If one does, and the status task has completed it's previous work and gone
--| to an accept state, then the mouse task wakes it up. Because the mouse
--| is programmed with Relative Bit Pad One format and needs five bytes of data
-- | in order to complete its report, after the first byte has come in, it is
-- only 2 milliseconds until the next byte comes in until five bytes have been
--| received. Then there is a gap of 18 milliseconds until the next byte will
--| be seen (assuming constant motion of the mouse). This is why there is
-- | very little processing of data until the fifth byte. It is entirely possible
-- | because the mouse is an asychronous device that up to three reports may be
--| generated and handled in one interval. This worst case must be accounted
-- for in timing considerations.
--|
use Status:
                                                -- for visibility to "="
                                                -- for visibility to "+"
use Types;
beain
 loop
   accept Report do
      -- $TP(0056) Mouse task start
     Receive_Control(HW_Config.COM2_status,DATA); -- receive status
     STATUS BYTE := Mouse Data.Byte to Bits(DATA); -- check statusbyte for errors
      if STATUS_BYTE(Mouse_Data.overflow) or
```

```
STATUS_BYTE(Mouse_Data.framing) then
       REPORT COUNT := 0;
                                                    -- start a new report
       Receive_Control(HW_Config.COM2_data,DATA); -- clear out data port
     else
       Receive_Control(HW_Config.COM2_data,DATA); -- get valid data
        if DATA > Mouse_Data.sync_byte then
                                                  -- check for new report
         REPORT_COUNT := 1;
                                                -- start of new report
       end if;
     end if;
     case REPORT_COUNT is
                                                 -- convert data to mouse X.Y
       when 1 =>
                                                 -- or buttons.
         BUTTON_PUSHED := Mouse_Data.Byte_to_Bits(DATA);
         REPORT_COUNT := REPORT_COUNT + 1;
        when 2 =>
         MOUSE_INPUT.LOW := Mouse_Data.Byte_to_Bit6(DATA);
         REPORT_COUNT := REPORT_COUNT + 1;
        when 3 =>
          MOUSE_INPUT.HIGH := Mouse_Data.Byte_to_Bit6(DATA);
         MOUSE_REPORT := Mouse_Data.Raw_to_Signed(MOUSE_INPUT);
         TEMP_X := MOUSE_REPORT.LOW12;
         REPORT COUNT := REPORT COUNT + 1;
        when 4 =>
         MOUSE_INPUT.LOW := Mouse Data.Byte to Bit6(DATA);
         REPORT_COUNT := REPORT COUNT + 1;
        when 5 =>
          -- don't move mouse if any buttons pushed.
          if (not BUTTON_PUSHED(Mouse_Data.reset)) and -- guarantee only one -
             (not BUTTON_PUSHED(Mouse_Data.mode)) and-- rendezvous per report
             (not BUTTON_PUSHED(Mouse_Data.launch)) then -- (RTE bug) -
            PREV_BUTTON_PUSH(Mouse_Data.reset) := FALSE;
            PREV BUTTON PUSH(Mouse Data.mode) := FALSE;
            PREV_BUTTON_PUSH(Mouse_Data.launch) := FALSE;
            MOUSE_INPUT.HIGH := Mouse_Data.Byte_to_Bit6(DATA);
            MOUSE_REPORT := Mouse_Data.Raw_to_Signed(MOUSE_INPUT);
            TEMP_Y := MOUSE_REPORT.LOW12;
            if Status.MODE = Status.MANUAL then
              MOUSE_BUFFER.MOUSE X := TEMP X;
              MOUSE_BUFFER.MOUSE Y := TEMP Y;
              --$TP(0057) Mouse rendezvous with Save start
                      -- must be conditional to work in INTERRUPT_HANDLER
                Mouse_Buffer.Save.Reticle_Motion;
                -- $TP(0058) Mouse rendezvous with Save end
             else
                null:
              end select;
            end if;
         else
-- A BUTTON IS DEPRESSED. FIRST LOOK AT "RESET" BUTTON
            if BUTTON_PUSHED(Mouse_Data.reset) and
```

```
not PREV BUTTON PUSH(Mouse Data.reset) then
              for I in Status.RESET_STATUS_TYPE loop
                Status.STATUS_CONTROL(I).DATA := 0;
                Status.STATUS_CONTROL(I).DISPLAYED := FALSE;
              end loop;
              Status.REQ COUNT := Status.REQ_COUNT + 1;
              CHANGE REQUESTED := TRUE;
              PREV_BUTTON_PUSH(Mouse_Data.reset) := TRUE;
              PREV_BUTTON_PUSH(Mouse_Data.reset) := FALSE;
            end if;
-- NOW LOOK AT MODE BUTTON...
-- When the MODE button is pushed, check to see if the RESET button is
-- currently active. If so, then do a system reset!
            if BUTTON_PUSHED(Mouse_Data.mode) then
              if BUTTON_PUSHED(Mouse_Data.reset) then
                Distrib.Restart;
                                                   -- perform system shutdown
              elsif not PREV_BUTTON_PUSH(Mouse_Data.mode) then
                if Status.MODE = Status.MANUAL then -- Change mode
                  Status.MODE := Status.AUTOMATIC;
                else
                  Status.MODE := Status.MANUAL;
                end if;
                Status.MODE_DISPLAYED := FALSE;
                Status.REQ COUNT := Status.REQ COUNT + 1;
                CHANGE REQUESTED := TRUE;
                PREV_BUTTON_PUSH(Mouse_Data.mode) := TRUE;
              end if;
            else
              PREV_BUTTON_PUSH(Mouse_Data.mode) := FALSE;
            end if;
-- FINALLY, LOOK AT LAUNCH BUTTON
            if BUTTON PUSHED (Mouse Data.launch) and
               not PREV_BUTTON_PUSH(Mouse_Data.launch) then
              if Status.MODE = Status.MANUAL then
                Mouse_Buffer.LAUNCH := TRUE;
                Mouse_Buffer.NEW_ABS_X := Mouse_Buffer.OLD_ABS_X;
                Mouse Buffer.NEW ABS_Y := Mouse_Buffer.OLD ABS Y;
              end if;
              PREV_BUTTON_PUSH(Mouse_Data.launch) := TRUE;
              if not BUTTON PUSHED (Mouse Data.launch) then
                PREV_BUTTON_PUSH(Mouse_Data.launch) := FALSE;
              end if:
            end if;
            if CHANGE_REQUESTED and then Status.REQ_COUNT = 1 then
              -- $TP(0059) Mouse rendezvous with Status start
```

```
select
               Status.Update.Signal;
               --$TP(0060) Mouse rendezvous with Status end
             el se
               null;
             end select;
           end if;
         end if;
         CHANGE_REQUESTED := FALSE;
         REPORT_COUNT := 0;
       when others => null;
     Send_Control(HW_Config.pic_8259,Mouse_Data.spec_eoi); -- specific EoI
     --$TP(0061) Mouse task end
   end Report;
 end loop;
end Char_In;
end Mouse;
```

```
Mouse_Buffer Package Spec.
--% Effects: Buffers mouse data input, translates it to pixel system. --
--% Modifies: No global data is modified (other than in own spec).
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
-- % Engineer: M. Sperry.
-----
-- | PACKAGE SPEC : Mouse_Buffer
--| Package Mouse_Buffer contains a task called Save which is responsible
--- for saving reports of mouse movement via a rendezvous with an interrupt
-- | task. The task then rendezvous with the display task to relocate the
--| reticle. The shared variables of the X and Y positions as well as the
-- | launch flag are contained here. The mode flag is contained in the
-- status package specification.
--1
-- Modifications Log
-- 88-10-24 : MPS => Original created.
with Types;
with Config;
package Mouse_Buffer is
stack_size : constant := 118; -- in bytes
MOUSE X
          : Types.WORD;
                        -- for use with the Save task in Mouse_Buffer
MOUSE Y
        : Types.WORD;
                        -- for use with the Save task in Mouse_Buffer
LAUNCH
         : BOOLEAN := FALSE;
                        -- absolute X position of Reticle on Screen
OLD_ABS_X : Types.WORD;
OLD_ABS Y : Types.WORD;
                        -- и у и и
NEW_ABS_X : Types.WORD;
                        -- for use by ENGAGE (latched values by Mouse pkg)
NEW_ABS_Y : Types.WORD;
task type Save_Type is
 entry Reticle_Motion;
 pragma PRIORITY(Config.save_priority);
end Save_Type;
for Save_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                                        stack_size);
          : Save Type;
                        -- for saving motion of mouse to display
Save
end Mouse_Buffer;
```

```
--% UNIT:
             Mouse_Buffer Package Body.
--% Effects: Buffers mouse data input, translates it to pixel system. --
--% Modifies: No global data is modified (other than in own spec).
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: M. Sperry.
.........
--| PACKAGE BODY : Mouse Buffer
     Package body Mouse Buffer is responsible for the implementation of the
--| buffering between the mouse interrupt routine and the screen. Note that
-- checks are performed to be sure that the reticle is within the screen
--| defined by Config. Also, note that the Y coordinate is reversed because
-- the screen on the EGA runs (in the Y direction) from 0 to 349 starting
-- | from the upper left and moving down, i.e., the mouse has Y direction as
--| positive moving up, and the EGA has Y positive moving down.
-- Modifications Log
-- 88-10-24 : MPS => Original created.
with Shapes;
with Graphics;
with Config;
with Debug_IO;
with Interrupt_Control;
with Time Stamp;
pragma ELABORATE(Debug_IO, Graphics, Interrupt_Control, Time_Stamp);
package body Mouse_Buffer is
use Types;
                                               -- neeued for visibility to '+'
task body Save_Type is
list_len
            : constant := 1;
left_limit : constant := Config.battlefield_screen_left;
right_limit : constant := Config.battlefield_screen_right;
top limit : constant := Config.battlefield_screen top;
bottom_limit : constant := Config.battlefield_screen_bottom;
PRIORITY
            : Graphics.PRIORITY_TYPE := Graphics.HIGH;
           : Graphics.MOVE_LIST_TYPE(list_len .. list_len);-- 1 item (reticle)
WORK_LIST
TEMP X
            : Types.WORD;
```

```
TEMP_Y
            : Types.WORD;
begin
-- Initial display of reticle
  WORK_LIST(list_len).XY_OLD := (Config.battlefield_center_x, Config.battlefield_center_y);
 WORK_LIST(list_len).XY_NEW := (Config.battlefield_center_x, Config.battlefield_center_y);
  WORK_LIST(list_len).OBJECT := Shapes.RETICLE;
  WORK_LIST(list_len).COLOR := Graphics.reticle_color;
  Graphics.Display.Move(PRIORITY, WORK_LIST);
  loop
    begin
                            -- exception block
      Time Stamp.Log(0062);
                              --$TP(0062) Mouse Buffer task and accept
      accept Reticle_Motion;
-- Get new positions of reticle (mouse)
      Interrupt_Control.Disable;
      TEMP_X := WORK_LIST(list_len).XY_OLD.X + MOUSE_X;
      TEMP_Y := WORK_LIST(list_len).XY_OLD.Y - MOUSE_Y;
     Interrupt_Control.Enable;
     Check bounds of reticle; don't let it go past edge of battlefied screen.
     if (TEMP X + Shapes.RETICLE_LEFT) < left_limit then
       TEMP_X := left_limit - Shapes.RETICLE_LEFT;
      elsif (TEMP_X + Shapes.RETICLE_RIGHT) > right_limit then
        TEMP_X := right_limit - Shapes.RETICLE_RIGHT;
      end if;
      if (TEMP Y + Shapes.RETICLE TOP) < top_limit then
        TEMP_Y := top_limit - shapes.reticle_top;
      elsif (TEMP_Y + Shapes.RETICLE_BOTTOM) > bottom_limit then
        TEMP_Y := bottom_limit - Shapes.RETICLE_BOTTOM;
      end if;
      WORK_LIST(list_len).XY_NEW.X := TEMP_X;
      WORK_LIST(list_len).XY_NEW.Y := TEMP_Y;
   update global accessable values
      Interrupt_Control.Disable;
      OLD ABS X := TEMP X;
      OLD_ABS_Y := TEMP_Y;
      Interrupt_Control.Enable;
                                -- $TP(0063) Mouse_Buffer rendezvous with Graphics start
      lime Stamp.Log(0063);
      Graphics.Display.Move(PRIORITY, WORK_LIST);
```

```
Time_Stamp.Log(0064); --$TP(0064) Mouse_Buffer rendezvous with Graphics end

WORK_LIST(list_len).XY_OLD := WORK_LIST(list_len).XY_NEW;
exception
    when others =>
        Debug_IO.Put_Line("Error in Save");
end; -- exception block
Time_Stamp.Log(0065); --$TP(0065) Mouse_Buffer task end
end toop;
end Save_Type;
end Mouse_Buffer;
```

```
Mouse Data Specification.
--% Effects: Provides relevant data structures and constants.
--% Modifies: Nothing.
--% Requires: Nothing.
--% Raises: Nothing.
--% Engineer: M. Sperry.
______
-- PACKAGE SPEC : Mouse_Data
--|
--| Package Mouse_Data provides the data structures and constants necessary
--| to intialize and run a Logitech C7 serial mouse at 4800 baud, no parity, 7
-- | data bits, and two stop bits. The Logitech mouse is capable of 8 different
--- formats. Relative Bit Pad One is chosen here because it allows twelve
--| bits of motion data for each report. Although this creates more work in
-- | the processing of each byte of data (there are five bytes of data in each
-- | report) there is more accuracy in the pointing device.
--| The mouse controls the movement of the reticle (defined in the Graphics
--| package) by receiving a report, generating the motion in X and Y
--| coordinates, and sending these values to package Mouse_Buffer for
--| processing (task Save).
The reports come in 28 msecs apart with a 2 msec interval between each
--| byte of the report. A report consists of the following in Relative Bit Pad
--| One:
--|
             5 4 3 2 1 0 = bit number
--| P
-- np 1
             0 L
                          R
                               0
                                    0 Byte 1 (minimum value = 64)
                      М
             X5 X4 X3
                         X2 X1 X0 Byte 2
--| np 0
             X11 X10 X9 X8 X7 X6 Byte 3
--| np 0
             Y5 Y4 Y3
                          Y2 Y1 Y0 Byte 4
             Y11 Y10 Y9 Y8 Y7 Y6 Byte 5
--| np 0
-- \- no parity
-- | L,M,R above stands for Left, Middle, and Right buttons; 1 = key pressed.
--1
    The mouse is located at COM2 on an AT which is base address 2F8 (hex).
--|
-- Modifications Log
-- 89-04-15 : MPS => Original created.
-- 89-08-08: MPS => Defined COM2 addresses in HW Config.
with Types;
with Low_Level_10;
with Unchecked_Conversion;
with HW_Config;
use Low_Level_10;
```

```
package Mouse_Data is
spec_eoi
                  : constant Low_Level_IO.BYTE := 16#63#;--specific end int
sync_byte
                   : constant Low_Level_IO.BYTE := 63; -- used to sync reports
-- The following constants are bit masks to be used with the BIT FIELD type.
                  : constant := 4;
                                        -- left button (reset statistics)
                  : constant := 3;
                                        -- middle button (change mode)
mode
                  : constant := 2;
                                        -- right button (fire rocket)
-- These constants are declared to aid in detecting serial errors during
-- transmision.
overflow
                  : constant := 1;
                                        -- position from status (2FD)
framing
                  : constant := 2;
                                        -- position from status (2FD)
-- Because the data bits are received six bits at a time, the following record
-- representation clauses are used to convert two bytes of data (a least and
-- most significant) to a single signed twelve bit number.
type BIT6_TYPE is range 0 .. 63;
type GAP_TYPE is range 0 .. 15;
type RAW_MOUSE_WORD is
  record
   LOW : BIT6_TYPE;
   HIGH : BIT6 TYPE;
   GAP : GAP_TYPE;
  end record;
for RAW_MOUSE_WORD use
  record
   LOW at 0 range 0 .. 5;
   HIGH at 0 range 6 .. 11;
   GAP at 0 range 12 .. 15;
  end record;
least_low12 : constant := -2048;
type SIGNED_MOUSE_WORD is
 record
   LOW12 : Types.WORD range least_low12 .. 2047;
   GAP : Types.WORD range 0 .. 15;
 end record;
for SIGNED MOUSE_WORD use
  record
   LOW12 at 0 range 0 .. 11;
   GAP at 0 range 12 .. 15;
```

```
end record;
-- Most significant bit for the following type definition on TANDY 4000: 15
type BIT FIELD is array(0..15) of BOOLEAN;
pragma pack(BIT_FIELD);
for BIT_FIELD'size use 16;
function Raw_to_Signed is new Unchecked_Conversion(RAW_MOUSE_WORD,
                                                            SIGNED_MOUSE_WORD);
function Byte_to_Bit6 is new Unchecked_Conversion(Low_Level_IO.BYTE,BIT6_TYPE);
function Bits_to_Byte is new Unchecked_Conversion(BIT_FIELD,Low_Level_IO.BYTE);
function Byte_to_Bits is new Unchecked_Conversion(Low_Level_IO.BYTE,BIT_FIELD);
pic_and_mask
               : constant BIT_FIELD :=
        (TRUE, TRUE, TRUE, FALSE, TRUE, TRUE, TRUE, TRUE, TRUE, TRUE,
        TRUE, TRUE, TRUE, TRUE, TRUE, TRUE, TRUE); -- will enable level 03 (COM2)
-- The following constants are used in the initialization procedure of mouse.
-- They are used to access the serial port COM2 on a TANDY PC (386).
access_baud
                   : constant Low_Level_IO.BYTE := 16#80#;
-- access baud rate rgtrs.
                   : constant Low_Level_IO.BYTE := 16#18#;
host_baud
-- 4800 baud (30h = 2400)
host_format
                 : constant Low_Level_[O.BYTE := 16#1E#;
-- 4800,e,7,1
                   : constant Low_Level_IO.BYTE := 16#20#;
acknowledge
-- mouse responds w/06h when
ack_response
                  : constant Low_Level_IO.BYTE := 16#06#;
-- sent a space (20h)
data_new
                : constant Low_Level_IO.BYTE := 16#61#;
-- char received
                 : constant Low Level IO.BYTE := 16#42#;
mouse format
-- Relative Bit Pad One
                  : constant low_Level_IO.BYTE := 16#4C#;
mouse_char_speed
-- 35 reports/sec when moving
general_int_enable : constant Low_Level_IO.BYTE := 16#08#;
-- for modem control register
specific_int_disable : constant Low_Level_IO.BYTE := 16#00#;
-- disable receive interrupt
specific_int_enable : constant Low_Level_IO.BYTE := 16#01#;
-- enable receive interrupt
pic_8259_mr
                  : constant Low_Level_IO.PORT_ADDRESS := 16#21#;
end Mouse_Data;
```

```
--% UNIT:
            Parameter Data Base Spec.
--% Effects: Provides rocket data types and initial values.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No exceptions.
--% Engineer: R. Chevier
.....
-- | PACKAGE SPEC : Parameter_Data_Base
--| This package defines the necessary default values for the rocket and
-- | the targets. There are four different target types described in
--| Simulate.Sensor.Targ_Sup. The type of rocket used to attack these targets
-- is described by the values below.
--1
with Types;
package Parameter_Data_Base is
-----
       ROCKET VALUES
.....
-- type DEGREES TYPE is digits 6 range 0.0..360.0;
-- type RATE_TYPE is digits 5;
-- subtype MAX_ROCKET_RANGE is Types.WORD range 1..100;
-- MAX_DEGRADED_ROCKETS : MAX_ROCKET_RANGE := 1;
-- type MASS_TYPE is digits 5 range 10.0..100.0;
-- type THRUST_TYPE is digits 6 range 100.0..100000.0;
-- type BURN_RATE_TYPE is digits 5 range 0.001..10.0;
-- type RESISTANCE_TYPE is digits 5 range 0.001..100.0;
-- type DRIFT_VELOCITY_TYPE is digits 5 range 0.0..0.5;
-- subtype ROCKET_TURN_ACCEL_TYPE is RATE_TYPE range 0.01..1000.0;
 C_Mass
                 : constant := 40.0;
 c fuel
                : constant := 300.0;
                                     -- kgs
                : constant := 6000.0; -- Newtons
 c thrust
                 : constant := 5.0;
                                       -- kgs/sec
 c_burn_rate
 c_turn_burn_rate : constant := 0.05; -- kgs/degree
 c_forward_drag : constant := 0.1875; -- Newton-secs/meter (was 0.09375)
 c_side drag
                 : constant := 0.203125; -- Newton-secs/meter
                  : constant := 0.0; -- meters/sec
 c_drift
 c_turn_rate
                  : constant := 200.0;
                                     -- degrees/sec
 type ROCKET PARAMETER TYPE is record
   MASS
                 : Types.LONG_FIXED := c_mass;
   FUEL
                  : Types.LONG_FIXED := c_fuel;
```

```
THRUST
                   : Types.LONG_FIXED := c_thrust;
   BURN_RATE
                  : Types.LONG_FIXED := c_burn_rate;
   TURN_BURN_RATE : Types.LONG_FIXED := c_turn_burn_rate;
   FORWARD_DRAG : Types.LONG_FIXED := c_forward_drag;
               : Types.LONG_FIXED := c_side_drag;
   SIDE DRAG
   DRIFT
                 : Types.LONG_FIXED := c_drift;
   TURN_RATE : Types.LONG_FIXED := c_turn_rate;
 end record;
      TARGET VALUES
 type TARGET PARAMETER TYPE is record
   MAX_VELOCITY_Y : Types.METERS; -- maximum velocity in Y per interval
   MAX_VELOCITY_X : Types.METERS; -- maximum velocity in x per interval
   DELTA VELOCITY X : Types.METERS; -- maximum change in x per interval
   CHANGE DIR FREQ : Types.WORD INDEX; -- freq that x dir changes in intrvls
 end record;
 type TARGET_PARAMS_ARRAY is array(Types.TARGET_CLASS_TYPE) of
                                               TARGET_PARAMETER_TYPE;
 TARGET_PARAMS : TARGET_PARAMS_ARRAY :=
                           ( Types.UNKNOWN =>
                               ( MAX_VELOCITY_Y => 2.000,
                                 MAX_VELOCITY_X => 1.500,
                                 DELTA_VELOCITY_X => 0.125,
                                 CHANGE_DIR_FREQ => 25),
                             Types.180
                                          =>
                               ( MAX_VELOCITY_Y => 1.750,
                                 MAX_VELOCITY_X => 1.250,
                                 DELTA_VELOCITY_X => 0.125,
                                 CHANGE_DIR_FREQ => 21),
                             Types.SA9
                                           =>
                               ( MAX_VELOCITY_Y => 1.875,
                                 MAX_VELOCITY_X => 1.375,
                                 DELTA_VELOCITY_X => 0.125,
                                 CHANGE_DIR_FREQ => 23),
                             Types.BMP2
                                         =>
                               ( MAX_VELOCITY_Y => 1.250,
                                 MAX_VELOCITY_X => 0.875,
                                 DELTA_VELOCITY_X => 0.125,
                                 CHANGE_DIR_FREQ => 15));
-- To simplify, all target types currently have the same DELTA_VELOCITY X
-- (meaning the same acceleration) and the same pixel size representation.
-- Therefore, they all have the same right and left border limits, and
-- consequently are all created within these borders.
 right_border_limit : constant := 3940.0;
```

```
left_border_limit : constant := 60.0;
x_start_limit : constant := right_border_limit - left_border_limit;
target_start_y : constant := 3960.0;
target_start_z : constant := 0.0;
end Parameter_Data_Base;
```

```
--% UNIT: RDL Package Body Subunit.
--% Effects: Supports all Rocket Data Link functions of Simulator.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
______
--- PACKAGE BODY : Simulate.RDL (Rocket Data Link)
--|
--|
     The RDL package provides tasks to interface to the Rocket Data Link
--| issuing messages for new rocket positions and receiving messages
-- commanding new rocket attitudes.
--|
-- Modifications Log
-- 88-10-30 : TEG => Original created.
separate(Simulate)
package body RDL is
                                   -- Rocket Data Link Simulator
 stack_size : constant := 348;
 task type Rock_Sup_Type is
   pragma PRIORITY(Config.rock_sup_priority);
  end Rock_Sup_Type;
  for Rock_Sup_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                            stack_size);
  Rock_Sup : Rock_Sup_Type;
  task body Rock_Sup_Type is separate;
  task body Report_Buf_Type is separate;
  task body Guide_Buf_Type is separate;
```

end RDL;

```
--% UNIT:
             Report_Buf Task Body Subunit.
--% Effects: Buffers Rocket report data between simulator and Control. --
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- TASK BODY : Simulate.RDL.Report_Buf
--|
--| The Report_Buf task acts as a buffer between the rocket data link
--| support task Rock Sup and the Rocket.Control task which processes
-- the rocket data. The task contains only accept statements for
-- | rendezvous purposes to allow for schedule slippage from both sides.
--| The data is a list of the rockets new positions as they fly across
--| the battlefield. Rocket.Control is the receiver of this list and
--| Rock sup is the supplier. This routine should be contrasted to Guide buf.
-- Note that if for some reason the Rock_Sup fails to deliver new rocket
--| positions, Rocket.Control will still display the old positions (but only
-- | for one interval, after which if they are still missing are considered
-- to have destroyed themselves). Note also that even if there are no rockets
-- | active, that a list is still passed with a length of zero.
--1
-- Modifications Log
-- 88-11-30 : TEG => Original created.
with Debug 10;
with Time Stamp;
separate (Simulate.RDL)
task body Report_Buf_Type is
  use Types;
  MSG COUNT
             : Types.WORD := 1;
  ROCKET_MSG : Rocket.ROCKET_MSG_TYPE;
begin
  ROCKET_MSG.NUM_ROCKETS := 0;
                                          -- default
  Loop
    select
      accept Put Report(DATA : in Rocket.ROCKET MSG TYPE) do
        Time_Stamp.Log(0066); -- $TP(0066) Report Buf accept Put Report start
        ROCKET MSG.NUM ROCKETS := DATA.NUM ROCKETS;
                                                    -- copy data
        ROCKET_MSG.ROCKET_LIST(Types.WORD_INDEX(1)..DATA.NUM_ROCKETS) :=
                    DATA.ROCKET_LIST(Types.WORD_INDEX(1)..DATA.NUM_ROCKETS);
        MSG_COUNT := 1;
        Time_Stamp.Log(0067); --$TP(0067) Report_Buf accept Put_Report end
```

```
end Put_Report;
     when MSG_COUNT = 1 =>
     accept Get_Report(DATA : out Rocket.ROCKET_MSG_TYPE) do
                                --$TP(0068) Report_Buf accept Get_Report start
       Time_Stamp.Log(0068);
       DATA.NUM_ROCKETS := ROCKET_MSG.NUM_ROCKETS;
       DATA.ROCKET_LIST(Types.WORD_INDEX(1)..ROCKET_MSG.NUM_ROCKETS) :=
           ROCKET_MSG.ROCKET_LIST(Types.WORD_INDEX(1)..ROCKET_MSG.NUM_ROCKETS);
       MSG_COUNT := 0;
       Time_Stamp.Log(0069);
                                --$TP(0069) Report_Buf accept Get_Report end
     end Get_Report;
   end select;
 end loop;
exception
 when others =>
   Debug_IO.Put_Line("REPORT_BUF termination due to exception.");
end Report_Buf_Type;
```

```
--% UNIT: Rocket Package Spec.
--% Effects: Provides structure for Rocket managment within BDS.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-----
-- PACKAGE SPEC : Rocket
---
--| This package contains the declaration to the control task which is
--| the main rocket processing task. It also declares the two main types
-- used for processing rocket information.
-- Modifications Log
-- 88-11-05 : TEG => Original created.
with Types;
with Config;
with Sync;
package Rocket is
stack_size
                   : constant := 1936; -- in bytes
------
-- REPORT INFORMATION
......
 type ROCKET_ITEM_TYPE is record -- provides essentials on a rocket
    SYNC_TAG : Sync.SEQ_TYPE;
    ROCKET_ID
                  : Types.WORD_INDEX;
    POSITION
                   : Types.POSITION_TYPE;
   end record;
 type ROCKET_LIST_TYPE is -- list of all rocket data
   array(Types.WORD_INDEX range <>) of ROCKET_ITEM_TYPE;
 type ROCKET_MSG_TYPE is record
    NUM_ROCKETS : Types.WORD_INDEX;
    ROCKET_LIST
                   : ROCKET_LIST_TYPE(1..Config.max_rockets);
 end record;
        GUIDANCE INFORMATION
............
 type ROCKET_GUIDE_TYPE is record
```

```
ROCKET_ID
                     : Types.WORD_INDEX;
   AIMPOINT
                      : Types.AIMPOINT_TYPE;
 end record;
 type ROCKET_GUIDE_LIST_TYPE is -- list of all guidance data
   array(Types.WORD_INDEX range <>) of ROCKET_GUIDE_TYPE;
 type ROCKET_GUIDE_MSG_TYPE is record
   NUM_ROCKETS : Types.WORD_INDEX;
   ROCKET_GUIDE_LIST : ROCKET_GUIDE_LIST_TYPE(1..Config.max_rockets);
 end record;
 task type Control_Type is
                                           -- for overall engagement control
   entry Start;
   entry Get_Next_Report(ROCKET_REPORT_MSG : in ROCKET_MSG_TYPE);
   pragma PRIORITY(Config.control_priority);
 end Control_Type;
 for Control_Type/STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                                      stack_size);
 Control
                        : Control_Type;
end Rocket; -- package specification
```

```
--% UNIT:
             Rocket Package Body.
--% Effects: Provides structure for Rocket managment within BDS.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: I. Griest.
-- | PACKAGE BODY : Rocket
--|
--| The Rocket package provides all processing to maintain the rockets
-- | in flight.
--- The rocket quidance activity is given overall control by the Control task.
--| "Control" is used to accept rocket reports, and is responsible for engaging
--| the targets, providing updates to the Graphics.Display task, and generating
--| the guidance messages for the Rocket Data Link. It achieves much of this
-- | with the assistance of one (or more) Guidance task(s). The Guidance task
-- is responsible for taking a set of the rockets and producing a new
-- | aimpoint for each rocket/target in that set. The activities of the
--| guidance task(s), as well as the Control task can be overlapped
-- considerably, and therefore may benefit from the addition of processors.
---
-- Modifications Log
-- 88-11-25 : TEG => Original created.
-- 89-11-22 : MPS => Aimpoint_Info type created to allow less traffic on the
with Debug 10;
with Status; -- maintains number Rockets Active
with Shapes;
                                             -- for rocket shapes
with Graphics: -- for graphics operations/colors
with Distrib;
package body Rocket is
guidance_stack_size
                              : constant := 660;
  GUIDANCE_LIST_ERROR : exception; -- if guidance list does not match history
-- This history data is provided to a guidance task, which in turn processes
-- it and returns the next guidance information needed for each rocket.
  type POSITION_DATA_TYPE is record -- containing rocket/target information
                                       -- if rocket was previously active
    ACTIVE
                         : BOOLEAN;
    ROCKET POS
                       : Types.POSITION_TYPE; -- latest rocket position
    TARGET POS
                         : Types.POSITION_TYPE; -- latest target position
```

end record;

```
type POSITION_LIST_TYPE is
                       array(Types.WORD_INDEX range <>) of POSITION_DATA_TYPE;
  type AIMPOINT_LIST_TYPE is
                       array(Types.WORD_INDEX range <>) of Types.AIMPOINT_TYPE;
 AIMPOINT_INFO
                 : POSITION_LIST_TYPE(1..Config.max_rockets);
 NEXT_GUIDE_MSG : ROCKET_GUIDE_MSG_TYPE;
 task type Guidance_Type is
   entry History(AIM_DATA : in POSITION_LIST_TYPE);
   entry Next_Guidance(AIMPOINT_LIST : out AIMPOINT_LIST_TYPE);
   pragma PRIORITY(Config.guidance_priority);
  end Guidance_Type;
  for Guidance_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                               guidance_stack_size);
  Rocket_Guide : array(Types.WORD_INDEX range 1..Distrib.num_guide_tasks)
                                                             of Guidance_Type;
  task body Guidance_Type is separate;
  task body Control_Type is separate;
end Rocket; -- package body
```

```
.....
--% UNIT: Rock_Sup Task Body Subunit.
--% Effects: Provides all Rocket Support for Simulator, including
         target intercept detection.
--% Modifies: Updates state of rockets and targets in Simulator DBase. --
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-----
-- Copyright(C) 1988, LabTek Corporation. Permission is granted to copy
-- and/or use this software provided that this copyright notice is included
-- and all liability for its use is accepted by the user.
-- TASK BODY : Simulate.RDL.Rock_Sup
--|
--| The rocket support task provides the necessary rocket motion, based
--| on previous position and the application of a new guidance aimpoint.
-- It generates a new report "ROCKET MSG" for a buffer task (Report Buf)
--! to forward to the BDS Rocket.Control task. Likewise, the Rocket.Control
-- task issues guidance messages to the buffer task (Guide_Buf) which are
-- | made available to the Rock_Sup task. ROCKET/TARGET intercepts are
-- checked in the shared data base within the simulator. In such cases,
--| both the rocket and target are destroyed (marked inactive).
---
-- Modifications Log
-- 88-12-05 : TEG => Original created.
-- 89-09-12 : MPS => Changed call to Traject to reflect new flight dynamics.
with Traject;
                   -- trajectory planner
with Calendar;
with Interrupt_Control;
with Time Stamp;
with Sync:
pragma ELABORATE(Traject, Calendar, Interrupt_Control, Time_Stamp);
separate (Simulate.RDL)
task body Rock_Sup Type is
 use Calendar;
                   -- for - operator
                    -- for operators
 use Types;
                    -- for sequence operations
 use Sync;
 start_position : constant Types.POSITION_TYPE :=
                           (Config.launch_x,Config.launch_y,Config.launch_z);
```

```
: Rocket.ROCKET_MSG_TYPE;
 ROCKET_MSG
                     : Rocket.ROCKET GUIDE MSG TYPE;
 GUIDE MSG
 GUIDE_MSG_INDEX
                     : Types.WORD_INDEX;
 REPORT_MSG_INDEX
                     : Types.WORD_INDEX;
                      : Types.POSITION_TYPE; -- temp
 POSITION
                      : Sync.SEQ TYPE := 0;
 SEQUENCE TAG
                      : Calendar.TIME;
 START_TIME
 DELAY_PERIOD
                      : DURATION:
-- MAKE REPORT: process current rocket ID
 procedure Make_Report(ID : Types.WORD_INDEX; POS : Types.POSITION_TYPE) is
                            -- checks if rocket has collided with
                            -- any targets or ground. If so, delete
                            -- target(s) and rocket.
                       : Types.LONG_FIXED;
   DELTA X
   DELTA Y
                       : Types.LONG FIXED;
                       : Types.LONG_FIXED;
   DELTA Z
                        : Types.LONG_FIXED; -- time for rocket to reach ground
   DELTA T
   ROCKET POS
                        : Types.POSITION_TYPE;
  begin -- of Make_Report
   --$TP(0070) Rock_Sup.Make_Report start
    if POS.Z < 0.0 then
     ROCKETS(ID).ACTIVE := FALSE;
                                             -- destroy rocket
-- compute time it took to get to zero
     DELTA_X := POS.X - ROCKETS(ID).POSITION.X;
     DELTA Y := POS.Y - ROCKETS(ID).POSITION.Y;
     DELTA_Z := POS.Z - ROCKETS(ID).POSITION.Z;
      if DELTA_Z = 0.0 then
       DELTA_T := 0.0;
      else
        DELTA T := Types.LONG FIXED(ROCKETS(ID).POSITION.Z/abs(DELTA_Z));
      end if;
-- find terminal position of Rocket
      ROCKET_POS.X := ROCKETS(ID).POSITION.X + DELTA_T*DELTA_X;
      ROCKET_POS.Y := ROCKETS(ID).POSITION.Y + DELTA_T*DELTA_Y;
-- TBD since targets are always at Z=O, collision point is always O
        ROCKET_POS.2 := ROCKETS(ID).POSITION.2 + Types.METERS(DELTA_T*DELTA_Z);
-- Now search target list to see if any targets within "kill_radius"
    perimeter of rocket
      for TARGET_ID in TARGETS'range loop
                                                   -- access to shared data
        Interrupt_Control.Disable;
        if TARGETS(TARGET ID).ACTIVE then
          DELTA_X := ROCKET_POS.X - TARGETS(TARGET_ID).POSITION.X;
```

```
DELTA_Y := ROCKET_POS.Y - TARGETS(TARGET ID).POSITION.Y;
-- TBD should use distance DISTANCE := Math.Sqrt( Types.METERS(DELTA X*DELTA X) +
-- TRA
                                   Types.METERS(DELTA_Y*DELTA_Y) +
--TBD
                                   Types.METERS(DELTA_2*DELTA_2)));
         if abs DELTA_X < Config.kill_radius and -- this makes square box
            abs DELTA_Y < Config.kill_radius</pre>
                                              -- around each target
         then
           TARGETS(TARGET_ID).ACTIVE := FALSE; -- destroy target
         end if;
       end if;
       Interrupt_Control.Enable;
     end loop:
                           -- Rocket did not hit ground or target
     REPORT_MSG_INDEX := REPORT_MSG_INDEX + 1;
     ROCKET_MSG.ROCKET_LIST(REPORT_MSG_INDEX) := (SEQUENCE_TAG, ID, POS);
   -- $TP(0071) Rock_Sup_Make_Report end
 end Make_Report;
ROCKET SUPPORT TASK BODY
-----
begin
 for ID in ROCKETS'range loop
                                         -- initialize to all inactive
  ROCKETS(ID).ACTIVE := FALSE;
 end loop;
 START_TIME := Calendar.CLOCK; -- find out when xeq begins
 loop
   --$TP(0072) Rock_Sup task start
   START_TIME := START_TIME + Config.interval;
   if SEQUENCE_TAG = Sync.SEQ_TYPE'last then -- update TIME TAG to be able
     SEQUENCE_TAG := 0;
                                           -- to differentiate between
                                           -- stale and new reports
     SEQUENCE_TAG := SEQUENCE TAG + 1;
   end if;
   --$TP(0073) Rock_Sup rendezvous with Guide_Buf start
   RDL.Guide_Buf.Get_Guide(GUIDE_MSG);
                                        -- fetch latest guidance message
   --$TP(0074) Rock_Sup rendezvous with Guide_Buf end
-- Go through each rocket, and if active, apply trajectory to
-- current position for 1 interval.
   GUIDE_MSG_INDEX := 1;
                                         -- pointer msg.rocket_guide_list
   REPORT_MSG_INDEX := 0;
   for ROCKET ID in ROCKETS'range loop
     if GUIDE_MSG_INDEX <= GUIDE_MSG.NUM_ROCKETS and then
        ROCKET_ID = GUIDE_MSG.ROCKET_GUIDE_LIST(GUIDE_MSG_INDEX).ROCKET_ID
     then
```

```
This rocket is in the list, see if it was previously active
       if not ROCKETS(ROCKET_ID).ACTIVE then
-- filter out guidance messages for rockets that have recently been
-- destroyed (but BDS doesn't know it yet)
          if GUIDE_MSG.ROCKET_GUIDE_LIST(GUIDE_MSG_INDEX).AIMPOINT.ELEVATION =
            Config.launch_elevation
                     -- a new launch
          then
           ROCKETS(ROCKET_ID).ACTIVE := TRUE; -- launch
           ROCKETS(ROCKET_ID).POSITION := start_position;
            Make_Report(ROCKET_ID,start_position); -- start at launcher
          end if;
        else
     Now compute new X,Y,Z position.
          Traject.Get_New_Position(ROCKET_ID,
                         GUIDE_MSG.ROCKET_GUIDE_LIST(GUIDE_MSG_INDEX).AIMPOINT,
                         POSITION);
          Make_Report(ROCKET_ID, POSITION);
          ROCKETS(ROCKET_ID).POSITION := POSITION;
        end if;
                 -- rocket active check
        GUIDE_MSG_INDEX := GUIDE_MSG_INDEX + 1;
                                                  -- no guidance for this rocket
        if ROCKETS(ROCKET ID).ACTIVE then
  no guidance information for active rocket, simply don't move it
          POSITION := ROCKETS(ROCKET_ID).POSITION;
          Make_Report(ROCKET_ID, POSITION);
        end if;
                   -- rocket active check
      end if;
                    -- guide entry exists check
    end loop;
-- New report list has been generated. Send it to buffer task.
    ROCKET_MSG.NUM_ROCKETS := REPORT_MSG_INDEX;
    --$TP(0075) Rock_Sup rendezvous with Report_Buf start
    RDL.Report_Buf.Put_Report(ROCKET_MSG);
                                            -- issue next rocket report
    --$TP(0076) Rock_Sup rendezvous with Report_Buf end
-- Delay to make rocket motion reports periodic
   DELAY_PERIOD := START_TIME - Calendar.CLOCK;
    if DELAY_PERIOD < 0.0 then
      START_TIME := CLOCK;
    end if;
    --$TP(0077) Rock_Sup task end
    delay DELAY_PERIOD;
```

end loop;
end Rock_Sup_Type;

```
Sensor Package Body Subunit.
--% Effects: Provides structure for all simulator Target motion.
--% Modifies: Simulator target data is updated.
--% Requires: Initialization is performed by Sensor.Initialize.
--% Raises: TARGET_CREATE_ERROR is raised if no room for more targets.--
--% Engineer: M. Sperry.
-- | PACKAGE BODY : Simulate.Sensor
--|
--| The sensor package supports the targ_sup task by keeping a history
--| of the old target position, the current X velocity of the target, a
-- desired X velocity of the target, how long to stay at that desired velocity
--| and finally the attributes of the class of the target. The Y velocity
-- is constant with respect to the class of the target, as is the turning
-- | frequency of the X direction.
--|
-- Modifications Log
-- 88-10-22 : TEG => Original created.
-- 89-11-27 : MPS => Get_New_ID, Get_New_Position, and Activate_Target created.
with Interrupt_Control;
with Math;
with Parameter_Data_Base;
with Time_Stamp;
with Debug_10;
  pragma ELABORATE(Math, Debug_IO, Interrupt_Control);
separate(Simulate)
package body Sensor is
                                        -- Target Sensor Simulator
use Types;
type HISTORY_REC is record
 OLD POS
            : Types.POSITION_TYPE;
  CURRENT_VEL_X : Types.METERS;
  DESIRED_VEL_X : Types.METERS; -- generated randomly every CHANGE_DIR_FREQ
  CHANGED_VEL_TIME: Types.WORD_INDEX; -- intervals since DESIRED was changed
  ATTRIBUTES
                 : Parameter_Data_Base.TARGET_PARAMETER_TYPE;
end record;
type HISTORY_TYPE is array(Types.TARGET_INDEX_TYPE) of HISTORY_REC;
TARGET HISTORY
                   : HISTORY TYPE;
LAST_USED_TARGET_ID : Types.TARGET_INDEX_TYPE;
```

```
procedure Initialize is
--| SUBPROGRAM BODY : Simulate.Sensor.Initialize
--|
--| Initialize is responsible for setting the LAST_USED_TARGET_ID to the first
--| allowable value of that type. Also, it sets all targets to an inactive
-- (FALSE) state.
--1
begin
 LAST_USED_TARGET_ID := Types.TARGET_INDEX_TYPE'first;
  for ID in Types.TARGET INDEX TYPE loop
   TARGETS(ID).ACTIVE := FALSE;
  end loop;
end Initialize;
function Get_New_ID return Types.TARGET_INDEX_TYPE is
-- | SUBPROGRAM BODY : Simulate.Sensor.Get_New_ID
-- To simplify the code in Targ Sup, this function keeps track of the last
--| target id used (a package level variable in Sensor.ab) and returns a new
-- | target id that is not currently being used. The target id's rollover at
-- | Config.max_targets.
--|
TARGET_ID
                  : Types.TARGET_INDEX_TYPE;
TARGET_CREATE ERROR : EXCEPTION;
begin
  TARGET_ID := LAST_USED_TARGET_ID;
           -- loop through each target id starting from LAST USED TARGET ID
    if not TARGETS(TARGET_ID).ACTIVE then
      LAST_USED_TARGET_ID := TARGET ID;
      if LAST_USED_TARGET_ID = Types.TARGET_INDEX TYPE'last then
        LAST_USED_TARGET_ID := Types.TARGET_INDEX TYPE'first;
      end if:
      exit:
      if TARGET_ID = Config.max_targets then
        TARGET_ID := Types.TARGET INDEX_TYPE'first;
       TARGET ID := TARGET_ID + 1;
      end if:
      if TARGET_ID = LAST_USED TARGET_ID then -- no more room for targets,
       raise TARGET_CREATE_ERROR;
                                                -- but told to create one
      end if;
    end if;
  end loop;
```

```
return TARGET_ID;
exception
  when TARGET_CREATE_ERROR =>
   Debug_IO.Put("TARGET_CREATE_ERROR raised in Simulate.Sensor.Get_New_ID");
end Get_New_ID;
procedure Activate_Target(TARGET_ID : Types.TARGET_INDEX_TYPE) is
--1
-- | SUBPROGRAM BODY : Simulate.Sensor.Activate_Target
-- Activate_Target initializes the record which controls the target's
-- | history. It also assigns a random new starting position (in X only,
--| the starting Y and Z positions are fixed) and a new class. The class
-- is chosen randomly via the package Math.
--1
NUM_OF_CLASSES : Types.WORD_INDEX;
CLASS
             : Types.TARGET CLASS TYPE;
POS X
              : Types.METERS;
MAX_X_VEL
             : Types.METERS;
begin
-- Limit the access to the shared data base in Simulate.
 Interrupt_Control.Disable;
-- Initialize Simulate.TARGETS data base.
  TARGETS(TARGET ID).ACTIVE := TRUE;
  TARGETS(TARGET ID).POSITION.Y := Types.LONG FIXED(
                                         Parameter_Data_Base.target_start_y);
  TARGETS(TARGET_ID).POSITION.2 := Types.LONG_FIXED(
                                         Parameter_Data Base.target start z);
  POS_X := Math.Get_Random_Num(Types.METERS(
                                         Parameter_Data_Base.x_start_limit));
  POS_X := POS_X + Parameter_Data_Base.left_border_limit;
  TARGETS(TARGET_ID).POSITION.X := Types.LONG_FIXED(POS X);
  NUM_OF_CLASSES := Types.WORD_INDEX(Types.TARGET_CLASS_TYPE'pos(
                                     Types.TARGET_CLASS_TYPE'(ast));
  CLASS := Types.TARGET_CLASS_TYPE'val(Math.Get_Random Num(NUM_OF_CLASSES+1));
  TARGETS(TARGET ID).TARGET CLASS := CLASS;
-- Enable the interrupts again.
  Interrupt_Control.Enable;
-- Initialize Sensor.TARGET_HISTORY data base.
  TARGET_HISTORY(TARGET_ID).OLD_POS := TARGETS(TARGET_ID).POSITION;
```

```
TARGET HISTORY(TARGET_ID).ATTRIBUTES :=
                                  Parameter_Data_Base.TARGET_PARAMS(CLASS);
 MAX_X_VEL := TARGET_HISTORY(TARGET_ID).ATTRIBUTES.MAX_VELOCITY_X;
 TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X := Math.Get_Random_Num(
                                            Types.METERS(2 * MAX_X_VEL));
  TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X := MAX_X_VEL -
                           TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X;
  TARGET_HISTORY(TARGET_ID).CHANGED_VEL_TIME := 0;
  TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X := Math.Get_Random_Num(
                                            Types.METERS(2 * MAX_X_VEL));
  TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X := MAX_X_VEL -
                           TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X;
end Activate_Target;
procedure Get New Position(TARGET ID : Types.TARGET INDEX TYPE) is
-- | SUBPROGRAM BODY : Sensor Get New Position
--1
--| Get_New_Position is responsible for updating the history of the targets
-- and more importantly to return to TARGETS a new target position in the
-- Types.POSITION_TYPE, which is made up of Types.LONG_FIXED. The target
-- is not allowed to leave the battlefield border area, therefore it changes
--| directions before bouncing against the side of the border.
--1
DIR FREQ
              : Types.WORD_INDEX;
MAX_X_VEL
            : Types.METERS;
MAX_Y_VEL : Types.METERS;
DELTA_X
              : Types.METERS;
CLASS
             : Types.TARGET_CLASS_TYPE;
INTERVALS_LEFT : Types.METERS;
X_POS_EST
             : Types.METERS;
begin
  Time Stamp.Log(0122); --$TP(0122) Sensor.Get New Position start
-- Place often used but complex address calculation type variables in
-- local space.
 CLASS := TARGETS(TARGET_ID).TARGET_CLASS;
 DIR_FREQ := TARGET_HISTORY(TARGET_ID).ATTRIBUTES.CHANGE_DIR_FREQ;
 MAX X VEL := TARGET HISTORY(TARGET ID).ATTRIBUTES.MAX VELOCITY X;
 MAX Y VEL := TARGET_HISTORY(TARGET ID).ATTRIBUTES.MAX VELOCITY Y;
 DELTA X := TARGET HISTORY(TARGET ID).ATTRIBUTES.DELTA VELOCITY X;
-- Check to see if it is time to change dir.
  if TARGET_HISTORY(TARGET ID). CHANGED VEL TIME = DIR FREQ then
-- Time to change the X direction. The DESIRED VEL X is a random number of
```

```
-- Types.METERS between +MAX_X_VEL and -MAX_X_VEL.
    TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X := Math.Get_Random_Num(
                                               Types.METERS(2 * MAX_X_VEL));
   TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X := MAX_X_VEL -
                            TARGET HISTORY(TARGET ID).DESIRED VEL X;
    TARGET_HISTORY(TARGET_ID).CHANGED_VEL_TIME := 0;
-- Increment the counter that keeps track of when it is time to change direction
 TARGET_HISTORY(TARGET_ID).CHANGED_VEL_TIME :=
                            .ARGET_HISTORY(TARGET_ID).CHANGED_VEL_TIME + 1;
-- Avoid hitting the battlefield border area.
  if TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X > 0.0 then
    if TARGETS(TARGET_ID).POSITION.X < Parameter_Data_Base.left_border_limit</pre>
    and TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X > 0.0 then -- going left
      TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X :=
                                      -TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X;
    end if:
  else
    if TARGETS(TARGET_ID).POSITION.X > Parameter_Data_Base.right_border_limit
    and TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X < 0.0 then -- going right
     TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X :=
                                      -TARGET HISTORY(TARGET ID).DESIRED VEL X;
    end if:
  end if;
-- Adjust the CURRENT_VEL_X by DELTA_X if need be.
  if TARGET_HISTORY(TARGET ID).CURRENT VEL X <
     TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X
    TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X :=
                            TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X + DELTA_X;
  elsif TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X >
        TARGET_HISTORY(TARGET_ID).DESIRED_VEL_X
  then
    TARGET_HISTORY(TARGET_ID).CURRENT_VEL_X :=
                            TARGET_HISTORY(TARGET_ID).CURRENT_VEL X - DELTA X;
 end if;
-- Ascertain the new position based on the velocities of the class, saving
-- the old position first, and guarantee mutally exclusive access.
  TARGET_HISTORY(TARGET_ID).OLD_POS := TARGETS(TARGET ID).POSITION;
  Interrupt_Control.Disable;
  if TARGETS(TARGET_ID).ACTIVE then
```

```
--% UNIT:
            Shapes Package Spec.
--% Effects: Provides all graphics symbology.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest / M. Sperry.
-- | PACKAGE SPEC : Shapes
--|
--| Package Shapes is responsible for determining the relative offsets
--| which define the shapes of all the possible symbols that can be drawn.
--| The reticle is the pointing box that is controlled by the mouse. Note
-- | that where the coordinate (0,0) is defined in terms of the shapes of
--| the object. For example the nose of the rocket is considered to be
--| explosive in our case. Therefore the nose of the rocket has the coordinates
-- | (0,0). Likewise, the center of the target has the (0,0) coordinate. The
-- | objects are all manipulated by their absolute coordinates or rather,
-- | the coordinate (0,0).
--|
-- Modifications Log
-- 88-10-12 : MPS => Original created.
-- 89-08-08 : MPS => Adjusted to work with new DDC compiler
with Types;
with Config;
package Shapes is
type SYMBOL TYPE is (ROCKET, TARGET, RETICLE, DOT, ZERO, ONE, TWO, THREE, FOUR,
                     FIVE, SIX, SEVEN, EIGHT, NINE, HORIZONTAL, VERTICAL);
type PIXEL is record
 x:Types.COORDINATE range Config.entire_screen_left..Config.entire_screen_right;
 Y:Types.COORDINATE range Config.entire_screen_top..Config.entire_screen_buttom;
end record;
                                                -- offset from base of pixel
type REL PIXEL is record
  X_OFFSET : Types.REL_COORDINATE;
                                                     -- positive goes right
                                                      -- positive goes down
  Y OFFSET : Types.REL COORDINATE;
end record;
type PIXEL_LIST is array(Types.WORD_INDEX range <>) of REL_PIXEL;
type OBJECT_PTR is access PIXEL_LIST;
```

```
reticle_left : constant := -5;
                                                -- constants used to check if
reticle_right : constant := 5;
                                               -- reticle going past screen
reticle_top : constant := -5;
                                                -- boundaries.
reticle_bottom : constant := 5;
-- The following two constants determine how far the target center can
-- be in meters from the indicated reticle center and still allow
-- aquisition of the target for launching a rocket. They are not the
-- same in X and Y, since the reticle is slightly rectangular.
reticle_x_error: constant := 40.25;
                                       -- METERS to allow target aquisition
reticle_y_error: constant := 49.50; -- METERS to allow target aquisition
NUMERIC
                : array(0..9) of SYMBOL_TYPE := (ZERO, ONE, TWO, THREE, FOUR,
                                             FIVE, SIX, SEVEN, EIGHT, NINE);
number_width : constant := 8;
                                                -- widest number in pixels
m1
              : constant := -1;
m2
              : constant := -2;
m3
              : constant := -3;
m4
              : constant := -4;
m5
              : constant := -5;
m6
              : constant := -6;
m7
              : constant := -7;
              : constant := -8;
OBJECT_PTR_TABLE : array(SYMBOL_TYPE) of OBJECT_PTR :=
 (TARGET => new PIXEL_LIST'(
                               (0, m2),
                            (m1,m1), (1,m1),
                         (m2,0), (0,0), (2,0),
                            (m1,1), (1,1),
                               (0,2)),
  ROCKET => new PIXEL_LIST'(
                            (0,0),
                            (0,1),
                           (0,2),
                           (0,3),
                     (m1,4), (1,4)),
  RETICLE => new PIXEL LIST'(
(m5,m5),(m4,m5),(m3,m5),
                                                       (3,m5),(4,m5),(5,m5),
(m5, m4),
                                                                     (5,m4),
(Em, 3m)
                                 (0,m3),
                                                                     (5,m3),
                                 (0, m2),
                                 (0,m1),
          (m3,0), (m2,0), (m1,0), (0,0), (1,0), (2,0), (3,0),
                                  (0,1),
                                  (0,2),
```

```
(0,3),
                                                                        (5,3),
(m5,3),
                                                                        (5,4),
(m5,4),
                                                           (3,5),(4,5),(5,5)),
(m5,5),(m4,5),(m3,5),
   DOT => new PIXEL_LIST'( (0,0),(0,0) ),
                                    (1,m8),(2,m8),(3,m8),(4,m8),(5,m8),
   ZERO => new PIXEL_LIST'(
                                                                         (6, m7),
                             (0,m7),
                                                                 (5,m6),(6,m6),
                             (0,mó),
                                                          (4,m5),
                                                                         (6,m5),
                             (0,m5),
                             (0,m4),
                                                   (3,m4),
                                                                         (6,m4),
                                                                         (6,m3),
                             (0,m3),
                                            (2,m3),
                                                                         (6,m2),
                             (0,m2),(1,m2),
                                                                         (6,m1),
                             (0,m1),
                                     (1,0), (2,0), (3,0), (4,0), (5,0)),
                                     (4,m8),
   ONE => new PIXEL_LIST'(
                              (3,m7),(4,m7),
                                     (4,m6),
                                     (4,m5),
                                     (4,m4),
                                     (4,m3),
                                     (4, m2),
                                     (4,m1),
                              (3,0), (4,0), (5,0)),
                                     (1,m8),(2,m8),(3,m8),(4,m8),
    TWO => new PIXEL_LIST'(
                                                                  (5, m7),
                              (0,m7),
                                                                  (5,mó),
                                                           (4,m5),
                                                    (3,m4),
                                            (2,m3),
                                     (1,m2),
                              (0,m1),
                              (0,0), (1,0), (2,0), (3,0), (4,0), (5,0)),
    THREE => new PIXEL_LIST'(
                                     (1,m8),(2,m8),(3,m8),
                                                           (4, 117),
                              (0, m7),
                                                           (4,m6),
                                                           (4,m5),
                                             (2,m4),(3,m4),
                                                           (4,m3),
                                                           (4,m2),
                                                           (4,m1),
                              (0,m1),
                                     (1,0), (2,0), (3,0)),
                                                            (4, m8),
    FOUR => new PIXEL_LIST'(
                                                    (3,m7),(4,m7),
                                             (2,m6),
                                                            (4,m6),
                                      (1,m5),
                                                            (4,m5),
                               (0,m4),(1,m4),(2,m4),(3,m4),(4,m4),(5,m4),
```

```
(4,m3),
                                                        (4, m2),
                                                        (4,m1),
                                                (3,0), (4,0), (5,0)),
                                  (1,m8),(2,m8),(3,m8),(4,m8),(5,m8),
FIVE => new PIXEL_LIST'(
                         (0, m7),
                         (0,m6),
                         (0,π5),
                                  (1,m4),(2,m4),(3,m4),(4,m4),
                                                               (5,m3),
                                                               (5, m2),
                                                               (5,m1),
                         (0,0), (1,0), (2,0), (3,0), (4,0), (5,0)),
SIX => new PIXEL_LIST'(
                                                (3,m8),(4,m8),
                                         (2,m7),
                                  (1, m7),
                         (0,m6),
                         (0,m5), (1,m5), (2,m5), (3,m5),
                                                        (4, m4),
                         (0,m4),
                                                        (4,m3),
                          (0,m3),
                                                        (4, m2),
                          (0, m2),
                          (0,m1),
                                                        (4,m1),
                                  (1,0), (2,0), (3,0)),
                                  (1,m8),(2,m8),(3,m8),(4,m8),(5,m8),
SEVEN => new PIXEL_LIST'(
                                                                (5,m7),
                          (0,m7),
                                                        (4,m6),
                                                 (3,m5),
                                         (2,m4),
                                  (1,m3),
                          (0,m2),
                          (0,m1),
                          (0,0)),
                                  (1,m8),(2,m8),(3,m8),(4,m8),
EIGHT => new PIXEL LIST'(
                                                                (5,m7),
                          (0, m7),
                                                                (5,m6),
                          (0,m6),
                                                                (5,m5),
                          (0,m5),
                                   (1,m4),(2,m4),(3,m4),(4,m4),
                                                                (5,m3),
                          (0,m3),
                                                                (5,m2),
                          (0, m2),
                          (0,m1),
                                                                (5,m1),
                                   (1,0), (2,0), (3,0), (4,0)),
                                   (1,m8),(2,m8),(3,m8),(4,m8),
NINE => new PIXEL_LIST'(
                                                                (5, m7),
                          (0,m7),
                                                                (5,m6),
                          (0,m6),
                          (0,m5),
                                                                (5,m5),
                                   (1,m4),(2,m4),(3,m4),(4,m4),(5,m4),
```

```
(5, m3),
                                                        (4,m2),
                                                (3,m1),
                                  (1,0), (2,0)),
HORIZONTAL => new PIXEL_LIST'((0, 0),(1, 0),(2, 0),(3, 0),(4, 0),(5, 0),
                               (6, 0), (7, 0), (8, 0), (9, 0), (10, 0), (11, 0),
                               (12,0),(13,0),(14,0),(15,0),(16,0),(17,0),
                               (18,0),(19,0),(20,0),(21,0),(22,0),(23,0),
                               (24,0),(25,0),(26,0),(27,0),(28,0),(29,0),
                               (30,0),(31,0),(32,0),(33,0),(34,0),(35,0),
                               (36,0),(37,0),(38,0),(39,0),(40,0),(41,0),
                               (42,0),(43,0),(44,0),(45,0),(46,0),(47,0),
                               (48,0),(49,0),(50,0),(51,0),(52,0),(53,0),
                               (54,0),(55,0),(56,0),(57,0),(58,0),(59,0),
                               (60,0),(61,0),(62,0),(63,0),(64,0),(65,0),
                               (66,0),(67,0),(68,0),(69,0),(70,0),(71,0),
                               (72,0),(73,0),(74,0),(75,0),(76,0),(77,0),
                               (78,0)),
VERTICAL => new PIXEL_LIST'((0, 0), (0, 1), (0, 2), (0, 3), (0, 4), (0, 5),
                               (0, 6), (0, 7), (0, 8), (0, 9), (0, 10), (0, 11),
                               (0,12),(0,13),(0,14),(0,15)));
```

end Shapes;

```
......
--% UNIT: Simulate Package Spec.
--% Effects: Provides shared data base for Simulator.
--% Modifies: No global data is modified.
--% Requires: Individual tasks are responsible for init. of global data.--
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | PACKAGE SPEC : Simulate
--1
--| The Simulate package is used to provide input and output to the
--| BDS system. It provides rocket flight paths and target generation.
-- Modificiations Log
-- 88-10-15 : TEG => Original created.
with Target;
with Rocket;
with Sync;
with Config:
package Simulate is
                                   -- Overall simulation package
 package Sensor is
                                    -- Target Sensor Simulator
   stack_size : constant := 114;
                                    -- in bytes
   task type Targ_Sup_Type is
     pragma PRIORITY(Config.targ_sup_priority);
     entry Next_Target_Msg(Data : out Target.TARGET_MSG_TYPE);
   end Targ_Sup_Type;
   for Targ_Sup_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                                           stack_size);
   Targ_Sup : Targ_Sup_Type;
 end Sensor;
 package RDL is
                                   -- Rocket Data Link Simulator
   report_buf_stack_size : constant := 302; -- in bytes
   guide_buf_stack_size : constant := 744;
                                             -- in bytes
-- The Report_Buf task buffers Rocket Reports from the Rock_Sup task
-- and provides them to the Rocket.Control task
   task type Report_Buf_Type is
     pragma PRIORITY(Config.report_buf priority);
     entry Put_Report(DATA : in Rocket.ROCKET_MSG_TYPE);
```

```
entry Get_Report(DATA : out Rocket.ROCKET_MSG_TYPE);
   end Report_Buf_Type;
   for Report_Buf_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit
                                              * report_buf_stack_size);
   Report_Buf : Report_Buf_Type;
-- The Guide_Buf task buffers new Guidance messages from the Rocket.Control
-- task for delivery to the Rock_Sup task.
   task type Guide_Buf_Type is
     pragma PRIORITY(Config.guide_buf_priority);
     entry Put_Guide(DATA : in Rocket.ROCKET_GUIDE_MSG_TYPE);
     entry Get_Guide(DATA : out Rocket.ROCKET_GUIDE_MSG_TYPE);
   end Guide_Buf_Type;
   for Guide_Buf_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit
                                              * guide_buf_stack_size);
   Guide_Buf : Guide_Buf_Type;
 end RDL;
end Simulate;
```

```
-----
--% UNIT: Simulate Package Body.
--% Effects: Provides shared data base for Simulator.
--% Modifies: No global data is modified.
--% Requires: Individual tasks are responsible for init. of global data.--
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- PACKAGE BODY : Simulate
---
--! This package is responsible for implementation of the "driver"
-- for the BDS. The simulator provides random target generation (subject
-- | to a limit maximum), rocket trajectories, and target motion.
-- Modifications Log
-- 88-10-29 : TEG => Original created.
with Types;
-- Simulator package to provide testing of BDS system
package body Simulate is
                                   -- Overall simulation package
------
-- TARGET DATA
-----
 type TARGET_SIM_TYPE is record -- provides individual target information
           : BOOLEAN;
   ACTIVE
   POSITION
               : Types.POSITION_TYPE;
   TARGET_CLASS : Types.TARGET_CLASS_TYPE;
 end record;
 type TARGETS_TYPE is
       array(Types.WORD_INDEX range 1..Config.max_targets) of TARGET_SIM_TYPE;
 TARGETS : TARGETS_TYPE;
-- ROCKET DATA
type ROCKET_SIM_TYPE is record -- provides individual rocket information
  ACTIVE
               : BOOLEAN;
   POSITION
                : Types.POSITION_TYPE;
 end record;
```

```
.....
--% UNIT: Status Package Spec.
-- X Effects: Maintains indicators and statistics on graphics display. --
--% Modifies: Flags are cleared in spec. when values are displayed.
--% Requires: Initialization must be signaled by main for first display.--
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: M. Sperry.
-----
-- | PACKAGE SPEC : Status
--|
-- | The purpose of the Status specification package is to provide visibility
-- to the data base which holds the requests from the mouse, et. al. The
--| requests are entered into a data table (called STATUS_CONTROL) and then
--| the table is checked to see if any updating of the statistics needs to be
--| done. The checking of the table is done at an atomic level to prevent
--| the shared data from being corrupted at critical times. The commands are
--| processed from the mouse interrupt as mode first, then reset if there are
-- | two commands to perform.
--1
-- Modifications Log
-- 88-11-08 : MPS => Original created.
with Types;
with Config;
package Status is
stack_size : constant := 252;
type MODE_TYPE is (AUTOMATIC, MANUAL);
type STATUS_TYPE is (AIRBORNE, TRACKED, EXPENDED, DESTROYED);
subtype RESET_STATUS_TYPE is STATUS_TYPE range EXPENDED..DESTROYED;
type STATUS_RECORD is record
 DATA : Types.WORD := 0;
                                          -- new statistic
 DISPLAYED : BOOLEAN := FALSE;
                                          -- need to display
end record;
type STATUS_TYPE_ARRAY is array(STATUS_TYPE'FIRST .. STATUS_TYPE'LAST) of
                                                       STATUS_RECORD;
-- define shared variables
```

```
MODE
               : MODE_TYPE := MANUAL;
MODE_DISPLAYED : BOOLEAN := FALSE;
STATUS_CONTROL : STATUS_TYPE_ARRAY;
REQ_COUNT
               : Types.WORD := 0;
STATUS ERROR
             : EXCEPTION;
                                               -- if data negative
-- define subprograms and tasks
procedure Initialize;
                                               -- initialization of screen
task type Update_Type is
 entry Signal;
  pragma PRIORITY(Config.update_priority);
end Update_Type;
for Update_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                              stack_size);
Update
               : Update_Type;
end Status;
```

```
--% UNIT: Status Package Body.
--% Effects: Maintains indicators and statistics on graphics display. --
--% Modifies: Flags are cleared in spec. when values are displayed.
--% Requires: Initialization must be signaled for first display.
                                                                       - -
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: M. Sperry.
-- | PACKAGE BODY : Status
--1
--| The purpose of the status package body is the implementation of the status
-- | update task. Although operating at a low priority, the update task updates
--| the various statistics by a rendezvous with the graphics task.
--1
-- Modifications Log
-- 88-11-08 : MPS => Original created.
with Graphics;
with Interrupt_Control;
with Shapes;
with Machine_Dependent;
with Interrupt_Control;
with Debug_IO;
with Time_Stamp;
pragma ELABORATE(Graphics, Interrupt Control, Debug 10, Time_Stamp);
package body Status is
use Types;
                                              -- for visibility to '+';
procedure Initialize is
-- | SUBPROGRAM BODY : Status.Initialize
---
--| This procedure is responsible for performing a rendezvous with graphics
--| for the purpose of printing the statistics titles. After this has been
-- | done, this procedure signals the Status. Update task causing the initial
-- | values of all the statistics to appear as well.
--1
type TITLE REC_TYPE is record
 X,Y : Types.WORD;
  TEXT : STRING(1..Config.stats_title_max_length);
  COLOR : Graphics.COLOR_TYPE;
end record;
```

.....

```
EMPTY : constant STRING := "";
TITLES: array(1..Config.number_of_titles) of TITLE_REC_TYPE :=
         ((0,0, "Airborne ", Graphics.status_color),
          (0,1, " Rockets: ", Graphics.status_color),
          (0,3, "Tracked ",Graphics.status_color),
          (0,4, " Targets: ", Graphics.status_color),
          (0,8, "Totals ", Graphics.status_color),
          (0,10,"Expended ",Graphics.status_color),
          (0,11," Rockets: ", Graphics.status_color),
          (0,13,"Destroyed ",Graphics.status_color),
          (0,14," Targets: ",Graphics.status_color),
          (0,18,"Mode:
                           ",Graphics.status_color),
          (0,20," Manual ",Graphics.status_color),
          (0,22," Automatic", Graphics.status_color));
begin
  for I in 1.. Config.number of titles loop
    Graphics.Display.Print_Titles(TITLES(I).X,TITLES(I).Y,
                                  TITLES(1).TEXT,
                                  TITLES(I).COLOR);
  end loop;
  Graphics.Display.Print_Titles(0,0,EMPTY,Graphics.status_color);
  Interrupt Control.Disable;
  Status.REQ COUNT := Status.REQ COUNT + 1; -- signal a request (print zeroes)
  Interrupt Control.Enable;
                                             -- display statistics values
  Status.Update.Signal;
end Initialize:
task body Update_Type is
--|
-- | TASK BODY : Status.Update
--| This task is used as a low priority task which ensures that updates to
--| the statistics are performed. Only those stats which have changed since
-- | the last update are written to the screen.
--1
                                                 -- for visibility to "+"
use Types;
                                                -- column that status_box starts in x
                  : constant := 11;
x start
x_end
                                                -- end column of status box
                  : constant := 90;
                                                -- status_box top AUTOMATIC
y_top_start_A
               : constant := 307;
y_bottom_start_A : constant := 322;
                                                -- status_box bottom AUTOMATIC
                                                -- status_box top MANUAL
y_top_start_M
                  : constant := 278;
y_bottom_start_M : constant := 293;
                                                -- status box bottom MANUAL
manual_offset : constant := 29;
                                                -- offset to draw status_box
                : constant := 1;
                                               -- range of components that
box_start
                                                -- make up status_box.
box_end
                 : constant := 4;
                 : constant Types.COORDINATE := 120; -- x end of all statistics
base_x
```

```
airborne_y
                : constant Types.COORDINATE := 25; -- y location of stat
tracked y
                : constant Types.COORDINATE := 67; -- y location of stat
expended_y
                : constant Types.COORDINATE := 165; -- y location of stat
destroyed_y
                 : constant Types.COORDINATE := 207; -- y location of stat
y_statistics
                 : constant array(STATUS_TYPE'first .. STATUS_TYPE'last) of
       Types.COORDINATE := (airborne_y, tracked_y, expended_y, destroyed_y);
type STATUS OLD is array(STATUS TYPE'first .. STATUS TYPE'last,
                     1 .. Config.statistics_length) of Graphics.MOVE_RECORD;
NEXT MODE
                 : MODE TYPE;
DISPLAY REQUIRED : BOOLEAN;
NEXT_DATA
             : Types.WORD;
BOX LIST
                : Graphics.MOVE_LIST_TYPE(Types.WORD_INDEX range box_start..box_end);
DATA OLD
                : STATUS OLD;
WORK LIST
                : Graphics.MOVE_LIST_TYPE(1 .. Config.statistics_length);
MOVE_PRIORITY : Graphics.PRIORITY_TYPE := Graphics.LOW;
procedure Initialize is
--| SUBPROGRAM BODY : Status.Update.Initialize
-- A procedure which intializes the DATA_OLD data base. This procedure does
-- NOT cause the digits to be drawn. Then, it initializes the status box
--| around 'manual'. Again, it does not cause the status_box to be drawn. A
-- | wakeup call from the main task will cause it to be drawn.
--1
begin
  for I in STATUS TYPE'first .. STATUS TYPE'last loop
    for J in 1 .. Config.statistics_length loop
     DATA_OLD(I,J).XY_OLD := (Types.COORDINATE(base_x),Types.COORDINATE(y_statistics(1)));
      DATA_OLD(I,J).XY_NEW := (Types.COORDINATE(base_x),Types.COORDINATE(y_statistics(1)));
      DATA OLD(I,J).OBJECT := Shapes.ZERO;
      DATA_OLD(1,J).COLOR := Graphics.status_color;
    end loop;
  end loop;
-- Now initialize top of status_box
  BOX_LIST(1).XY_OLD :=
              (Types.COORDINATE(x_start), Types.COORDINATE(y top start A));
  BOX_LIST(1).XY_NEW :=
              (Types.COORDINATE(x_start), Types.COORDINATE(y_top_start_A));
  BOX_LIST(1).OBJECT := Shapes.HORIZONTAL;
  BOX_LIST(1).COLOR := Graphics.status_box_color;
-- define bottom of status box
```

```
BOX_LIST(2).XY_OLD :=
              (Types.COORDINATE(x_start), Types.COORDINATE(y_bottom_start_A));
 BOX_LIST(2).XY_NEW :=
              (Types.COORDINATE(x_start), Types.COORDINATE(y_bottom_start_A));
 BOX_LIST(2).OBJECT := Shapes.HORIZONTAL;
 BOX_LIST(2).COLOR := Graphics.status_box_color;
-- define left side of status box
 BOX_LIST(3).XY_OLD :=
              (Types.COORDINATE(x_start), Types.COORDINATE(y_top_start_A));
 BOX_LIST(3).XY_NEW :=
              (Types.COORDINATE(x_start), Types.COORDINATE(y_top_start_A));
 BOX_LIST(3).OBJECT := Shapes.VERTICAL;
 BOX_LIST(3).COLOR := Graphics.status_box_color;
-- define right side of status_box
 BOX_LIST(4).XY_OLD :=
              (Types.COORDINATE(x_end), Types.COORDINATE(y_top_start_A));
 BOX_LIST(4).XY_NEW :=
              (Types.COORDINATE(x_end), Types.COORDINATE(y_top_start_A));
 BOX_LIST(4).OBJECT := Shapes.VERTICAL;
 BOX_LIST(4).COLOR := Graphics.status_box_color;
  when others => Debug_IO.Put_Line("Exception raised in Status.Initialize");
end Initialize;
procedure Update_Box(NEXT MODE : MODE TYPE) is
--|
--| SUBPROGRAM BODY : Status.Update.Update_Box
     A procedure which updates the four objects which represent the status_box
-- | surrounding one of the modes.
OFFSET : Types.WORD;
begin
                           --$TP(0078) Status.Update_Box start
 Time_Stamp.Log(0078);
  if NEXT_MODE = AUTOMATIC then
                                          -- draw status_box at 'automatic'
    BOX_LIST(1).XY_NEW.Y := Types.COORDINATE(y_top_start_A);
    BOX_LIST(2).XY_NEW.Y := Types.COORDINATE(y_bottom_start_A);
    BOX_LIST(3).XY_NEW.Y := Types.COORDINATE(y_top_start_A);
   BOX_LIST(4).XY_NEW.Y := Types.COORDINATE(y_top_start A);
                                           -- draw status_box at 'manual'
    BOX_LIST(1).XY_NEW.Y := Types.COORDINATE(y_top_start_M );
    BOX_LIST(2).XY_NEW.Y := Types.COORDINATE(y_bottom_start_M);
```

```
BOX_LIST(3).XY_NEW.Y := Types.COORDINATE(y_top_start_M);
    BOX LIST(4).XY NEW.Y := Types.COORDINATE(y top start M);
 end if;
-- Rendezvous with Graphics to draw new status_box
 Time_Stamp.Log(0079);
                           --$TP(0079) Status.Update_Box rendezvous with Graphics start
 Graphics.Display.Move(MOVE_PRIORITY, BOX_LIST(Types.WORD_INDEX range box_start..box_end));
 Time_Stamp.Log(008.
                           -- $TP(0080) Status. Update Box rendezvous with Graphics end
-- Update status_box lists
  for I in Types.WORD_INDEX range box_start .. box_end loop
    BOX_LIST(1).XY_OLD := BOX_LIST(1).XY_NEW;
 end loop;
 Time_Stamp.Log(0081);
                           --$TP(0081) Status.Update_Box end
end Update_Box;
procedure Display_Digits(NEXT_DATA : in out Types.WORD;
                                 : STATUS_TYPE) is
                         STAT
-- | SUBPROGRAM BODY : Status.Update.Display_Digits
--|
     A procedure which takes the DATA_OLD numbers, divides by 10 to get a
--| single digit. That digit is used as an index into Shapes.NUMERIC, which
--| holds values to draw that number for Graphics. It updates DATA_OLD in the
--1
DIGIT
          : Types.WORD;
STAT_X_LOC : Types.COORDINATE;
begin
  Time_Stamp.Log(0082);
                           --$TP(0082) Status.Display_Digits start
-- Erase previous data
  for I in 1 .. Config.statistics_length loop
   DATA_OLD(STAT,I).COLOR := Graphics.background_color;
    WORK_LIST(Types.WORD_INDEX(I)) := DATA_OLD(STAT,I);
  end loop;
 Time_Stamp.Log(0083);
                           --$TP(0083) Status.Display_Digits rendezvous with Graphics(1) start
 Graphics.Display.Move(MOVE_PRIORITY, WORK_LIST);
  Time Stamp.Log(0084);
                           - STP(0084) Status.Display_Digits rendezvous with Graphics(1) end
-- Move new into old, then display
  STAT_X_LOC := base_x;
  for I in reverse 1 .. Config.statistics_length loop
```

```
DIGIT := NEXT_DATA mod 10;
                                                 -- get rightmost digit
   DATA_OLD(STAT,I).OBJECT:= Shapes.NUMERIC(INTEGER(DIGIT));
   DATA_OLD(STAT,I).COLOR := Graphics.status_color;
   DATA_OLD(STAT, I).XY_NEW.X := STAT_X_LOC;
    STAT_X_LOC := STAT_X_LOC - Shapes.number_width; -- moving left
    WORK_LIST(Types.WORD_INDEX(I)) := DATA_OLD(STAT,I);
    NEXT_DATA := NEXT_DATA / 10;
                                                 -- get next digit
  end loop;
                           --$TP(0085) Status.Display_Digits rendezvous with Graphics(2) start
  Time_Stamp.Log(0085);
  Graphics.Display.MOVE(MOVE PRIORITY, WORK LIST);
  Time_Stamp.Log(0086); --$TP(0086) Status.Display_Digits rendezvous with Graphics(2) end
  Time Stamp.Log(0087);
                          --$TP(0087) Status.Display_Digits end
exception
  when others =>
    Debug_IO.Put_Line("Exception raised in Status.Display_Digits");
end Display Digits;
-- body of UPDATE task
Begin
  Initialize; -- inside task body call to initialize data structures, et. al.
  loop
                              -- $TP(0114) Status task start
    Time_Stamp.Log(0114);
   Time_Stamp.Log(0115);
                             -- $TP(0115) Status accept Signal start
    accept Signal;
    Time_Stamp.Lcg(0088);
                              --$TP(0088) Status accept Signal end
    Interrupt_Control.Enable;
                                                 -- exception block
    begin
        Interrupt_Control.Disable;
        DISPLAY REQUIRED := not MODE DISPLAYED;
        NEXT_MODE := MODE;
        MODE DISPLAYED := TRUE;
        Interrupt_Control.Enable;
        if DISPLAY_REQUIRED then
                                                  -- update new status box
          Update_Box(NEXT_MODE);
        end if;
        for I in STATUS_TYPE'first .. STATUS_TYPE'last loop
          Interrupt_Control.Disable;
          DISPLAY_REQUIRED := not STATUS_CONTROL(1).DISPLAYED;
          NEXT_DATA := STATUS_CONTROL(I).DATA;
          STATUS_CONTROL(1).DISPLAYED := TRUE;
          Interrupt_Control.Enable;
          if DISPLAY REQUIRED then
            Display_Digits(NEXT_DATA,!);
          end if;
        end loop;
        Interrupt_Control.Disable;
        REQ_COUNT := REQ_COUNT - 1;
```

```
exit when REQ_COUNT = 0;
    Interrupt_Control.Enable;
    end loop;
    Time_Stamp.Log(0089);    --$TP(0089) Status task end
    exception
    when others => Debug_IO.Put_Line("Exception raised in Status task");
    end;
    end loop;
end Update_Type;
```

```
--% UNIT: Sync Package Spec.
--% Effects: No current use. Will provide greater synchronize in futr.--
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | PACKAGE SPEC : Sync
--| Package Sync contains a time type for use in synchronizing message
-- | reception and transferring.
--|
-- Modifications Log
-- 88-11-25 : TEG => Original created.
-- 89-11-22 : MPS => Created the SEQ_TYPE to keep track of messages across
                  the net synchronized with respect to time.
with Types;
package Sync is
 type SEQ_TYPE is new Types.WORD_INDEX;
end Sync;
```

```
--% UNIT:
           Target Package Spec.
--% Effects: Provides structure for BDS Target management.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | PACKAGE SPEC : Target
--1
--| Package Target provides target tracking and display management. In
--| addition, it provides the data structures necessary to keep a list
--| of targets alive. These data bases are accessed in a guaranteed mutually
--| exclusive way, since more than one task accesses the data structures
--| declared here. The TARGET_DATA_TYPE uses a record representation clause
--| because the number of allowed targets is a relatively large number. This
--| number is defined in the constant Config.max_targets. The clause reduces
--| the number of words necessary from three to one. Although pragma PACK
-- | may have also been used to limit the amount of traffic through the
-- rendezvous, it would not have been standard (i.e., the bit ordering may
-- | have been different from implementation to implementation).
--1
-- Modifications Log
-- 88-11-12 : TEG => Original created.
with Types;
with Config;
package Target is
  track_stack_size : constant := 3928;
  track_data_stack_size : constant := 1506;
  subtype TARGET_ID_TYPE is Types.WORD_INDEX range 0..Config.max_targets;
  type TARGET ITEM TYPE is record
                                 -- provides individual target information
   TARGET ID : TARGET ID TYPE;
   POSITION
                 : Types.POSITION_TYPE;
   TARGET_CLASS : Types.TARGET_CLASS_TYPE;
  end record;
  type TARGET_LIST_TYPE is
                                 -- list of all available targets items
            array(Types.WORD_INDEX range <>) of TARGET_ITEM_TYPE;
                                 -- incoming message from Sensor
  type TARGET_MSG_TYPE is record
   NUM_TARGETS
                : Types.WORD_INDEX;
```

```
: TARGET_LIST_TYPE(Types.TARGET_INDEX_TYPE);
   TARGET LIST
 end record;
 type TARGET_STATUS_TYPE is record
             : BOOLEAN;
   ACTIVE
                    : BOOLEAN;
   ENGAGED
   CLASS
                    : Types.TARGET_CLASS_TYPE;
 end record;
   for TARGET_STATUS_TYPE use record
     ACTIVE at 0 range 0..0;
     ENGAGED at 0 range 1..1;
     CLASS at 0 range 2..3;
   end record;
 type TARGET_DATA_TYPE is record
   STATUS : TARGET_STATUS_TYPE;
POSITION_NEW : Types.POSITION_TYPE;
POSITION_OLD : Types.POSITION_TYPE;
 end record;
 type TARGET_DATA_LIST_TYPE is -- used to communicate with Rocket.Control
   array(Types.TARGET_INDEX_TYPE) of TARGET_DATA_TYPE;
 task type Track_Type is
   entry Start;
   pragma PRIORITY(Config.track_priority);
 end Track_Type;
 for Track_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                                 track_stack_size);
 Track
                      : Track_Type;
 task type Track Data Type is
   entry Put(DATA
                    : in TARGET_DATA_LIST_TYPE; -- put new list
             NEXT_ENGAGE : out TARGET_ID_TYPE; -- get new engagement
             NEXT_DISENGAGE : out TARGET_ID_TYPE); -- and disengagement
                            : out TARGET DATA_LIST TYPE; -- get new list
   entry Get(DATA
             NEXT ENGAGE : in TARGET ID TYPE; -- put new engagement
             NEXT_DISENGAGE : in TARGET_ID_TYPE); -- and disengagement
   pragma PRIORITY(Config.track_data_priority);
 end Track_Data_Type;
 for Track_Data_Type'STORAGE_SIZE use INTEGER(Config.bytes_per_storage_unit *
                                                  track_data_stack_size);
 Track_Data
                      : Track_Data_Type;
end Target; -- package specification
```

```
--% UNIT: Target Package Body.
--% Effects: Provides structure for BDS Target management.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
--% PACKAGE BODY: Target
--|
--| Package Target provides target tracking and display management.
--|
-- Modifications Log
--
-- 88-12-03: TEG => Original created.
--

package body Target is
task body Track_Type is separate;
task body Track_Data_Type is separate;
```

.,....

```
--% UNIT:
           Targ_Sup Task Body Subunit.
--% Effects: Provides Simulator motion control for all targets.
--% Modifies: Modifies TARGETS and TARGET_HISTORY global data.
--% Requires: No initialization is required.
--% Raises: TARGET_CREATE_ERROR if told to create when max exceeded. --
--% Engineer: M. Sperry.
-----
-- | TASK BODY : Simulate.Sensor.Targ_Sup
---
-- A task which sends a list to the caller describing new targets and
--| targets which have made it past the bottom border of the BDS and thus are
--- considered to have been destroyed since these targets are no longer the
--| concern of the BDS. These targets are described by not being on
-- | the list. Note that new targets are created first and then those
--| that need to be destroyed are processed. This task is timed so that
-- the list is ready only during 100 millisecond intervals. In an attempt
--| to generate random numbers, channel two on the timer chip is used.
--1
-- Modifications Log
-- 88-10-25 : MPS => Original created.
-- 89-08-08: MPS => All references to hardware were made to point to HW Config.
-- 89-11-29 : MPS => Re-structured Targ_Sup to use calls in body of Sensor.
with Calendar;
with Debug 10;
with Time_Stamp;
with HW_Config;
with Distrib;
  pragma ELABORATE(Calendar, Debug_IO, Time_Stamp, Distrib);
separate (Simulate.Sensor)
task body Targ_Sup_Type is
                                              -- for visibility to "-"
use Calendar;
                                              -- for visibility to "/" etc.
use Types;
CURRENT_NUM_OF_TARGETS : Types.WORD_INDEX;
                                                -- local count of targets
TARGET COUNTER
                : Types.WORD_INDEX;
                                                 -- Target index for array
TEMP
                     : Types.POSITION_TYPE;
                                                 -- for fixed compiler bug
START_TIME
                  : Calendar.TIME;
DELAY PERIOD
                    : DURATION;
NEW_TARGET_ID
                    : Types.TARGET_INDEX_TYPE;
```

```
-- Targ_Sup task body
begin
  CURRENT_NUM_OF_TARGETS := 0; -- no targets - yet.
  Initialize;
-- Take the time.
  START_TIME := Calendar.Clock;
    Time_Stamp.Log(0092);
                            --$TP(0092) Targ_Sup task start
    START_TIME := START_TIME + Config.interval;
-- Check number of Targets; if less than maximum, then add a new
-- Target to the list.
    if CURRENT_NUM_OF_TARGETS < Distrib.NUM_TARGETS then
      NEW_TARGET_ID := Get_New_ID;
     Activate_Target(NEW_TARGET_ID); -- initializes TARGETS and TARGET_HISTORY
    end if;
-- Move each target.
    for ID in Types.TARGET_INDEX_TYPE loop
     if TARGETS(ID).ACTIVE then
        Get_New_Position(ID); -- updates TARGETS(ID).POSITION
      end if;
    end loop;
-- See if any targets made it to the enemy line.
-- These targets are no longer the concern of the BDS. They
-- are deleted from the list.
    for ID in Types.TARGET INDEX TYPE loop
      Interrupt_Control.Disable;
      if TARGETS(ID).ACTIVE then
        if TARGETS(ID).POSITION.Y < Config.launch_y then</pre>
          CURRENT_NUM_OF_TARGETS := CURRENT_NUM_OF_TARGETS - 1;
          TARGETS(ID).ACTIVE := FALSE;
        end if;
      end if;
      Interrupt_Control.Enable;
    end loop;
-- Move the list into the target list kept by the target spec.
    Time_Stamp.Log(0093);
                             --$TP(0093) Targ_Sup accept Next_Target_Msg start
    accept Next_Target_Msg(DATA : out Target.TARGET_MSG_TYPE) do
      TARGET COUNTER := 0;
      for 1D in Types.TARGET_INDEX_TYPE loop
```

```
Interrupt_Control.Disable;
       if TARGETS(ID).ACTIVE then
         TARGET_COUNTER := TARGET_COUNTER + 1;
         TEMP := TARGETS(ID).POSITION; -- fixed compiler code bug
         DATA.TARGET_LIST(TARGET_COUNTER).POSITION := TEMP;
         DATA.TARGET_LIST(TARGET_COUNTER).TARGET_CLASS :=
                                           TARGETS(ID).TARGET_CLASS;
         DATA.TARGET_LIST(TARGET_COUNTER).TARGET_ID := ID;
       end if;
       Interrupt_Control.Enable;
     end loop;
-- Update number of active targets in the BDS.
     CURRENT_NUM_OF_TARGETS := TARGET_COUNTER;
     DATA.NUM_TARGETS := TARGET_COUNTER;
   end Next_Target_Msg;
   Time_Stamp.Log(0094);
                             --$TP(0094) Targ_Sup accept Next_Target_Msg end
-- Schedule next list out.
   DELAY_PERIOD := START_TIME - Calendar.Clock;
    if DELAY_PERIOD < 0.0 then
      START_TIME := Calendar.Clock;
    end if;
                             --$TP(0095) Targ_Sup end
   Time_Stamp.Log(0095);
   delay DELAY_PERIOD;
 end loop;
-- accept Clock(Time : in Sync.TIME_TYPE); --TBD
end Targ_Sup_Type;
```

```
Track Task Body Subunit.
-- X Effects: Provides all target tracking and display for BDS.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- | TASK BODY : Target.Track
--|
-- The TRACK task is used to control all of the target display information.
-- It accepts data from the Sensor and maintains it for the Rocket.Control
-- | task. It is responsible for accepting the information on the targets
-- and giving that information (in the form of a Graphics.WORK_LIST) to
--| the Graphics task. This routine can be contrasted to Rocket.Control which
--| performs many similiar functions for the rockets.
--| Unlike the Rocket.Control task however, there is no intermediate buffer
--| task which will allow for schedule slippage like the one between the
-- | Rocket.Control and the Simulate.RDL.Rock_Sup task.
--| There is a timing loop done in this task since the rest of the system
-- | derives its timing from this task and the Rocket.Control task. It contains
-- its own timing mechanism so that if one of the tasks (or possibly another
--| processor) goes down, the entire BDS won't be locked up.
--1
-- Modifications Log
-- 88-10-04 : TEG => Orignal created.
with Graphics;
with Shapes;
with Interrupt_Control;
with Grid_to_Pixel;
with Simulate;
with Debug 10;
with Status;
with Time Stamp;
  pragma ELABORATE(Graphics, Shapes, Interrupt_Control, Grid_to_Pixel,
                 Simulate, Debug_IO, Status, Time_Stamp);
separate (Target)
task body Track Type is
  use Types;
  package Sensor renames Simulate.Sensor; -- make simulation transparent
                                -- for operators only
  use Types;
  TARGET_MSG : TARGET_MSG_TYPE;
  MOVE_TARGETS : Graphics.MOVE_LIST_TYPE(Types.TARGET_INDEX_TYPE);
  MOVE INDEX
                : Types.WORD_INDEX;
```

.....

```
DESTROYED
                 : Types.WORD;
 CREATED
                 : Types.WORD;
 PIXEL_POINT
                : Shapes.PIXEL;
 TARGETS
                 : TARGET_DATA_LIST_TYPE;
 MSG_INDEX
                 : Types.WORD_INDEX;
 NEXT_ENGAGED
                                        -- 0 if no new engagement
                 : Types.WORD_INDEX;
 NEXT_DISENGAGED : Target.TARGET_ID_TYPE;-- keep track of disengagements
                 : Graphics.COLOR_TYPE;
 ENGAGE FLAG
                 : BOOLEAN;
 CLASS
                 : Types.TARGET_CLASS_TYPE;
 POSITION
                 : Types.POSITION_TYPE; -- temp for making changes
 ESCAPED_TARGETS: Types.WORD; -- targets which made it past the BDS border
 accept Start;
-- INITIALIZATION
  for I in TARGETS' range loop
   TARGETS(1).STATUS := (FALSE, FALSE, UNKNOWN); -- init to default
 end loop;
 loop
   Time_Stamp.Log(0096);
                             --$TP(0096) Track task start
   Time_Stamp.Log(0097);
                             --$TP(0097) Track rendezvous with Targ_Sup start
   Sensor.Targ_Sup.Next_Target_Msg(TARGET_MSG);
   Time_Stamp.Log(0098);
                             --$TP(0098) Track rendezvous with Targ Sup end
-- Zero out counters
   CREATED := 0;
   DESTROYED := 0;
   ESCAPED TARGETS := 0;
   Maintain history information.
-- Go through each target to examine its new status
   MSG INDEX := 1;
   MOVE_INDEX := 0;
   for TARGET_ID in TARGETS'RANGE Loop
      if TARGETS(TARGET_ID).STATUS.ACTIVE then
       if MSG_INDEX > TARGET_MSG.NUM_TARGETS or else
           TARGET_MSG.TARGET_LIST(MSG_INDEX).TARGET_ID
                                      /= TARGET_ID then -- target destroyed
-- Target has been destroyed, keep local accumulation of destroyed
   targets, and add to list for Display task to erase target.
         DESTROYED := DESTROYED + 1;
-- If this target has escaped the BDS, count it in the targets which escaped.
```

```
if TARGETS(TARGET_ID).POSITION_NEW.Y <= Config.launch_y then
           ESCAPED_TARGETS := ESCAPED_TARGETS + 1;
         end if;
-- To mark as inactive : (ACTIVE => FALSE, ENGAGED => FALSE, CLASS => UNKNOWN)
         TARGETS(TARGET_ID).STATUS := (FALSE, FALSE, Types.UNKNOWN);
         MOVE INDEX := MOVE INDEX + 1;
         PIXEL_POINT := Grid_To_Pixel(TARGETS(TARGET_ID).POSITION_NEW);
         COLOR := Graphics.background_color;
         MOVE_TARGETS(MOVE_INDEX) := (PIXEL_POINT,
                                       PIXEL_POINT,
                                        Shapes.TARGET,
                                        COLOR);
       else
                                                       -- move the target
-- Found a current existing target in the latest sensor report,
-- update target information and add it to move list.
         POSITION := TARGET_MSG.TARGET_LIST(MSG_INDEX).POSITION;
         MOVE INDEX := MOVE INDEX + 1;
         CLASS := TARGETS(TARGET_ID).STATUS.CLASS;
         ENGAGE_FLAG := TARGETS(TARGET_ID).STATUS.ENGAGED;
         COLOR := Graphics.target_color(CLASS, ENGAGE_FLAG);
         MOVE_TARGETS(MOVE_INDEX) :=
                   (XY_OLD => Grid_to_Pixel(TARGETS(TARGET_ID).POSITION_NEW),
                   XY_NEW => Grid_to_Pixel(POSITION),
                   OBJECT => Shapes.TARGET,
                   COLOR => COLOR
         TARGETS(TARGET_ID).POSITION OLD := TARGETS(TARGET_ID).POSITION_NEW;
          TARGETS(TARGET_ID).POSITION_NEW := POSITION;
         MSG_INDEX := MSG_INDEX + 1;
       end if;
                      -- new/old target check
      else
                                   -- this target wasn't previously active
        if MSG_INDEX <= TARGET_MSG.NUM_TARGETS and then
           TARGET_MSG.TARGET_LIST(MSG_INDEX).TARGET_ID
                                               = TARGET ID then -- new target
-- New Target has been created, set status and put it on display
         CREATED := CREATED + 1;
 -- mark as active
         TARGETS(TARGET_ID).STATUS :=
                  (TRUE,
                                -- ACTIVE
                   FALSE,
                                 -- Engaged
                   TARGET_MSG.TARGET_LIST(MSG_INDEX).TARGET_CLASS); -- class
         TARGETS(TARGET_ID).POSITION OLD :=
                                               -- set both old and new
                              TARGET MSG.TARGET LIST(MSG INDEX).POSITION;
         TARGETS(TARGET_ID).POSITION_NEW :=
```

```
TARGET_MSG.TARGET_LIST(MSG_INDEX).POSITION;
      MOVE_INDEX := MOVE_INDEX + 1;
      CLASS := TARGETS(TARGET_ID).STATUS.CLASS;
      ENGAGE FLAG := TARGETS(TARGE1 ID).STATUS.ENGAGED;
      COLOR := Graphics.target_color(CLASS, ENGAGE_FLAG);
      MOVE TARGETS(MOVE INDEX) :=
               (XY_OLD => Grid_to_Pixel(TARGETS(TARGET_ID).POSITION_OLD),
                XY_NEW => Grid_to_Pixel(TARGETS(TARGET_ID).POSITION_NEW),
                OBJECT => Shapes.TARGET,
                COLOR => COLOR
               );
      MSG_INDEX := MSG_INDEX + 1;
    end if;
                                      -- end of new target check
  end if;
                                      -- active check
end loop;
Now update status if any created or destroyed
if CREATED /= DESTROYED or DESTROYED > 0 then
  Interrupt_Control.Disable;
  Status.STATUS CONTROL(Status.TRACKED).DATA :=
     Status.STATUS_CONTROL(Status.TRACKED).DATA + (CREATED - DESTROYED);
 Status.STATUS_CommonL(Status.TRACKED).DISPLAYED := FALSE;
  Status.STATUS_CONTROL(Status.DESTROYED).DATA :=
                            Status.STATUS_CONTROL(Status.DESTROYED).DATA +
                            DESTROYED - ESCAPED TARGETS;
  Status.STATUS CONTROL(Status.DESTROYED).DISPLAYED := FALSE;
  Status.REQ_COUNT := Status.REQ_COUNT + 1;
  if Status.REQ_COUNT = 1 then
  Time_Stamp.Log(0099);
                           --$TP(0099) Track rendezvous with Status start
    Status.Update.Signal;
  Time_Stamp.Log(0100);
                           --$TP(0100) Track rendezvous with Status end
  end if;
  Interrupt Control.Enable;
end if;
Time Stamp.Log(0101);
                          --$TP(0101) Track rendezvous with Track_Data start
Target.Track_Data.Put(TARGETS, NEXT_ENGAGED, NEXT_DISENGAGED);
                                             -- send copy to Rocket.Control
Time_Stamp.Log(0102);
                          -- $TP(0102) Track rendezvous with Track_Data end
if NEXT_ENGAGED > 0 then
  TARGETS(NEXT ENGAGED).STATUS.ENGAGED := TRUE; -- set engaged
end if;
if NEXT_DISENGAGED > 0 then
  TARGETS(NEXT_DISENGAGED).STATU..ENGAGED := FALSE;
end if;
Time_Stamp.Log(0103);
                         --$TP(0103) Track rendezvous with Graphics start
Graphics.Display.Move(Graphics.LOW, MOVE_TARGETS(1..MOVE_INDEX));
Time_Stamp.Log(0104);
                         ---$TP(0104) Track rendezvous with Graphics end
Time_Stamp.Log(0105);
                         --$TP(0105) Track task end
```

```
end loop;
exception
when others =>
   Debug_IO.Put_Line("TRACK termination due to exception.");
end Track_Type;
```

```
--% UNIT:
            Track Data Task Subunit.
--% Effects: Provides buffering of target tracking data between the
             Track task and the Control task for rocket engagement.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
______
-- TASK BODY : Target.Track_Data
--1
     The Track_Data task is used to buffer the most recent target list
--| from the Target.Track task and provide it to the Rocket.Control
--| task. It also buffers new engagements or disengagements from the
-- | Rocket.Control task to notify the Target.Track task that a new target
-- | has been engaged or an old target destroyed.
     Note that only one new target can be engaged every update interval.
-- | If the NEXT_ENGAGE parameter is 0, this is an invalid TARGET_ID, and
-- | implies that no new target is engaged.
     Although there is a guard used here, it is only used for the first
--| rendezvous from Rocket.Control. This helps the BDS system to achieve a
-- | known initial state and asynchronous timing.
--|
-- Modifications Log
-- 88-10-11 : TEG => Original created.
with Time Stamp;
with Interrupt_Control;
  pragma ELABORATE(Time_Stamp,Interrupt_Control);
separate (Target)
task body Track_Data_Type is
  use Types;
  BUFFERED_DATA
                    : Target.TARGET_DATA_LIST_TYPE;
  BUFFERED_ENGAGE
                    : Target.TARGET_ID_TYPE;
  BUFFERED_DISENGAGE : Target.TARGET_ID_TYPE;
  DATA_COUNT
                    : Types.WORD := 0;
begin
-- Initialize local copy of data
-- initialize all target status to:
              (ACTIVE => FALSE, ENGAGED => FALSE, CLASS => UNKNOWN)
                                           -- default is no new engagement
  BUFFERED_ENGAGE := 0;
```

```
for I in BUFFERED_DATA range loop
   BUFFERED_DATA(I).STATUS := (FALSE, FALSE, Types.UNKNOWN);
 end loop;
 loop
   select
     accept Put(DATA
                             : in TARGET_DATA_LIST_TYPE;
                NEXT_ENGAGE : out TARGET_ID_TYPE;
                NEXT_DISENGAGE : out TARGET_ID_TYPE) do
       Time_Stamp.Log(0106);
                                --$TP(0106) Trackdat accept Put start
       Interrupt_Control.Disable; -- BUGFIX for RTE
       BUFFERED_DATA := DATA;
       Interrupt_Control.Enable;
       NEXT_ENGAGE := BUFFERED_ENGAGE;
       NEXT_DISENGAGE := BUFFERED_DISENGAGE;
       DATA_COUNT := 1;
       Time_Stamp.Log(0107); --$TP(0107) Trackdat accept Put end
     end Put;
     when DATA_COUNT > 0 =>
     accept Get(DATA
                             : out TARGET_DATA_LIST_TYPE;
                NEXT_ENGAGE : in TARGET_ID_TYPE;
                NEXT_DISENGAGE : in TARGET_ID_TYPE) do
                                --$TP(0108) Trackdat accept Get start
       Time_Stamp.Log(0108);
       Interrupt_Control.Disable; -- BUGFIX for RTE
       DATA := BUFFERED DATA;
       Interrupt_Control.Enable;
       BUFFERED_ENGAGE := NEXT_ENGAGE;
       BUFFERED_DISENGAGE := NEXT_DISENGAGE;
       DATA COUNT := 1;
       Time_Stamp.Log(0109); --$TP(0109) Trackdat accept Get end
     end Get:
   end select;
 end loop;
end Track_Data_Type;
```

```
--% UNIT: Traject Function Spec.
--% Effects: Computes rocket motion based on previous motion and
            aimpoints received in guidance messages.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: R. Chevier.
--| SUBPROGRAM SPEC : Traject
--1
--| Function Traject takes the current rocket information including the
-- direction it is headed in and determines the new absolute position
-- of the rocket. This work is done in a three dimensional system.
--|
-- Modifications Log
-- 88-10-29 : TEG => Original created.
-- 89-08-29 : MPS => Original replaced by R. Chevier's version.
with Types;
package Traject is
procedure Get_New_Position(ROCKET_ID : Types.WORD_INDEX;
                          AIMPOINT : Types.AIMPOINT_TYPE;
                          POS
                                      : out Types.POSITION_TYPE);
end Traject;
```

```
.....
--% UNIT: Traject Function Body.
--% Effects: Computes rocket motion based on previous motion and
       aimpoints received in guidance messages.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: R. Chevier
--| SUBPROGRAM BODY : Traject
--|
--| Function Traject: Is the trajectory planner for rockets and takes an
-- | Azimuth, Elevation X,Y,Z position and constant velocity and returns a new
-- | rocket position.
--1
-- Modifications Log
-- 88-12-01 : TEG => Original created.
-- 89-08-29: MPS => Replaced original with R. Chevier's version.
-- 89-09-07 : MPS => Added the Get_New_Position function
with Config;
with Parameter_Data_Base;
with Rocket;
with Math;
with Time_Stamp;
 pragma ELABORATE(Math);
package body Traject is
use Types;
                                -- for operators
use Math;
                                -- for faster fixed math
bam_converter : constant Types.LONG_FIXED := 182.03125;
type DRIFT_RECORD_TYPE is record
  SIN_AZIMUTH : Types.LONG_FIXED := 0.0;
  SIN_ELEVATION : Types.LONG_FIXED := 0.0;
  COS_AZIMUTH : Types.LONG_FIXED := 0.0;
  COS_ELEVATION : Types.LONG_FIXED := 0.0;
end record;
type VELOCITY_RECORD_TYPE is record
 X : Types.LONG_FIXED := 0.0;
  Y : Types.LONG_FIXED := 0.0;
  Z : Types.LONG_FIXED := 0.0;
end record;
```

```
type LOCAL_ROCKET_REC is record
 ACTIVE : BOOLEAN := FALSE;
 POSITION : Types.POSITION_TYPE;
 VELOCITY : VELOCITY RECORD TYPE;
 ANGLE : Types.AIMPOINT_TYPE;
  FUEL
          : Types.LONG_FIXED;
end record;
type ROCKET HISTORY REC is record
 LOCAL_ROCKET : LOCAL_ROCKET_REC;
                : Rocket.ROCKET_GUIDE_TYPE;
 GUIDANCE
 ROCKET_DEFAULTS : Parameter_Data_Base.ROCKET_PARAMETER_TYPE;
            : DRIFT_RECORD_TYPE;
 DELTA_T
                : Types.RATE_TYPE := Types.RATE_TYPE(Config.interval);
end record;
type ROCKET_HISTORY_ARRAY is array(Types.ROCKET_INDEX_TYPE) of
            ROCKET_HISTORY REC;
ROCKET_HISTORY : ROCKET_HISTORY_ARRAY;
procedure Initialize(INDIVIDUAL_ROCKET_HISTORY : in out ROCKET_HISTORY_REC) is
  INDIVIDUAL_ROCKET_HISTORY.LOCAL ROCKET.ACTIVE := TRUE;
  INDIVIDUAL_ROCKET_HISTORY.LOCAL_ROCKET.VELOCITY := (0.0,0.0,0.0);
  INDIVIDUAL_ROCKET_HISTORY.LOCAL_ROCKET.ANGLE := (Config.launch_azimuth,
                                                 Config.launch_elevation);
  INDIVIDUAL_ROCKET_HISTORY.LOCAL_ROCKET.FUEL := Parameter_Data_Base.c_fuel;
  INDIVIDUAL_ROCKET_HISTORY.LOCAL_ROCKET.POSITION := (Config.launch x,
                                            Config.launch_y,Config.launch_z);
end Initialize;
procedure Turn_Rocket
         (FUEL
                       : in out Types.LONG_FIXED;
          ROCKET_ANGLE : in out Types.BAM;
          BDS_ANGLE : Types.BAM;
          DELTA_T
                        : Types.RATE TYPE;
          TURN_RATE
                        : Types.LONG_FIXED;
          TURN_BURN_RATE : Types.LONG_FIXED) is
  MAX_TURN
                : Types.LONG FIXED;
  DELTA_ANGLE : Types.LONG_FIXED;
           : Types.LONG_FIXED;
  FUEL_USED
               : Types.LONG_FIXED;
  BURN_TIME
  BAMS_TURNED : Types.LONG_FIXED;
  DEGREES_TO_TURN : Types.LONG_FIXED;
begin
                                                   --Turn_Rocket
                          --$TP(0118) Traject.Turn_Rocket start
  Time_Stamp.Log(0118);
  DELTA_ANGLE := Types.LONG FIXED(BDS_ANGLE) - Types.LONG FIXED(ROCKET ANGLE);
```

```
if DELTA_ANGLE /= 0.0 and FUEL > 0.0 then
                                             -- don't turn it if told not to
   MAX_TURN := DELTA_T * TURN_RATE;
-- If the rotation in this iteration turns the rocket too far
-- then calculate only the fuel needed to rotate the rocket
-- the required amount.
   DEGREES_TO_TURN := abs DELTA_ANGLE / bam_converter;
    if DEGREES_TO_TURN < MAX_TURN then
      BURN_TIME := abs DEGREES_TO_TURN / TURN_RATE;
     FUEL_USED := TURN_BURN_RATE * abs DEGREES_TO_TURN;
      ROCKET_ANGLE := BDS_ANGLE;
      --Put("BDS Angle
                               :"); Int IO.Put(BDS ANGLE); New Line;
      --Put("Rocket Angle
                               :"); Int_IO.Put(ROCKET_ANGLE); New_Line;
      --Put("Delta Angle
                             :"); Long_Fxd_IO.Put(DELTA_ANGLE); New_Line;
                             :"); Long_Fxd_10.Put(TUPN_RATE); New_Line;
      --Put("Turn Rate
      --Put("Turn Burn Rate :"); Long_Fxd_10.Put(TURN_BURN_RATE); New_Line;
     --Put("Burn Time
                               :"); Long_Fxd_IO.Put(BURN_TIME); New_Line;
     --Put("Completed Turn Fuel:"); Long Fxd IO.Put(FUEL USED); New Line;
    else
-- Or if the time step was not large enough for rotation
-- completion then calculate FUEL used and new ANGLE based on
-- rotation completed during this step.
      FUEL_USED := TURN_BURN_RATE * MAX_TURN;
      if DELTA ANGLE < 0.0 then
                                        -- subtract from current direction
       BAMS_TURNED := MAX_TURN * bam_converter;
       ROCKET_ANGLE := ROCKET_ANGLE - Types.BAM(BAMS_TURNED);
                                           -- add to current direction
       BAMS_TURNED := MAX_TURN * bam_converter;
       ROCKET_ANGLE := ROCKET_ANGLE + Types.BAM(BAMS_TURNED);
     end if:
    end if;
    FUEL := FUEL - FUEL_USED;
    if FUEL < 0.0 then
     FUEL := 0.0;
    end if;
  end if;
  Time Stamp.Log(0119); -- $TP(0119) Traject.Turn Rocket end
end Turn_Rocket;
procedure Calc_Trajectory
         (LOCAL_ROCKET : in out LOCAL ROCKET REC;
          GUIDANCE : Rocket.ROCKET_GUIDE_TYPE;
          ROCKET_DEFAULTS : Parameter_Data_Base.ROCKET_PARAMETER_TYPE;
                      : DRIFT RECORD TYPE;
          DRIFT
                         : Types.RATE_TYPE) is
          DELTA T
```

```
drag
                 : constant Types.LONG FIXED := 0.984375:
                   -- roughly 2% of velocity per iteration
gravity
                 : constant Types.LONG FIXED := 9.80665;
VX
                 : Types.LONG_FIXED := (LOCAL_ROCKET.VELOCITY.X);
                 : Types.LONG_FIXED := (LOCAL ROCKET.VELOCITY.Y);
٧Z
                 : Types.LONG_FIXED := (LOCAL ROCKET.VELOCITY.Z);
Х
                 : Types.LONG_FIXED :=Types.LONG_FIXED(LOCAL_ROCKET.POSITION.X);
                 : Types.LONG_FIXED :=Types.LONG_FIXED(LOCAL_ROCKET.POSITION.Y);
                 : Types.LONG_FIXED :=Types.LONG_FIXED(LOCAL_ROCKET.POSITION.Z);
7
ELEVATION
                                    := Types.BAM(LOCAL ROCKET.ANGLE.ELEVATION);
                 : Types.BAM
AZIMUTH
                 : Types.BAM
                                    := Types.BAM(LOCAL_ROCKET.ANGLE.AZIMUTH);
                 : Types.LONG_FIXED := LOCAL_ROCKET.FUEL;
FORWARD_VELOCITY : Types.LONG_FIXED;
                 : Types.LONG FIXED;
TOTAL_MASS
                 : Types.LONG_FIXED;
DRAG_FORCE
                 : Types.LONG FIXED:
AY,AX,AZ
                 : Types.LONG FIXED;
SIN ELEVATION
                 : Types.LONG_FIXED;
COS_ELEVATION
                 : Types.LONG_FIXED;
COS_AZIMUTH
                 : Types.LONG_FIXED;
SIN_AZIMUTH
                 : Types.LONG FIXED;
TEMP_VAL
                 : Types.LONG_FIXED;
begin
                                           -- Calc_Trajectory
  Time_Stamp.Log(0120);
                            -- $TP(0120) Traject.Calc_Position start
  SIN_ELEVATION := Math.Sin(ELEVATION);
  SIN AZIMUTH := Math.Sin(AZIMUTH):
  COS_ELEVATION := Math.Cos(ELEVATION);
  COS AZIMUTH := Math.Cos(AZIMUTH);
  TEMP_VAL := VX*VX + VY*VY + VZ*VZ;
  FORWARD_VELOCITY := Math.Sqrt(TEMP_VAL);
-- Check amount of fuel left.
  if FUEL <= 0.0 then
    THRUST := 0.0;
  else
    THRUST := ROCKET DEFAULTS.THRUST;
  end if;
  TOTAL_MASS := ROCKET_DEFAULTS.MASS + FUEL;
  --Put("Thrust
                    :"); Long_Fxd_IO.Put(THRUST); New Line;
  --Put("Drag_Force :"); Long_Fxd_IO.PUT(DRAG_FORCE); New_Line;
  --Put("Cos_Elev :"); Long_Fxd_IO.Put(COS_ELEVATION); New Line;
  --Put("Sin_Az
                   :"); Long_Fxd_IO.Put(SIN_AZIMUTH); New_Line;
  --Put("Total_Mass :"); Long_Fxd_IO.Put(TOTAL_MASS); New_Line;
    COMPUTE ACCELERATION IN EACH AXIS
 DRAG FORCE := 0.0:
                               -- for now, null out drag acceleration
  AY := ((THRUST - DRAG_FORCE) * COS_ELEVATION) * SIN_AZIMUTH;
  AY := AY / TOTAL MASS;
```

```
AX := ((THRUST - DRAG_FORCE) * COS_ELEVATION) * COS_AZIMUTH;
 AX := AX / TOTAL MASS:
 AZ := (THRUST - DRAG_FORCE) * SIN_ELEVATION;
 AZ := AZ - TOTAL MASS * gravity;
 AZ := AZ / TOTAL_MASS;
- lose % of velocity per/iteration due to drag
 LOCAL_ROCKET.VELOCITY.X := DELTA_T * AX + VX * drag;
 LOCAL_ROCKET.VELOCITY.Y := DELTA_T * AY + VY * drag;
 LOCAL_ROCKET.VELOCITY.Z := DELTA_T * AZ + VZ * drag;
-- Update position of rocket
 X := X + DELTA_T * LOCAL_ROCKET.VELOCITY.X;
 LOCAL_ROCKET.POSITION.X := X;
 Y := Y + DELTA T * LOCAL ROCKET. VELOCITY.Y;
 LOCAL_ROCKET.POSITION.Y := Y;
 Z := Z + DELTA_T * LOCAL_ROCKET.VELOCITY.Z;
 LOCAL_ROCKET.POSITION.Z := Z;
-- New_Line;
-- Put_Line("
                  Velocity Acceleration");
-- Long_Fxd_IO.Put(LOCAL_ROCKET.VELOCITY.X,6,2,0);
-- Long_Fxd_IO.Put(AX,6,2,0);
-- Put_Line("
                   X");
-- Long_Fxd_IO.Put(LOCAL_ROCKET.VELOCITY.Y,6,2,0);
-- Long_Fxd_10.Put(AY,6,2,0);
-- Put_Line("
                   Y");
-- Long_Fxd_IO.Put(LOCAL_ROCKET.VELOCITY.2,6,2,0);
-- Long_fxd_IO.Put(AZ,6,2,0);
-- Put_Line("
-- New_Line;
-- Check for impacts to speed up code
 if Z > 0.0 then
-- When finished with the calculation update the current mass.
   LOCAL_ROCKET.FUEL := FUEL - DELTA_T * ROCKET_DEFAULTS.BURN_RATE;
   if LOCAL_ROCKET.FUEL < 0.0 then
    LOCAL_ROCKET.FUEL := 0.0;
   end if;
-- Calculate rocket turns.
   Turn_Rocket (LOCAL_ROCKET.FUEL,
                LOCAL_ROCKET.ANGLE.ELEVATION,
                GUIDANCE.AIMPOINT.ELEVATION,
                DELTA_T, ROCKET_DEFAULTS.TURN_RATE,
```

```
ROCKET_DEFAULTS.TURN_BURN_RATE);
   Turn Rocket (LOCAL ROCKET.FUEL,
                LOCAL_ROCKET.ANGLE.AZIMUTH,
                 GUIDANCE.AIMPOINT.AZIMUTH,
                 DELTA_T, ROCKET_DEFAULTS.TURN_RATE,
                 ROCKET_DEFAULTS.TURN_BURN_RATE);
 end if;
 Time Stamp.Log(0121);
                            -- $TP(0121) Traject.Calc Position end
end Calc_Trajectory;
procedure Get_New_Position(ROCKET_ID : Types.WORD_INDEX;
                           AIMPOINT : Types.AIMPOINT_TYPE;
                                     : out Types.POSITION TYPE) is
begin
 Time_Stamp.Log(0110);
                            --$TP(0110) Traject Start
  if not ROCKET_HISTORY(ROCKET_ID).LOCAL ROCKET.ACTIVE then
    Initialize(ROCKET_HISTORY(ROCKET_ID));
 ROCKET_HISTORY(ROCKET_ID).GUIDANCE.AIMPOINT := AIMPOINT;
 Calc_Trajectory(ROCKET_HISTORY(ROCKET_ID).LOCAL_ROCKET,
                  ROCKET_HISTORY(ROCKET_ID).GUIDANCE,
                  ROCKET HISTORY(ROCKET ID).ROCKET DEFAULTS.
                  ROCKET_HISTORY(ROCKET_ID).DRIFT,
                  ROCKET_HISTORY(ROCKET_ID).DELTA_T);
 POS := ROCKET_HISTORY(ROCKET_ID).LOCAL_ROCKET.POSITION;
  if ROCKET_HISTORY(ROCKET_ID).LOCAL_ROCKET.POSITION.2 <= 0.0 then
    ROCKET_HISTORY(ROCKET_ID).LOCAL_ROCKET.ACTIVE := FALSE; -- kill the rocket
 end if;
 Time Stamp.Log(0111);
                            -- $TP(0111) Traject end
end Get_New_Position;
end Traject;
```

```
--% UNIT: Types Package Spec.
--% Effects: Provides general purpose data types.
--% Modifies: No global data is modified.
--% Requires: No initialization is required.
--% Raises: No explicitly raised exceptions are propagated.
--% Engineer: T. Griest.
-- PACKAGE SPEC : Types
---
--| This package contains all the global types needed for the BDS and the
--| simulator. The type WORD and its derivatives replace the type INTEGER
--| to increase portability. The type BAM is an acronym for a Binary Angle
--| Measurement and the transformation from degrees to BAMs is performed by
--| BAMs = 32767/180 * degrees. The BDS and the simulator use three
--| dimensional components and the screen (obviously) display of the event
-- | shows it in two dimensions only.
--1
-- Modifications Log
-- 88-10-10 : TEG => Original created.
-- 89-08-29: MPS => Added definitions for new rocket flight path equations.
with Config;
package Types is
type WORD is range -32768 .. 32767;
 for WORD'size use 16:
type WORD_INDEX is range 0 .. 32767;
 for WORD_INDEX'size use 16;
subtype ROCKET_INDEX_TYPE is WORD INDEX range 1..Config.max rockets;
subtype TARGET_INDEX_TYPE is WORD_INDEX range 1..Config.max_targets;
subtype COORDINATE is Types.WORD;
subtype REL_COORDINATE is Types.WORD;
type METERS is delta 0.125 range -Config.meters_in_battle_area ..
                                                Config.meters_in_battle_area;
type LONG_FIXED is delta 0.015625 range -33_554_432.0..33_554_431.0;
 for LONG_FIXED'size use 32;
-- RATE_TYPE is used to compute velocities and accel accurately (2**-16)
```

```
type RATE_TYPE is delta 1.525879E-5 range -32_768.0..32_767.0;
 for RATE TYPE'size use 32;
sqrt_large_number : constant := 2508.0; -- approx sqrt(LONG_FIXED'last)/4
type POSITION_TYPE is record
                                      -- for absolute position
                                       -- assume battlefield oriented ENU
 X
           : LONG_FIXED;
          : LONG FIXED;
 Z
           : LONG_FIXED;
end record;
type BAM is range -32768 .. 32767;
                                     -- binary angle measurement 32768/180
                                       -- East North Up origins (0)
type EXTENDED_BAM is new LONG_INTEGER; -- for large calculations
type AIMPOINT TYPE is record
 AZIMUTH : BAM;
 ELEVATION : BAM;
end record;
-- T80 - Main Battle Tank
-- SA9 - GASKIN surface to air missle launcher
-- BMP2 - Infantry Combat Vehicle
type TARGET_CLASS_TYPE is (UNKNOWN, T80, SA9, BMP2);
end Types;
```

```
-- % UNIT:
            Distrib Package Body.
-- | PACKAGE BODY : Distrib
--1
-- | OPERATION :
--| This package body makes calls to the runtime in order to obtain
-- | configuration values which are based on the number of available
-- | processors.
--|
-- Modifications Log
-- 88-12-05 : TEG => Original Created.
-- 89-12-06 : TEG => Enhanced to support dynamic configuration/reconfiguration
-- DISTRIBUTION CONTROL PARAMETERS
......
package body Distrib is
 type BOUND_TYPE is (LOW, HIGH);
 subtype GUIDE_RANGE is Types.WORD_INDEX range 1..Distrib.max_guide_tasks;
 ROCKET CONFIG : array (GUIDE RANGE, GUIDE RANGE, BOUND TYPE)
    of Types.WORD_INDEX :=
-- if 1 task, all rockets on #1
    (1 \Rightarrow (1 \Rightarrow (LOW \Rightarrow 1, HIGH \Rightarrow 20), 2 \Rightarrow (LOW \Rightarrow 1, HIGH \Rightarrow 1)),
-- if 2 tasks, 5 rockets on #1, 15 on #2
     2 => (1 => (LOW => 1, HIGH => 5), 2 => (LOW => 6, HIGH => 20)));
-- The follwing four functions provide configuration information based
-- on operator entered information and system configuration operations.
-- They are provided by the Distributed RunTime Environment
 function Get_Num_Rockets return Types.WORD_INDEX;
 function Get Num Targets return Types.WORD_INDEX;
 function Get_Num_Guide_Tasks return Types.WORD_INDEX;
 function Get_Master_Status return BOOLEAN;
_____
-- LATER DECLARATIVE ITEMS (BODIES)
-- RESTART is used to stop operation of the BDS and allow the operator
-- setup a different configuration. It is only called when the MODE
-- button is pressed while the RESET button is held down on the mouse.
-- The Ada body version simply locks up the machine with interrupts disabled.
```

.....

```
pragma INTERFACE(ASM86, Get_Num_Guide_Tasks);
 pragma INTERFACE_SPELLING(Get_Num_Guide_Tasks, "D1DRTE?GETTASKS");
 pragma INTERFACE(ASM86, Get_Num_Targets);
 pragma INTERFACE_SPELLING(Get_Num_Targets, "D1DRTE?GETTARGETS");
 pragma INTERFACE(ASM86, Get_Num_Rockets);
 pragma INTERFACE_SPELLING(Get_Num_Rockets, "D1DRTE?GETROCKETS");
 pragma INTERFACE(ASM86, Get_Master_Status);
 pragma INTERFACE_SPELLING(Get_Master_Status, "D1DRTE?GETMASTER");
begin
 NUM_ROCKETS := Get_Num_Rockets;
 NUM_TARGETS := Get_Num_Targets;
 NUM_GUIDE_TASKS := Get_Num_Guide_Tasks;
 MASTER := Get_Master_Status;
 for I in Types.WORD_INDEX range 1..NUM_GUIDE TASKS loop
   Guide_Low (I) := ROCKET_CONFIG(NUM_GUIDE_TASKS,I,LOW);
   Guide_High(I) := ROCKET_CONFIG(NUM_GUIDE_TASKS,I,HIGH);
 end loop;
end Distrib;
```

12 Appendix B - Distributed Runtime Source Code

The source code for the distributed runtime uses an 8086 family assembly language code. It is divided into modules which implement the major functional areas. These include: Initialization and system configuration, interprocessor synchronization, runtime routines, network setup, network I/O, distributed task control blocks, and the vendor runtime interface. Two include files: DA_HW.ASM and DA_DEF.ASM are used to define system constants and data structures.

```
.XLIST
FILE: DA_DEF.ASM
   Distributed Ada - Definitions
   Definitions for system values
   Copyright (C) 1989, LabTek Corporation
DEF VRTIF ADDR
                     equ
                            4000H
                            3
DEF_addr_size
                     equ
                                   ; # of WORDs in Ethernet Address
; NETWORK MESSAGE CONTROL FIELD VALUES
; The first 6 fields are constant for ALL network traffic
packet
                     struc
DEF_pkt_dest
                     dw
                            3 dup (?)
DEF_pkt_source
                     d₩
                            3 dup (?)
DEF_pkt_length
                     ₫₩
DEF_pkt_sequence
                     d₩
                            ?
DEF_pkt_cmd
                                 ; designate type of message
                            ?
                                  ; destinative task id
DEF_pkt_TID
                     ₫₩
DEF pkt entry ID
                     dw
DEF_pkt_my_PID
                     dw
                            ?
                                ; source processor ID
DEF_pkt_my_TID
                     dw
                                 ; source task ID
DEF_pkt_data
                     du
                                  ; data always starts here
packet
                     ends
; Offset from list pointer to next node pointers
DEF_next_ptr
                     equ
                                   ; offset to next pointer in buffer
DIR entry
              struc
DTCB_dir_local dw
                                   ; local/distrib runtime flag
DTCB_dir_pid
                     ?
                                   ; PID for this task
              dw
DTCB_dir_TCB
                     ?
                                   ; pointer to distrib TCB
DTCB_dir_COUNT dw
                     ?
                                   ; Counter for task type
DIR_entry
              ends
DTCB_dir_size
              equ
                     size DIR_ENTRY
                                             ; size of each entry
   TCB Offsets
DEF_tcb_reply
                     equ
                            6
DEF_return_addr
                     equ
                            8
DEF_num_entries
                           12
                     equ
DEF_entry_table
                     equ
   Within each TCB is an entry table
   The table contains a record for each entry with the following fields:
```

```
DEF entry rec
                 struc
DEF_entry_profile_ptr
                 ď₩
                       ?
DEF_entry_wait
DEF_entry_queue
                 d₩
                       ?
                       ?
DEF_entry_rec
                 ends
; CPU Designations
                       3
                                ; maximum number of CPUs
DEF_max_cpus
                 equ
DEF_alpha
                       0
                 equ
DEF_bravo
                       1
                 equ
DEF_charlie
                 equ
                       2
DEF_NA
                       - 1
                                ; not applicable (no CPU)
                 equ
; PROCESSOR / TASK / ENTRY IDs ;
; Note: PIDs increment by 6, ;
      TIDs and EIDs by 2.
; TASK IDs are unique.
; COMMANDS received via messages
0
DEF_sync_start
                 equ
DEF_sync_ready
                 equ
                       1
DEF_sync_continue
                       2
                 equ
DEF_request_entry
                       3
                 equ
                       4
DEF_rendezvous_end
                 equ
DEF local_call
                       5
                 equ
DEF_ACK
                       -1
                 equ
{\tt DEF\_cold\_start}
                 equ
   SYNC PHASE packet retry/delay values
                       5
DEF_retry_times
                 equ
DEF_sync_delay
                       10
                                   ; some delay between retries
                 equ
                       100
DEF_WATCH_DOG_LIMIT
                                   ; 10ms per count
                 equ
```

```
; Parameter Passing convention to runtime network msg routines.
; Standard Call Frame for IO_Xmit (This is reverse order of being pushed)
; Therefore these values can be used relative to the BP
xmit
             struc
             dw
                   2 dup(?); reserve space for near return and bp
DEF PID
             dw
                         ; destination processor ID
DEF_CMD
                        ; command for this packet
             dw
                   ?
             dw
                        ; Task for which the command operates
DEF_TID
                  ?
DEF_ENTRY
             dw ?
                        ; entry ID for the command (if applicable)
DEF_MY_TID
                   ?
             dw
                         ; originating Task ID
                        ; profile pointer (in CS) for entry parameters
DEF_PROFILE
             d₩
                   ?
DEF_MODE
             dw
                   ?
                        ; current calling mode (in or out)
DEF PARM LIST
             dd
                        ; pointer (seg/offset) for parameter list
xmit
             ends
DEF_xmit_frame equ
                 size xmit
                               ; size of parameter frame
; Parameter Constraint Layout
constraint
             struc
DEF_low_desc
             d₩
DEF_high_desc dw
                   ?
DEF size desc dw
constraint
             ends
DEF_in
             equ
                   1
DEF_out
                   2
             equ
DEF_in_out
                   3
                                ; bit-wise "or" of "in" & "out"
             equ
; Parameter Profile Layout
        *******
        * Number of Parameters *
        *******
           PARM1 : Mode
                              ; in, out, or in out
           ******
           PARM1 : Type/Length *
                                ; negative if unconstrained, otherwise
                                ; this is a WORD count
; Task Control Block Layout
; TASK_ID:
             each block is pointed to by an entry in the
             TASK_DIRECTORY which is indexed by the TID. The TID
```

```
is essentially the task's priority (with a provision
              for tasks of the same type to have sequentially lower
              priority as they are created.
  Sync_Semaphore: The sync semaphore is used to suspend (or resume)
              execution of the associated task for rendezvous.
  Reply Pointer: Contains the buffer descriptor of the reply msg.
  Number of Entries: provides the number of entries for this task.
  Entry_Table: The Entry table provides a record for each of the
              entries defined in the task. The record contains:
                     PROFILE_PTR: pointer to the parameter profile
                              described above.
                     WAITING: flag indicating that the accepting
                              task is waiting for an entry call
                              for this entry.
                     Queue : Head of buffer descriptor linked to
                              this entry.
.LIST
```

-203-

```
.XLIST
; FILE: DA_HW.ASM
   Distributed Ada - Hardware Definition Include File
   Copyright(C) 1989, LabTek Corporation, Woodbridge, CT
Ethernet Board Hardware Configuration
base
                                  310H
                                           ; base address of board
                         equ
vector number
                         equ
                                  5 H
                                           ; vector number for board
                                ODCOOH
                                           ; address of ethernet memory
net_memory_seg
                         equ
                                 2000н
net_memory_size
                         equ
                                           ; 8K bytes
   LAN Controller Page O registers
NIC cr
             equ base + 0;
                                    -- control register of NIC
NIC_pstart
             equ base + 1;
                                    -- page start register
NIC_pstop
             equ base + 2;
                                    -- page stop register
NIC_bndy
             equ base + 3;
                                    -- boundary register
NIC_tpsr
             egu base + 4;
                                    -- transmit page start register
             equ base + 5;
NIC_tbcr0
                                   -- transmit byte count rgtr hi
NIC tbcr1
             equ base + 6;
                                   -- transmit byte count rgtr lo
NIC isr
             equ base + 7;
                                   -- interrupt status register
NIC_rsar0
             equ base + 8;
                                    -- remote start address rgtr lo
             equ base + 9;
NIC_rsar1
                                   -- remote start address rgtr hi
NIC_rbcr0
             equ base + 10;
                                    -- remote byte count rgtr lo
NIC rbcr1
             equ base + 11;
                                    -- remote byte count rgtr hi
NIC rcr
             equ base + 12;
                                    -- receive configuration rgtr
NIC tcr
             equ base + 13;
                                    -- transmit configuration rgtr
             equ base + 14;
NIC dcr
                                    -- data configuration register
NIC imr
             equ base + 15;
                                    -- interrrupt mask register
; controller page 1 registers - NIC address setup registers
; These registers are written to establish what the actual
; physical address will be.
phys_address_0 equ base + 1;
                             ; physical address registers.
phys_address_1 equ base + 2;
                             ; These registers are accessed
phys_address_2 equ base + 3;
                             ; via NIC_cr bits 7,6 = 0,1.
phys_address_3 equ base + 4;
                             ; LAN registers are accessed
phys address 4 equ base + 5;
                              ; via cntrl bits 3.2 = 0.0.
phys_address_5 equ base + 6;
NIC_curr
             equ base + 7;
                             ; only written once during init
   Controller Page 2 - Ethernet PROM ADDRESS memory
  These locations contain the "preferred" address as contained
```

```
in PROM. These will typically be copied to the physical
   address registers above (page 1).
prom address 0 equ base + 0;
                                       -- station address 0
prom_address_1 equ base + 1;
                                       -- station address 1
prom_address_2 equ base + 2;
                                       -- station address 2
prom_address_3 equ base + 3;
                                       -- station address 3
prom_address_4 equ base + 4;
                                       -- station address 4
prom_address_5 equ base + 5;
                                       -- station address 5
   Gate Array registers (note: offset of 400H)
              equ base + 400H;
                                    -- page start register
pstr
               equ base + 401H;
pspr
                                     -- page stop register
              equ base + 402H;
                                    -- drq timer register
dqtr
              equ base + 403H;
bcfr
                                    -- base configuration register
              equ base + 404H;
                                    -- prom configuration register
pcfr
              equ base + 405H;
                                    -- ga configuration register
qacfr
              equ base + 406H;
                                    -- gate array (ga) control rgtr
cntrl
              equ base + 407H;
streq
                                    -- ga status register
              equ base + 408H;
                                    -- interrupt/DMA cnfgrtn rgtr
idcfr
              equ base + 409H;
                                    -- DMA address register hi
damsb
              equ base + 40AH;
                                    -- DMA address register to
dalsb
vptr2
              equ base + 40BH;
                                    -- vector pointer rate H2
vptr1
              equ base + 40CH;
                                    -- vector pointer rgtr H1
vptr0
              equ base + 40DH;
                                    -- vector pointer rgtr #0
              equ base + 40EH;
                                    -- register file access hi
rfmsb
rflsb
              equ base + 40FH;
                                    -- register file access lo
·****************************
;* Ethernet (3com) Initialization Values *
                         03h
eth_enable_reset
                   equ
                                        ; enable reset
eth_disable reset
                         00h
                   equ
                                         ; disable reset
eth_access_prom
                         04h
                    equ
                                         ; access prom bytes
                         00h
eth recv select
                   equ
                                         : select external Xceiver
                         49h
                                        ; 8k of mem-map I/O, w/interrupts
eth lan config
                   equ
                         08h
eth rem DMA burst
                   equ
                                         ; # of bytes to transfer on DMA burst
                         80h
eth_irq_line
                    equ
                                         ; interrupts occur on IRQ5
                         20h
                                         ; 8k configuration for remote DMA
eth_rem_DMA_config equ
                         20h
eth_xmit_buf_start equ
                                        ; begin of transmission buffer (OH)
eth recv buf start equ
                         26h
                                        ; receive queue
                                                                     (0600H)
eth_recv buf end
                         40h
                                         ; 20 pages, 256 bytes/page (2000H)
                         2000h
eth_offset
                   equ
                                         ; difference between page & address
eth_recv_begin
                   equ
                        600h
                                        ; actual offset in RAM seg for begin
eth_recv_end
                         2000h
                                         ; actual offset in RAM seg for end
                   equ
eth_start_nic
                         02h
                                         ; start NIC
                   equ
                         01h
                                         ; stop the NIC
eth_nic_stop
                   equ
```

```
eth_nic_DMA_config
                          48h
                                          ; local DMA operations, 8 byte bursts
                    equ
                          00h
eth_remote_DMA_lo
                    eau
                                          ; DMA remote unused (lo)
                          00h
eth_remote_DMA_hi
                                          ; DMA remote unused (hi)
eth_packet_types
                          00h
                                          ; receive only good packets
                    eau
                          02h
eth_nic_mode
                    equ
                                          ; internal loopback mode
                          00h
                                          ; FOR NOW, DO NOT USE BOUNDRY REG!
eth_bndy_start
                    equ
                    equ Offh
eth int status
                                          ; clear status of all ints at start
eth_ints_disabled
                          00h
                                          ; enable no interrupts
                    equ
eth_access_page_0
                          00h
                                          ; access page 0 again (for cmd reg)
                    equ
                                          ; access NIC page 1 registers
                          40H
eth_access_page_1
                    equ
                          00h
                                          ; exit internal loopback mode
eth_exit_mode
                    eau
                                          ; mask for packet receive interrupt
nic_prx
                    egu
                            1
                                          ; mask for packet transmit interrupt
nic_ptx
                    equ
send
                    equ
                            4
                                          ; command byte to start transmission
  Interrupt Controller Commands
NET_EOI
               equ 60H + vector_number ; -- End Of Interrupt (specific)
TIMER EOI
                                          ; timer is interrupt channel 0
; Ethernet controller routine specifications
; Ethnet_Init initializes a 3com Etherlink II board to transmit and receive
; packets via a memory mapped interface with the board located at DC00:0000.
: The base address from which the registers are located is 310h. The init
; routine intializes the memory to zeroes before it completes. Although no
; DMA is used to transfer the data from main memory to the board's memory
; (which is referred as remote DMA operations), there is no choice but to
; use the local DMA operations (transferring bytes or words from the board's
; memory to the board's output fifo's).
        LIST
```

```
55,132
      page
      TITLE RTE - Distribted Ada Runtime Module
; FILE: DA_RTE.ASM
; RTE - DISTRIBUTED Ada RUNTIME MODULE
; Copyright(C) 1989, LabTek Corporation, Woodbridge, CT
Runtime Code to implement prototype Distributed Ada Services
; This module implements the remote rendezvous operations to
 support distributed Ada.
  Currently provided are:
    Remote_Entry, Remote_Select, Remote_Accept, Remote_End_Accept,
    Remote_Elab_Start, Remote_Elab_Wait, Remote_Elab_Continue.
    Local_End_Accept
  Ver Date
             Description
; 0.1 Nov-88 : Initial prototype
  0.2 Dec-89: Version 2 - Flexible task distribution Added
.model large
      public Initialize
   These are entry points called by the vendor runtime interface to
   invoke the runtime by generated code
      public Request_Entry, Activate_Complete, Accept, Rendezvous_Complete
      public Select, Create_Task
   The IO module invokes the runtime services when messages are received
   via the NET_RECEIVE call
      public NET_Receive
                                  ; called by 10
; Vendor Runtime Services
      extrn VRTIF_Init:near
                                  ; Vendor Supplied P Semaphore operation
       extrn VRTIF_Wait:far
             VRTIF_Signal_I:far
                                  ; Vendor Supplied V operation/interrupt
       extrn
                                  ; Vendor Supplied V operation
       extrn
             VRTIF Signal:far
; After or instead of using the distributed runtime, control may be
  passed bact to the vendor runtime through this interface
```

```
;
             VRTIF_Create_Task:near
       extrn
             VRTIF_Activate_Complete:near
       extrn
       extrn VRTIF Entry:near
             VRTIF_Rendezvous_Complete:near
       extrn
       extrn VRTIF_Accept:near
       extrn VRTIF_Select:near
       extrn
             VRTIF_Lower_Priority:near
  Vendor task control block information and runtime data segment address
             VRTIF_tcbtid:abs
                                  ; offset to priority within vendor TCB
       extrn
             VRTIF_task_ptr:word
       extrn
                                   ; offset to current TCB with runtime DS
             VRTIF_DS:word
                                   ; offset within user DS to runtime DS
       extrn
             VRTIF_SELECT_REC:abs ; number of bytes per "select record"
       extrn
              Sync:near
                                   ; call synchronize
       extrn
                                   ; restart system on "COLD_START"
       extrn
              Shut_Down:near
       extrn
             TASK_DIRECTORY:word
; Network IO Services
       extrn TX_READY:near
                                   ; Transmit ready semaphore
       extrn IO_XMIT:near
                                   ; Start transmission routine
       extrn
             IO_Network_Init:near
       extrn
             10_ALLOCATE:near
                                   ; allocate a buffer
             IO DEALLOCATE:near
                                   ; deallocate a buffer
       extrn
             PID:word
                                   ; THIS processor ID
       extrn
              SYNCHRO_SEMAPHORE:word
       extrn
       extrn
              CONTINUE_SEMAPHORE:word
             Outchr:near
                                   ; for debugging only
       extrn
       include DA_DEF.ASM
                                   ; system definitions
cseg
       segment common
       assume cs:cseg,ds:cseg,es:cseg
       org
              1400H
Initialize
               -- no parameters
Initialize:
       call
              IO Network Init
              VRTIF_init
       call
       ret
; Prior to each Create Task, synchronize all CPU's to keep elaboration
; going sequencially
```

```
Create_Task:
       push
               ds
       push
               ax
                al,'c'
        mov
                Outchr
                              ; aa
        call
       mov
               ax,cs
               ds,ax
       mov
       call
               Sync
                              ; do synchronization
       pop
               ax
       pop
               VRTIF_Create_Task; return to vendor runtime
       jmp
; A task has completed activation and called "ACTIVATED". Since there
; are no parameters, simply nest the call to the vendor runtime so it
; will return here when done. First, provide a unique ID based on
  priority for each task.
                          Then we see if the task should remain
 alive. If not, suspend it on a dummy semaphore
Activate_Complete:
       push
               bp
       mov
               bp,sp
       push
               ax
       push
               bx
       push
               СХ
       push
               dx
       push
               si
               di
       push
       push
               ds
       push
               ds,[VRTIF_DS]
       mov
       mov
               si,[VRTIF_TASK_PTR]
               cx, [si+VRTIF_TCBTID]
                                      ; get priority (of this task type)
       mov
  During Activation all tasks have the priority of their task type, however
  since the priority is used to identify tasks, and possibly serveral tasks
  will be of the same task type, count the tasks of each task type and
  assign them a unique priority. (note the initial priorities must be
  assigned with sufficient space so that this has no effect on scheduling).
  Decreasing Ada priorities have increasing VRT priority (by two).
       mov
               di,cx
               di,di
       add
                                      ; mult by four to make index
       add
               di,di
   Modifying count for base task is atomic action
       pushf
       cli
```

```
ax,cs:TASK_DIRECTORY.DTCB_dir_Count[di]; get # of tasks for type
        mov
        add word ptr cs:TASK_DIRECTORY.DTCB_dir_Count[di],2; adjust it for next
        popf
                                         ; see if delta on this priority
        or
                ax,ax
                ACT_COMPLETE10
                                         ; if so skip changing of pririty
        jΖ
        add
                cx,ax
                                         ; compute new priority
                                         ; save priority
        push
        call
                VRTIF_Lower_Priority
                                         ; setpriority lower cx=priority si=V-TCB
                di
        pop
        add
                di,di
        add
                di,di
ACT_COMPLETE10:
                                         ; set DRT data segment
        mov
                ax,cs
        mov
                ds,ax
                bx, TASK_DIRECTORY.DTCB_dir_TCB[di]
        mov
                                                         ; get DRT TCB
                bx,DEF_return_addr(bx)
                                                         ; point to return addr
        lea
                di,[bp+2]
                                         ; get return address offset
        MOV
                [bx].di
        mov
                                         ; save in TCB
                di,[bp+4]
        mov
                                         ; get return address segment
                (bx+2),di
        mov
        pop
                es
                ds
        рор
                di
        pop
        pop
                si
                dx
        pop
        pop
                СХ
                bx
        pop
        pop
                ax
        pop
                bp
                sp,4
        add
                                         ; trash return address (saved in TCB)
                                         ; simulate a FAR call
        push
                cs
                VRTIF_Activate_complete
        call
                sp,4
                                         ; make room for return address
        push
                bp
        mov
                bp,sp
        push
                ax
        push
                bx
        push
                СХ
                dx
                                         ; "mul" affects this
        push
                si
        push
        push
        push
                ds
        push
                es
                ds,[VRTIF_DS]
                                     ; get Vendor runtime data segment
        mov
```

```
si,[VRTIF_TASK_PTR]
                                        ; fetch Current Task TCB
        mov
                bx,[si+VRTIF_TCBTID]
                                        ; get priority (our task type)
        mov
        mov
                ax,cs
                                         ; load DRT data segment
                ds,ax
       mov
                                        ; mult by four to make index
        add
                bx,bx
        add
                bx,bx
                ax, TASK DIRECTORY.DTCB_dir_pid[bx]; fetch PID for this task
        mov
        стр
                ax,[PID]
                                         ; see if this is the processor
        jΖ
                Keep_alive
  If here, this task should not continue to run... suspend it.
        pushf
                                         ; this must be atomic
        cli
        хог
                ax,ax
                                         ; init a dummy semaphore
                [DUMMY_SEM],ax
        mov
                [DUMMY_SEM+2],ax
        mov
                [DUMMY_SEM+4],ax
        mov
        push
                ax,DUMMY_SEM
        lea
        push
                VRTIF_Wait
                                        ; go to sleep forever
        call
        popf
        int
                                         ; if here... ERROR!
  This task should be allowed to live, let it continue
Keep_alive:
        mov
                si, TASK_DIRECTORY.DTCB_dir_TCB[bx]
                                                         ; fetch TCB
        lea
                si,DEF_return_addr(si)
                ax,[si]
                                        ; fetch offset
        mov
        mov
                [bp+2],ax
                                        ; put on stack
                ax,[si+2]
                                         ; fetch segment
        mov
        mov
                [bp+4],ax
                es
        pop
        pop
                ds
                di
        pop
        pop
                si
                dx
        pop
        pop
        pop
                bx
        рор
                аx
        pop
                bp
        retf
   Programs will come here when they want to do an entry call. If
    the call is to a task with remote callers, we must go through the
```

distributed runtime, otherwise go to local runtime.

```
Input parameters:
          ES:DX points to Parameter List
               task id to call
          CX
                entry id to call
Request Entry:
       push
                              ; save for vendor runtime
               ds,[VRTIF DS] ; fetch local runtime data segment
       mov
               si,[bx+VRTIF_tcbtid] ; get distributed task id (priority)
       mov
       MOV
                              ; save task id
               di,[VRTIF_TASK_PTR] ; get vendor TCB of current task
       mov
               di,[di+VRTIF_tcbtid]; fetch distrib. tid of current task
       mov
       рор
                              ; restore ds
       add
               si,si
                              ; mult by four to make index
       add
               si,si
               cs:TASK DIRECTORY[si], OFFFFH
                                             ; see if local or remote
       test
       jnz
                              ; if entry must be done by distributed runtime
  This entry is strickly local, let vendor runtime handle it
       imp
               VRTIF Entry
                              ; go to vendor runtime
 This entry must be handled by the distributed run- me, check to see if
  the called task is on this processor.
Dist_Entry:
        push
                al,'r'
        mov
        call
                Outchr
        pop
       TOV
               si,cs:TASK_DIRECTORY.DTCB_dir_pid[si] ; fetch PID of called task
       cmp
               si,cs:[PID]
                                               ; compare against my PID
               Remote_Entry
       jnz
               Local_Entry
                                             ; do local if the same
       jmp
Remote_Entry
   Send "Request Entry" message with copied parameters and profile
   Wait on Entry Wait_Semaphore
   Copy OUT parameters
   Release Buffer
   IN PARAMETERS:
                distributed task id to call
          AX
          CX
                entry id to call
          ΙD
                distributed task ID of this caller task
          ES:DX points to Parameter List
```

```
NOTE: Stack Parameters are removed by caller
RE Parm List
                equ
                        -4
                                ; dword
RE Profile
                                ; parameter profile ptr
                equ
                        -6
RE Count
                equ
                        -8
                                ; parameter count
RE_TCB
                        -10
                                ; dist. TCB of server
                equ
                        -12
RE_TID
                                ; Distrib. Task ID of caller
                equ
RE BUFF DESC
                equ
                        -14
                                : descriptor of reply buffer
RE_BUFF_PTR
                        -16
                                ; address to packet data
                equ
Remote_Entry:
         push
                 аx
                 al,'R'
         MOV
         call
                 Outchr
         pop
                 ax
        push
                bo
        mov
                bp,sp
        add
                sp, RE BUFF PTR
                                        ; local parameters to save
                                        ; save caller's DS
        push
        push
                cs
                                        ; load DRT data segment
        pop
                ds
                (bp+RE TID), di
                                        ; save task id of caller
        mov
                [bp+RE_Parm_list+2],es ; save segment of parameter list
        mov
                [bp+RE_Parm_ ist],dx
                                        ; save offset of parameter list
        mov
  Build call frame to transmit entry call to designated task
  PARM LIST
                                ; push segment of parameter list
        push
        push
                                ; push offset of parameter list
                dx
                dx,DEF in
                                ; calling Xmit for IN mode
        mov
; MODE
        push
                dx
                                ; get entry id
                bx,cx
        mov
                bx,bx
                                ; muit by two
        add
        add
                bx,bx
                                ; mult by four
                bx,bx
                                ; mult by eight (8 bytes per entry descriptor)
        add
                                ; get destination TID in SI
                si,ax
        mov
                si,si
        add
                                ; mult by four to make index
        add
                si,si
                si,Task_Directory.DTCB_dir_TCB[si]
        mov
                                                        ; fetch dist. TCB
                [bp+RE_TCB],si ; save for later
        mov
                dx,DEF_Entry_Table+DEF_entry_profile_ptr(si+bx) ;profile offset
        mov
                [bp+RE_Profile],dx ; save for later
        mov
; PROFILE
        push
                dx
                                ; push as parameter
  TID of Source
                di
        push
; ENTRY
                                ; push entry id
        push
                СХ
```

```
; TID of Destination
                               ; push (DA) task id
       push
  CMD for remote entry call
       mov
               di,DEF_request_entry
       push
; PID of Destination
                               ; get back TID of dest.
                di.ax
       mov
                di.di
                                : mult by four to make index
        add
        add
                di,di
                di.Task Directory.DTCB_dir_PID[di] ; fetch PID
        mov
                                ; push PID
        push
                                ; parameters are copied by xmit
        call
                IO_Xmit
    Now wait for rendezvous Complete to wake up
                si,[bp+RE TID]
                                       ; get my dist TID
        mov
                si,si
        add
        add
                si,si
                si, Task Directory.DTCB dir_TCB[si] ; fetch my dist. TCB
        mov
        push
                si
                                ; base of DA TCB is semaphore
        push
                VRTIF_Wait
                                ; go to sleep waiting for end rendezvous
        call
    Copy out parameters back. Use the TCB definitions
    to determine how many parameters, their size, and what type (ie. must
    allow for unconstrained arrays).
   First get address of buffer and stick it in local
                                        ; get my dist TID
                si,[bp+RE_TID]
        mov
        add
                si,si
                si,si
        add
                si,Task_Directory.DTCB_dir_TCB(si) ; fetch dist. TCB
        mov
                si,DEF_TCB_Reply[si] ; get reply buffer descriptor
        mov
                [bp+RE_BUFF_DESC],si ; save it for later deallocation
        mov
                                        ; get actual buffer address
                si,[si]
        mov
                si,DEF_pkt_data(si)
                                        ; point directly to data
        lea
                [bp+RE_BUFF_PTR],si
                                        ; save pointer
        mov
                si,[bp+RE_Profile]
                                        ; get parameter profile ptr
        mov
        cld
                                        ; make sure auto increment
                                        ; get number of parameters
        lodsw
R_Entry_10:
                                        ; see if done
                ax,ax
        or
                                        ; continue if not done
                R_Entry_12
        jnz
                                        ; if done
        jmp
                R_Entry_30
R Entry 12:
                [bp+RE_COUNT],ax
                                        ; update parameter count
        mov
```

```
lodsw
                                        ; get parameter Mode
       mov
               cx,[si]
                                        ; fetch parameter type/length
        add
                si,2
                                        ; skip over type/length
                [bp+RE_PROFILE],si
                                        ; update profile pointer for next
       mov
                si,[bp+RE parm_list]
                                        ; point to parameter list
       lds
; note: vendor puts segment/offset in reverse of normal order
                                        ; segment of data
       push
                data_seg[si]
        push
                data_off[si]
                                        ; offset of data
                                        ; see if unconstrained type
                cx,cx
        jge
                R_Entry_15
  process an unconstrained object as a parameter. Note, the
  descriptor is always copied, so we must skip 3 words in buffer
  and over two in the parameter list
                [si+4]
        push
                                        ; descriptor segment
       push
                [si+6]
                                        ; offset of descriptor
       add
                word ptr [bp+RE_parm_list],si; update parameter list index
       MOV
                si, [bp+RE_BUFF_PTR]
                                        ; adjust buffer pointer over constraint
       mov
        add
                                        : skip over constraint
        mov
                [bp+RE_BUFF_PTR],si
                                        ; update
                                        ; get offset of descriptor
       DOD
                ds
                                        ; get segment of descriptor
        pop
                                        ; save MODE of parameter
       push
                cx,[si+DEF_low_desc]
                                        ; get low bound of constraint
        mov
                                        ; get high bound of constraint
        mov
                ax,[si+DEF_high_desc]
        mov
                dx,[si+DEF_size_desc]
                                       ; get size of object
  Copy the parameter data iff MODE is correct and array is not null
        mov
                bx,cs
                                        ; reload DRT data segment
                ds,bx
        mov
        pop
                bx
                                        ; get mode of parameter
                di
                                        ; get offset of data
        pop
                                        ; get segment of data
        pop
        and
                bx,DEF_out
                                        ; see if we should copy data
        jΖ
                R_Entry 20
                                        ; if not, go on
                ax,cx
                                        ; compute difference in range
        sub
        inc
                ax
                                        ; adjust to include end points
        mul
                dx
                                        ; compute size in words
                R_Entry_20
                                        ; if array is empty go to next parm
        jle
        mov
                                        ; put in count register
                si,[bp+RE_BUFF_PTR]
        mov
                                        ; transfer from packet buffer
        rep
                [bp+RE_BUFF_PTR],si
                                        ; update pointer
        mov
        jmp
                R_Entry 20
                                        ; go on to next parameter
    Constrained parameter, CX is length in bytes, copy it into packet buffer
```

```
R_Entry_15:
       add
                                      ; move to next object address
               word ptr [bp+DEF_parm_list],si; update parameter list index
       mov
                                      ; get data offset
       рор
       pop
                                      ; get parameter data segment
       and
               ax,DEF out
                                      ; see if mode is right to copy out
                                      ; restore distrib. data segment
       mov
               ax,cs
       mov
               ds,ax
               R Entry 20
                                      ; skip copy of data if not out mode
       jz
               si,[bp+RE_BUFF_PTR]
                                      ; get buffer pointer in DS:SI
       mov
       inc
                                      ; round odd bytes up when convert
       shr
               cx,1
                                      ; to words
       rep
               MOVSW
               [bp+RE_BUFF_PTR],si
       mov
                                      ; update current packet buffer ptr
R_Entry_20:
               si,[bp+RE_Profile]
                                      ; get next parameter profile
       mov
       mov
               ax,[bp+RE_Count]
                                      ; get the counter back in ax
       dec
                                      ; count down
               R Entry 10
       jmp
  Free buffer, restore stack, and return to entry caller
  (it restores DS and any any stack frame it may have built)
R_Entry_30:
               bx,[bp+RE_BUFF_DESC]
       mcv
                                    ; get reply buffer descriptor back
       call
               IO Deallocate
                                      ; return used buffer
       pop
                                      ; restore caller's data segment
       mov
               sp,bp
                                      ; dallocate locals
               bp
       pop
       retf
Local Entry : This routine is called for an entry of a task
                       which is local (same processor) as the caller
     inputs:
       AX : TID of called task
       BX : Vendor TCB of called task
       CX : Entry ID
       DX : offset to parmeter list
       ES: segment of parameter list
       si : PID of called task
       di : MY_TID
   Although the task is local to the caller, an IO buffer is allocated
   to store the necessary pointers required by accepting tasks. This
   is later deallocated as part of the local_end_accept routine. The
   calling task is always suspended, and if the accepting task is "waiting"
   it is signaled to wake up.
```

```
Only the TID EID and MY_TID fields within the buffer are valid during
   local rendezvous. Also, the data fields have the address of the
   various objects/desciptors rather than the data itself.
   NOTE: There is no need to deallocate the buffer allocated here because
          it is deallocated by the server task. (There is only one
          buffer used by local tasks, rather than two as for remote tasks.)
LE_ENTRY PTR
                        -2
                                        ; word: bp offset to current entry table
                equ
LE_TCB_PTR
                equ
                        -4
                                        ; word: bp offset to target TCB base
LE_MY_TCB
                        -6
                                        ; word: bp offset to my TCB base
                equ
Local_Entry:
         push
                 ax
;
         mov
                 al,'L'
                 Outchr
         call
        рор
                 aх
        push
                bp
        mov
                bp,sp
        add
                sp,LE_MY_TCB
                                        ; allocate space for locals
        push
                                        ; save caller's data segment
                                        ; load DRT data segment
        push
                cs
        pop
                ds
        push
                                        ; save TID
        call
                IO Allocate
                                        ; get a buffer descriptor ptr in BX
        pop
                si,[BX]
                                        ; fetch buffer address
        mov
   currently only one parameter is used (either in or out). Take advantage
    of this to simplify interface to accepting task. The address of the
    data area is provided in the first part of the buffer. NOTE: this address
    is backwards (segment=low address, offset=high address).
        push
                ах
                di
        push
                di.dx
        mov
                ax,es:[di]
                                        ; transfer parm list to buffer
        mov
                [si],ax
                                        ; buffer so as to point to the
        mov
                ax,es:[di+2]
                                          ; data and descriptors actually
                [si+2],ax
                                        ; processor
        mov
                ax, es: [di+4]
                [si+4].ax
        mov
        mov
                ax, es: [di+6]
                [si+6],ax
        mov
        рор
        pop
        mov
                DEF_pkt_tid[si],ax
                                        ; put in called task TID
                DEF_pkt_my_tid[si],di
        mov
                                         ; and put in calling task id there
```

```
DEF_pkt_cmd(si),DEF_local_call; indicate this is a local call
        mov
                di,di
        add
                                        ; mult by four
                di,di
        add
                di,TASK_DIRECTORY.DTCB dir TCB[di] ; get my TCB addr
        mov
                [bp+LE_MY_TCB],di
                                          ; save it
        mov
                [si+DEF_pkt_Entry_ID],CX ; save entry id
        mov
                                          ; get TID of called task into si
        mov
                si,ax
                si,si
                                          ; mult by four
        add
        add
                si,si
                ax,TASK_DIRECTORY.DTCd_dir_TCB[si] ; fetch dist. TCB addr
        mov
        mov
                [bp+LE_TCB_PTR],ax
                                          ; save base of TCB
                si,cx
                                          ; compute entry table address
        mov
                si,si
        add
                                          ; * 4
                si,si
        add
        add
                si,si
                si,ax
                                          ; add base of (DA) TCB
        add
                si,Def_Entry_table
        add
        mov
                [bp+LE_Entry_PTR],si
                                          ; save
        lea
                si,DEF_Entry_Queue(si)
                                          ; fetch entry queue head
  ATOMIC action follows... Queue entry, if waiting signal acceptor
        pushf
        cli
        call
                INSERT
                                        ; place buffer descriptor on entry Q
                si,[bp+LE_Entry PIR]
                                        ; fetch entry table address again
                DEF_Entry_Wait[si],OFFFFH ; see if WAITING
        test
                                        ; go on if not
        jΖ
  server is waiting on accept, signal it
        mov
                si,[bp+LE_TCB_PTR]
                                        ; get task Control Block
                cx,DEF_num_entries[si] ; get number of entries
        mov
        lea
                si,DEF_entry_table[si] ; point to base of table
le010:
                DEF_Entry_Wait[si],0
                                        ; clear (all) waiting flags
        mov
                si, size DEF_Entry_Rec ; go to next entry record
        add
                le010
        loop
                                        ; segment of semaphore
        push
                cs
                ax,[bp+LE_TCB_PTR]
                                        ; offset of semaphore
        mov
        push
        call
                VRTIF_Signal
                                        ; wake up server (may preempt ourselves)
  NOTE: This is the end of the atomic region (above Vendor runtime call
         reenables interrupts!
;
le020:
        popf
                                        ; restore interrupt level
        push
                                        ; now try to suspend ourselves
```

; semaphore is first thing in TCB

```
VRTIF_Wait
                                   ; may not suspend if server is higher
      call
                                   ; priority and has already signaled us!
      pop
                                   ; restore DS
                                   ; remove locals
              sp,bp
      mov
      pop
              bp
      retf
; Accept - is invoked by the generated code to wait for arrival of
          a caller.
              INPUTS:
                     AX is entry to accept
              OUTPUTS:
                     ES:BX is parameter list pointer
Accept:
      push
              ds
      mov
              ds, [VRTIF DS]
                                   ; get runtime data segment
              si,[VRTIF_TASK_PTR]
      mov
              si,VRTIF_tcbtid[si]
                                   ; fetch my TID
      mov
              ds
      pop
              si,si
                                   ; mult by four to make index
      add
              si,si
       add
       test
              cs:TASK_DIRECTORY.DTCB_dir_LOCAL[si],OFFFFH; distributed?
              Dist_Accept
                                  ; must to a distributed accept
       jnz
              VRTIF_Accept
                                   ; otherwise, return to vendor runtime
       jmp
Distributed Accept (TASK_ID, ENTRY_ID) return ES:BX_Param_Pointer
    NOTE: THIS HANDLES BOTH ACCEPTANCE FOR LOCAL AND REMOTE CALLS
         THROUGH THE DISTRIBUTED RUNTIME.
    Simple Accept, see if someone on entry queue, if so
    return with pointer to buffer in ES:BX, otherwise set
    "Waiting" flag and go to sleep on semaphore.
    Inputs: TASK_ID, ENTRY_ID
    Outputs: Returns ES:BX pointing to Parameter Data List
            Also, Buffer descriptor is placed in "Reply" pointer.
RA_TCB
                     - 2
                            ; word: my TCB
              equ
RA ENTRY
                     -4
                            ; word: this entry
              equ
```

ax, [bp+LE_MY_TCB]

mov push

```
Dist_Accept:
         push
                 ax
                 al,'a'
        mov
         call
                 Outchr
                                ; aa
         pop
                 аx
        push
        mov
                bp,sp
        sub
                sp,4
                                ; save old data segment
                ds
        push
        push
                                ; load data segment
        pop
                ds
                si, TASK_DIRECTORY.DTCB_dir_TCB[si]
        mov
                                                         ; fetch TCB ptr
                                                         ; save it
                [bp+RA_TCB],si
        mov
                bx,ax
                                                ; compute entry index
        MOV
                                ; * 2
                bx,bx
        add
                                ; * 4
        add
                bx,bx
                bx,bx
                                ; * 8 (eight bytes per entry)
        add
                bx,DEF entry table(si+bx)
        lea
                                                ; point to my entry of interest
                [bp+RA_ENTRY],bx
        mov
                                                 ; save it too
        pushf
                                ; save interrupt status
        cli
                                ; go atomic
                DEF_entry_Queue+2[bx],OFFFFH; if Zero, then queue is empty
        test
                                ; if caller is there, take it!
        jnz
                RA010
    No caller on entry queue. Set waiting flag and go to sleep
        mov
                [bx+DEF_entry_wait],1; set flag
        push
                                ; push segment of my task semaphore
        push
                                ; address of my tcb
        call
                VRTIF_WAIT
                                ; go to sleep waiting for caller
  NOTE after vendor runtime call - interrupts are enabled!
   Now Something is on the queue, provide address of parameter list in
  ES:BX and return to caller.
RA010:
                                      ; restore interrupt status
        popf
                si,[bp+RA TCB]
                                     ; get ICB pointer back
        mov
        mov
                bx, [bp+RA ENTRY]
                                     ; get the entry address back
; note: the wait flag is cleared by the caller
        mov
                bx,DEF_entry_queue+2[bx]; get buffer descriptor from queue
                DEF_tcb_reply[si],bx ; save descriptor for end rendezvous
        mov
        mov
                bx, [bx]
                                     ; fetch buffer address into BX (return)
                                     ; get segment into ES, making ES:BX pair
                ax,cs
        mov
        mov
                es,ax
                                     ; parameter list is in buffer
                                      ; It has been put there by either the
                                      ; local or remote entry call mechanisms
                                     ; restore data segment
                ds
        pop
                                      ; remove locals
        mov
                sp,bp
        pop
                bp
```

retf

```
Select -
       INPUTS:
              STACK frame has open alternatives. As best as we can
              tell, it looks like this:
                      [flags]
                      [ entry # ]
                      [ unknown ]
              Each alternative appears to have three words with the
              "flags" word being not-zero. If it is zero, this indicates
              the end of the list.
       OUTPUTS:
              All input parameters are removed from the stack and replaced
              the parameter list pointer and a selector which indicates
              which alternative was selected.
SELECT_LIST
              equ
                                   ; offset from bp to open alternatives
                                   ; offset to flags withing list record
FLAGS
                     0
              equ
ENTRY_ID
              equ
                     2
                                    ; offset to ID# within list record
Select:
              ds
       push
              ds, [VRTIF DS]
                                    ; get runtime data segment
       MOV
              si,[VRTIF_TASK_PTR]
       mov
       mov
              si,VRTIF tcbtid(si)
                                    ; fetch my TID
              de
       pop
              si,si
                                    ; mult by four to make index
       add
              si,si
       add
              cs:TASK_DIRECTORY.DTCB_dir_local[si],OFFFFH ; distributed?
       test
       jnz
              Dist Select
                                   ; must to a distributed select
       jmp
              VRTIF Select
                                   ; otherwise, return to vendor runtime
Dist_Select
   Check to see if any of the entries have callers. If not,
   set the "Waiting" Flag in each of them, and go to sleep.
  If one entry has a queued request, accept it and return
  offset for "Case" table and parameter list pointer on the stack
   The offset for the case table is the entry id + 1.
   INPUTS: Index into TASK_DIRECTORY is in SI
DS TCB
                     - 2
              egu
                                     ; word: my TCB
                     -4
DS ENTRY
              equ
                                     ; word: this entry
DS_ALTER
              equ
                     -6
Dist_Select:
```

```
al,'s'
;
         mov
                                ; aa
         call
                 Outchr
        push
                bp
        mov
                bp,sp
                                        ; allocate local storage
        add
                sp,DS_ALTER
                                        ; save DS
        push
        MOV
                ax,cs
                                          ; set to Distr. runtime data segment
                ds,ax
        mov
                si, TASK DIRECTORY.DTCB_dir TCB[si]
                                                        ; fetch TCB ptr
        mov
                [bp+DS TCB],si
                                                         ; save it
        mov
    ENTER CRITICAL REGION (cannot allow task to go on an entry queue
    after we have checked it, but before setting waiting flag.
        pushf
        cli
; First check each entry to see if any has a caller...
; Go through all open alternatives
                                          ; will come back here after resume
Rem Sel00:
                ax,[bp+fi . __LIST]
        lea
                                           ; get address of entry list
                [bp+PS A LR],ax
                                           ; save in local variable
        mov
Rem_Sel10:
                bx, [bp+DS_ALTER]
                                          ; get pointer
        mov
                SS:FLAGS[bx],OffffH
                                           ; test if end of the list
        test
                Rem_Sel15
                                           ; did not find it
        jΖ
                ax, SS:ENTRY_ID[bx]
                                           ; get entry ID
        illov
                                           ; compute entry index
        mov
                bx,ax
        add
                bx,bx
                                           ; * 2
                                           ; * 4
        add
                bx,bx
        add
                                           ; * 8 (eight bytes per entry)
                bx,DEF_entry_table[si+bx] ; point to entry of interest
        lea
                DEF_entry_queue+2(bx),OFFFFH; if Zero, then queue is empty
        test
                                           ; if caller is there, take it!
        jnz
                Rem_Sel50
                word ptr [bp+DS_ALTER], VRTIF_SELECT_REC ; bytes per record
        add
                                          ; loop till end of list
        jmp
                Rem_Sel10
    all of the Entry Queues are Empty, mark each Waiting flag
    and go to sleep.
Rem_Sel15:
                ax, [bp+SELECT LIST]
                                           ; get address of entry list
        lea
        mov
                [bp+DS_ALTER],ax
                                           ; save in local variable
Rem Sel20:
                bx, [bp+DS_ALTER]
                                           ; get pointer
        mov
                SS:FLAGS[bx],OffffH
                                           ; test if end of the list
        test
                Rem Sel30
        jΖ
                                           ; done
                ax,SS:ENTRY_ID[bx]
                                          ; get entry ID
        mov
        mov
                bx,ax
                                           ; compute entry index
                bx,bx
                                           ; * 2
        add
```

```
add
                bx.bx
                                          ; * 8 (eight bytes per entry)
        add
                bx,bx
        lea
                bx,DEF_entry_table(si+bx) ; point to entry of interest
                DEF_entry_wait(bx),1
        mov
                                          ; set waiting
                word ptr [bp+DS_ALTER], VRTIF_SELECT_REC
        add
                Rem_Sel20
                                          ; loop till end of list
        jmp
   The following runtime call will suspend this task, when it
    resumes, the interrupt flag will be set again, and presumably,
   one of the entries will have a caller queued.
Rem_Sel30:
                                        ; push segment of wait semaphore
        push
                [bp+DS TCB]
                                        ; push offset of wait_semaphore taskid
        push
                VRTIF Wait
                                        ; do wait on semaphore
    Now clear all the waiting flags
        cli
        Lea
                ax, [bp+SELECT_LIST]
                                          ; get address of entry list
                [bp+DS ALTER],ax
                                          : save in local variable
        mov
Rem_Sel40:
        mov
                bx, [bp+DS ALTER]
                                          ; get pointer
                SS:FLAGS[bx],OffffH
                                          ; test if end of the list
        test
                Rem Sel45
                                           ; done
        įΖ
                ax, SS: ENTRY_ID [bx]
                                                 ; get entry ID
        mov
        mov
                bx,ax
                                          ; compute entry index
                                          ; * 2
        add
                bx,bx
                                          ; * 4
                bx.bx
        add
                                          ; * 8 (eight bytes per entry)
        add
                bx,bx
        lea
                bx,DEF_entry_table[si+bx] ; point to entry of interest
        mov
                DEF_entry_wait[bx],0
                                          ; clear waiting
                word ptr [bp+DS_ALTER], VRTIF_SELECT_REC
        add
        jmp
                Rem Sel40
                                          ; loop till end of list
Rem Sel45:
                Rem_Sel00
                                        ; go back and find caller
        imp
    There is a caller on this entry queue, do start accept
    Fetch the Caller's buffer, which has a (backward) pointer to
   the parameter data
Rem_Sel50:
                                        ; no longer critical
        mov
                si,DEF_entry_queue+2(bx) ; fetch buffer descriptor
        mov
                di,[bp+DS_TCB]
                                        ; get base of my TCB back
                DEF_tcb_reply[di],si
                                        ; put buff descriptor into reply ptr
        mov
        mov
                si,[si]
                                        ; get actual buffer (which is parm list)
                                        ; make entry id # compatible with VRTIF
        inc
                аx
```

```
Now pull parameters off of stack, and replace with parm_list ptr and
   case selector
       рор
               ds
                                      ; get DS back
                                      ; start with all locals
       mov
               sp,bp
       pop
               bp
                                      ; get back saved bp
               bx
                                      ; get return offset
       DOD
       pop
                                      ; get return segment
   Go thru open alternative list, removing three words per entry
Rem_Sel60:
               dx
                                      ; get ENTRY flag ??
       pop
               dx,dx
       or
                                      ; zero?
               Rem_Sel70
                                      ; if zero, this is end of list
       jΖ
                                      ; remove this alternative
       pop
               dχ
               dx
       pop
               Rem Sel60
       jmp
Rem_Sel70:
       push
               CS
                                      ; segment of parm list ptr
                                      ; offset of parm list (buffer)
       push
       push
               ax
                                      ; selector for case
       push
               СХ
                                      ; put return segment back on
       push
               bx
                                      ; and return offset
       retf
                                      ; and leave
; This is called by the generated code to indicate end of an accept body.
  When the rendezvous complete call is made, determine if the caller was
  on my processor. If not, use the Remote end accept, otherwise use the
  local end accept
               Inputs:
                       No user inputs, only the REPLY pointer
                       provides information regarding the responding task.
Rendezvous_Complete:
       push
               ds, [VRTIF DS]
                                      ; get runtime data segment
               si,[VRTIF_TASK_PTR]
       mov
       mov
               si,VRTIF tcbtid(si)
                                      ; fetch my TID
               ds
       pop
        add
               si,si
                                      ; mult by four to make index
       add
               si,si
               cs:TASK DIRECTORY.DTCB dir LOCAL(si),OFFFFH; distributed?
       test
               Dist End Accept
                                      ; must to a distributed accept
       jnz
               VRTIF_Rendezvous Complete; otherwise, return to vendor runtime
        imp
```

```
Distributed_End_Accept -
         Reply ptr has got the buffer descriptor, use it to determine
         if call was local or remote
DEA_ENTRY
               equ
                      - 2
                              ; local word for entry pointer
Dist_End_Accept:
        mov
                al,'e'
                              ; aa
        call
                Outchr
       push
               bo
       mov
               bp,sp
                                     ; local data
       sub
               sp,2
       push
               ds
                                     ; save previous DS
                                     ; load data segment
               cs
       push
       pop
               si,TASK_DIRECTORY.DTCB_dir_TCB[si]; fetch TCB of my task
       mov
               di,DEF_tcb_reply[si]
                                           ; fetch buffer descriptor
               di,[di]
       mov
               ax,DEF_pkt_entry_id[di]
                                             ; fetch Entry id
       mov
               bx,ax
       mov
               bx,bx
       add
                                             ; mult by 2
       add
               bx,bx
                                             ; * 4
               bx,bx
                                             ; * 8
       add
               bx,DEF_entry_table(si+bx)
       lea
                                             ; point to entry
               [bp+DEA ENTRY],bx
       mov
                                             ; save entry record ptr
       lea
               bx,DEF_entry_queue(bx)
                                             ; point to entry queue
       call
               REMOVE
                                     ; pull entry off queue BX now a buffer
               DEF_pkt_cmd(di),DEF_local_call ; see if this is local
       cmp
       įΖ
               Local_End_Accept
Send output parameters to caller.
    Release buffer used to hold input (and output for now) parameters.
       INPUT: SI is my TCB address
               DI points to buffer used for this rendezvous
               BX points to buffer descriptor
       NOTE: Stack frame is already build for local parameters
Remote_End_Accept:
                al,'R'
        mov
        call
                Outchr
       push
                                     ; save buffer descriptor
; Build stack for XMIT
; PARM LIST PTR
```

```
; segment of buffer
       push
              cs
                                     ; first part of buffer is parm list
       push
; MODE
       mov
               ax,DEF_out
                                     ; out mode
       push
; PROFILE
                                     ; get base of this entry
               bx, [bp+DEA_ENTRY]
               bx,DEF entry_profile_ptr[bx] ; fetch profile
       mov
       push
; MY_TID
               ax,[di+DEf_pkt_TID] ; This task was the original TID
       push
               ax
; ENTRY
               ax,[di+DEF_pkt_entry_ID]
       mov
       push
; TID
               di,[di+DEF_pkt_MY_TID] ; get caller's task ID
       push
; CMD
               ax,DEF_rendezvous_end
       mov
       push
; PID
               di,di
                                      ; mult TID by four to make index
       add
       add
               di,di
               ax,TASK_DIRECTORY.DTCB_dir_pid[di]
                                                     ; fetch PID
               aх
       push
                                      ; transmit reply
        call
               IO_Xmit
   Now we are done with the received buffer, release it
                                      ; get descriptor ptr back
       pop
                                      ; release buffer, descriptor in BX
               IO_Deallocate
       call
               ds
                                      ; restore DS
       pop
        mov
               sp,bp
        pop
               bр
        retf
;
     Local End Accept
     Allow caller to continue (Note: this is for entry calls with parameters
     that are all passed by reference. No copy-back is required).
     All entries whether remote or local use a buffer, therefore deallocate
     it when complete.
        INPUT: SI is this task's TCB address
               DI points to buffer used for this rendezvous
               BX points to buffer descriptor
        NOTE: Stack frame is already build for local parameters
```

```
Local_End_Accept:
  Now wake up caller
               al,'L'
        mov
        call
               Outch:
       mov
              si,DEF_pkt_my_tid[di]
                                   ; get TID of caller
       call
              IO_Deallocate
                                    ; done with buffer deallocate @ BX
       add
              si,si
                                    ; mult by four to make index
       add
              si,si
              si, TASK DIRECTORY.DTCB dir TCB[si]
       mov
                                                  ; get TCB of caller
       push
                                    ; push segment of semaphore
       push
                                    ; push calling Task's TCB (SEMAPHORE)
              VRTIF_Signal
       call
                                    ; signal task to continue
       рор
                                    ; restore DS
       mov
              sp,bp
       pop
              bp
       retf
; Net_Receive - processes an incoming message
 This routine is called by the interrupt handler (in ;
; the IO Module) to initiate action based on the
 receipt of a packet. When the service handler is
; called, BX contains the address of the buffer
; descriptor.
Net_Receive:
                                    ; get address of actual buffer
       mov
              si,[bx]
              di,[si+DEF_pkt_cmd]
       mov
                                    ; fetch command
       or
              di,di
                                    ; do range check
              Net_Receive_Error
       js
              di,command limit
       cmp
              Net_Receive_Error
       jg
       shl
              di,1
                                    ; make command into word index
              vector[di]
       jmp
Net_Receive_Error:
              al,'$'
       mov
       call
              Outchr
       call
              IO_deallocate
                                    ; trash message
       ret
   The following vector table implements the 'case' statement
   on the message ACTION Field
vector label
              word
       d₩
              offset Sync_Start
              offset Sync_Ready
```

```
offset Sync_Continue
      d₩
      d₩
            offset Entry_Call
      dw
            offset Rendezvous_End
      dw
            offset Shut_Down
                                      ; COLD START
            label word
vector_end
command limit
                  (vector_end - vector) / 2 - 1
            equ
  Future versions of the vector table will include
       Begin_Remove_Entry
       End_Remove_Entry
       Begin_Abort
       End Abort
       Begin_Terminate
       End_Terminate
       Shared_Variable_Request
      etc.
; This code section is executed upon receipt of a message initiating
; a Begin Elaborate request. 8X points to buffer descriptor.
  NOTE: THIS IS ONLY RECIEVED BY SLAVES!
Sync_Start:
            IO_Deal(ocate
                               ; no need for buffer
      call
      push
                               ; wait up slave
            ax, SYNCHRO_SEMAPHORE
      lea
      push
      call
            VRTIF_Signal_I
                               ; signal task to continue
      ret
; This code section is executed upon receipt of a message initiating
; an End Elaborate. This message implies that the specified elaboration has
; been completed on the remote processor and elaboration can continue
; on the primary processor.
: INPUTS: BX points to buffer descriptor.
; NOTE: THIS IS ONLY RECEIVED BY THE MASTER
Sync_Ready:
            IO Deallocate
                               ; no need for buffer
      call
                                ; wait up slave
      push
            ax, SYNCHRO_SEMAPHORE
      lea
      push
            VRTIF_Signal_I
                               ; signal task to continue
      call
      ret
```

```
Sync_Continue: Executed when a "sync_continue" message arrives..
       NOTE: ONLY RECEIVED BY SLAVES, half way through syncrhonization
Sync_Continue:
                                       ; no need for buffer
       call
               10 Deallocate
                                       ; wait up slave
       push
       lea
               ax, CONTINUE_SEMAPHORE
       push
       call
               VRTIF_Signal_I
                                      ; signal task to continue
       ret
This code section is executed upon receipt of a message initiating
  an entry call
  Place buffer on Entry queue, If "Waiting" for that entry is TRUE,
  then clear all Waiting Flags and signal Wait Semaphore.
      BX = Buffer descriptor pointer
      SI = Buffer pointer
  This code assumes only a single parameter (simplification for prototype)
               NOTE: pointers to data and descriptors are stored backward
                     from normal Intel OFFSET, SEGMENT format
                                       ; offset to type/len field in profile
type len
                       4
                       0
                                       ; position within buffer for ptr to data
data_seg
               equ
data_off
               equ
                       2
desc_seg
                                       ; position within buffer for ptr to desc
               equ
desc_off
               equ
true_data
                                       ; offset for data (after descriptor)
               equ
Entry_Call:
               dx,bx
                                       ; save buffer descriptor
       mov
               bx,[si+DEF_pkt_tid]
                                       ; get task id
       mov
       add
               bx,bx
                                       ; mult by four to make index
       add
               bx,bx
               bx,TASK DIRECTORY.DTCB_dir_TCB[bx] ; get task control block
       mov
               ax, [si+DEF_pkt_Entry_ID]; fetch entry id
       mov
                                      ; compute entry offset
       mov
               di,ax
               di,di
                                      ; mult times 2
        add
        add
               di,di
                                       ; times 4
                                       ; times 8
        add
               di,di
               di,DEF_entry_table(di+bx) ; point to current entry
       push
    currently only one parameter is used (either in or out). Take advantage
```

```
of this to simplify interface to accepting task. The address of the
   data area is provided in the first part of the buffer. NOTE: this address
   is backwards (segment=low address, offset=high address).
               di_DEF entry_Profile_Ptr[di] ; point to parameter profile
       mov
               [di+type_len],OFFFFH
                                       ; see if constrained
       test
                                       ; restore entry pointer
               di
       DOD
                                       ; go on if constrained
               Entry 010
       ins
  Parameter is unconstrained, first pointer is to data, second to descriptor
  The data will actually be offset by six (6) bytes to leave room for a
  descriptor in front of the packet data.
                                       : stuff cs of buffer
               data_seg[si],cs
       mov
               ax,DEF_pkt_data+true_data(si) ; address of true data
       lea
                                       ; put in packet
               data_off(si],ax
       mov
                                              ; segment of descriptor
               desc_seg[si],cs
       mov
               ax,DEF_pkt_data[si]
                                       ; offset of descriptor
       lea
               desc_off[si],ax
       mov
               Entry_020
        jmp
  Handle simple case of constrained array
Entry_010:
                                        ; stuff cs of buffer
                data_seg[si],cs
                                        ; address of data
                ax.DEF_pkt_data(si]
        Lea
        mov
                data_off[si],ax
  ATOMIC action follows... Queue entry, if waiting signal acceptor
Entry_020:
                                        ; bx := buffer; dx := TCB_base
        xchg
                bx,dx
                si,DEF_entry_queue(di) ; si points to entry queue
        lea
        pushf
        cli
                                        ; place buffer descriptor on entry Q
        call
                INSERT
                                       ; get entry WAITING flag
                cx,DEF_entry_wait(di)
        mov
                cx,cx
                                        ; test waiting flag
        ٥r
                                        ; go on if not
                Entry_040
        jΖ
   server is waiting on accept, clear all waiting flags and signal it
                                        ; get TCB
        mov
                si,dx
                cx,DEF_num_entries[si]
        mov
Entry 030:
                DEF entry_table+DEF_entry_wait[si],0; clear wait flag
        mov
                si,size DEF_entry_rec ; go to next entry
        add
                Entry_030
        Loop
                                        ; segment of semaphore
        push
                cs
                                        ; offset of semaphore (first in TCB)
        push
                dх
```

```
call
             VRTIF Signal I
                                 ; wake up server
Entry_040:
                                 ; restore interrupt level
      popf
                                 ; return to interrupt handler
      ret
; Rendezvous_End -
; This code section is executed upon receipt of a message completing
  an accept body (end rendezvous)
  Post buffer containing Out Parameters and signal task to wake up
  INPUTS:
;
     BX = Buffer descriptor pointer
     SI = Buffer Pointer
Rendezvous_End:
             si,[si+DEF_pkt_tid]
                                 ; fetch task id of caller
      mov
      add
             si,si
                                 ; mult by four to make index
      add
             si,si
             si,TASK DIRECTORY.DICB dir TCB[si]; fetch task control block
      mov
             [si+DEF_TCB_REPLY],bx ; provide caller with reply buffer
      mov
                                 ; push segment of caller semaphore
      push
             cs
                                 ; push offset of same (TCB)
      push
             si
      call
             VRTIF Signal I
                                 ; wake up caller
                                 ; to finish interrupt
      ret
REMOVE - Remove Entry that is on entry queue
  Inputs: BX points to entry Q
  Output: BX points to buffer descriptor that was dequeued
 All other registers are preserved
REMOVE:
      push
             ax
      push
             si
  do list operation as atomic action
      pushf
      cli
             si,[BX+DEF_NEXT_PTR]
                                 ; fetch buffer descriptor
      MOV
                                 ; get next buffer
             ax,[si+DEF_NEXT_PTR]
      mov
```

```
[BX+DEF_NEXT_PTR],ax
      mov
                                ; update queue head
      popf
            bx,si
                               ; return pointer in BX
      mov
            si
      pop
      pop
            ax
      ret
INSERT - INSERT Entry onto the end of an entry queue
  Inputs: SI points to entry Q
         BX points to buffer descriptor
 Outputs: SI points to last entry on Q
; All other registers are preserved
INSERT:
      push
            aх
 do list operation as atomic action
      pushf
      cli
INSERT10:
      mov
             ax,[si+DEF_next_ptr] ; get next buffer on entry queue
             ax,ax
                              ; see if end of list
      or
      jΖ
            INSERT20
                         ; end of list, go insert it
 this is not end of list, keep searching
             si,ax
      mov
             INSERT10
      jmp
 found spot on list, insert it
INSERT20:
             [si+DEF_next_ptr],bx ; put on end of list
      mov
      popf
                                ; restore interrupt flag
      pop
             ax
      ret
      align
DUMMY_SEM dw
                3 dup (?); dummy semaphore for making zombie tasks
cseg
      ends
      end
```

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page 55,132 TITLE IO - Distributed Ada Network IO

; FILE: DA_IO.ASM

10 MODULE - Low Level Network Functions

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The IO module provides the low_level interface to the network hardware and receive message buffering.

This code is loaded into all processors, and adapts to the the network hardware in its host. Which routines are used is determined solely by the calls made from the application code and the messages received.

The 10 interface is implemented as four separate functions: Initialize

Transmit

11 01131111

Receive

Interrupt Procesing

The initialize function obviously must be called prior to any other, and establishes the interrupt vector and enables, as well as prepares the hardware for use. It is also responsible for initilizing data structures used to buffer incoming packets.

The Transmit function is used by one task at a time, and is ; guarded by a semaphore to provide mutual exclusion. Once the ; transmit resource is granted, the data is copied into the on-card; buffer and sent out via hardware commands. (Normally, hardware ; packet acknowledge should be provided, however Ethernet does not ; support this, so we have implemented an acknowledge with time-out; protocol that provides network error detection. Note that ; acknowledgement packets take priority over regular traffic. ; Currently, no re-try is supported, however it would be a rather ; simple matter of keeping a transmit buffer queue and retransmit ; on errors. The more serious problem is how to insure real-time ; performance in the presence of multiple retries. Obviously the ; retry count would have to be programmable (and possibly time ; sensitive. If an acknowledgment times-out, a reconfiguration ; operation is executed to recover the system in a reduced state.

The Receive function is provided to assist in transferring the data to the requested destination. It clears the outstanding acknowledgement request.

The Interrupt Processing handles both transmit complete and reception interrupts. For transmit complete, the resource is simply made available again by performing a V operation on the

```
; trasmit semaphore. For Receive interrupts, a buffer is allocated;
 from a linked list of fixed sized buffers. Then the incoming
; data is copied to the buffer and the distributed runtime is
 invoked to process the request. It may simply post the fact the;
  message has arrived (and queue to an entry), or it may cause a
 task to resume which involves signalling (V - operation) the
 suspended task.
 Refer to individual procedure headers for parameter information ;
 and calling requirements.
              Description
 Ver Date
; 0.1 Nov-88 : Initial prototype
 0.2 Dec-89: Added Packet Acknowledge/Error Detection, and
              allowed for system restart (compiler initialized
              data is restricted to a WARM START flag.)
; 0.3 Feb-90 : Greatly improved multi-packet processing and
              interrupt handling.
.model large
       include DA_DEF.ASM
                                     ; contains software definitions
       include DA_HW.ASM
                                     ; contains hardware specifics
       public IO_Network_Init, IO_Xmit
       public TX READY
                                    ; semaphore
       public IO ALLOCATE, IO DEALLOCATE
       public Ack_Check
       public outchr
                                    ; for debug
       public RECEIVE_FLAG
                                    ; for sync_phase IO
       extrn VRTIF_Signal_I:far
                                    ; signal semaphore
       extrn VRTIF Wait:far
                                    ; wait on semaphore "P"
       extrn VRTIF 18259:abs
                                    ; address of 8259
       extrn VRTIF_vector_base:abs
                                    ; base of vector table
       extrn VRTIF_timestamp:far
                                    ; time stamping routine
       extrn Setup:near
                                    ; Initialize Network I/F
       extrn NET_Receive:near
                                    ; part of runtime code
       extrn Shut_Down:near
                                    ; if network ack failure
       extrn COLD_START:word
                                    ; NZ if this is first pass thru
       extrn SYNC_PHASE:word
                                     ; determines operational phase
       extrn NET_TABLE:byte
                                     ; provides network addresses
       extrn PID:word
                                     ; THIS processor's ID #
       extrn TASK DIRECTORY:word
                                    ; (DTCB) table of tasks
       extrn WATCH DOG:word
                                    ; (DTCB) table of watch dog timers
       extrn WATCH_LIST:word
                                    ; (DTCB) list of processors to watch
```

```
software support buffers
buff size
                            2048
                                   ; bytes in local buffer
                                   ; number of buffers
                            20
num_buff
                     equ
min_packet
                     equ
                            64
                                    ; minimum number of bytes in a packet
       segment common
cseg
              2800H
                                    ; makes listings eaiser to use!
       org
       assume cs:cseg,ds:cseg,es:cseg; ,ss:sseg
; NETWORK_INIT : load Interrupt Vector and clear pointers ;
IO_Network_Init:
       push
       push
              bx
       push
              СХ
       push
              dх
       push
              ds
  Do low level Network Interface Card Initialization
       call
              Setup
  init network variables
;
              [SEQUENCE],0
       mov
                                         ; zero out sequnce counter
       mov
              ax,cs
       mov
              ds,ax
              [RECEIVE PTR], eth_recv_begin ; receive pointer
       mov
                                         ; init semaphore
              [TX READY],1
       mov
       mov
              [TX_READY+2],0
              [TX_READY+4],0
       mov
              [RECEIVE_FLAG],0
       mov
                                           ; init flag
  Initialize Receive buffer list
              ax, RX BUFF_Q
       lea
              [RY_BUFF_HEAD],ax
       mov
       lea
              ax, RX BUFFER
                            ; points to actual buffers
       mov
              cx,num_buff
                            ; number to link
              bx,RX_BUFF_Q
                            ; points to buffer descriptors
Init30:
       MOV
              [bx],ax
                            ; put in current buffer pointer
              dx, [bx+4]
                            ; DX is address of next descriptor
       lea
       mov
               [bx+2],dx
                            ; put it in as next pointer
```

```
ax,buff_size
                                ; point AX at next buffer
        add
                bx,dx
                                : change descriptor pointer to next
        mov
                Init30
        loop
  now fix up last pointer
                word ptr [bx-2],0
                                         ; terminate list
        mov
  Initialize Outstanding Acknowledgements lists
       XOL
                ax,ax
                                        ; indicate none outstanding
                tACK_PENDING],ax
        MOV
                [ACK_HOLDING],ax
                                        ; or waiting to be xmitted
       mov
        lea
                si, ACK RECORDS
                                         ; all acks are free
                [ACK_FREE],si
        mov
                cx,num_buff-1 ; number to link (same as number of buffers)
Init40:
        lea
                dx,[si+ack_size]; DX is address of next descriptor
                [si],dx
                               ; put it in as next pointer
                si,dx
                                ; change descriptor pointer to next
        mov
                Init40
        loop
; fix up last pointer
                word ptr [si],0
                                       ; terminate list
  load interrupt vector if this is a cold start
                word ptr [COLD_START], OFFFFH
        test
        jΖ
                Warm_Start
                ax,0
        mov
                ds.ax
        mov
                bx,VRTIF_vector_base+(vector_number*4)
       mov
                ax, offset Interrupt_Handler
                [px],ax
        mov
                ax,cs
                (bx+2],ax
  Note: Preliminary board initialization was done in SETUP code, now
         just enable interrupts
Warm_Start:
                dx, VRTIF_18259+1
        mov
                al,dx
                                        ; get interrupt mask
                ah,OFEH
       mov
                                        ; mask to clear zero bit
        mov
                cl,vector_number
                                        ; load shift count register
                ah,cl
        rol
        and
                al,ah
                                        ; enable level
                dx,al
                                        ; update controller chip
        out
```

```
mov
              dx,nic_cr
                                   ; command register
              al,eth_access_page_0
                                   ; access NIC page O registers
      mov
              dx,al
      out
                                   ; interrupt mask register
              dx, nic_imr
      mov
       mov
              al,nic_prx+nic_ptx
                                   ; enable xmit/recv interrupts
              dx,at
       out
              ds
       pop
       pop
              dx
       pop
              CX
       pop
              аx
       pop
       ret
  routine for debugging only - all registers preserved
  Prints character in AL
outchr:
       push
              dx
       push
              ах
              dx,3fdh
       mov
out10:
              al,dx
       in
              al,20h
       and
       jΖ
              out10
              ax
       pop
              dx,3f8h
       mov
              dx,al
       out
       pop
              dx
       ret
header_size
                     10 ;words:dst=3,src=3,RCP=1,priority=1,seq=1,length=1
              equ
rcp_offset
                     12; bytes to receive control pointer
; XMIT - transmit the message specified by parameter list
         starting at address is at SS:bp+DEF_PARM_LIST
   NO GENERAL REGISTERS ARE PRESEVED
  NOTE: During system synchonization this routine works
        differently so as to avoid use to the vendor runtime and;
        provide more control to the appplication (no ack timeout);
        This is designated by the boolean "SYNC_PHASE"
; INPUTS:
              PID
                            ; destination processor ID
              CMD
                            ; command for this packet
                            ; Task for which the command operates
              TID
```

```
ENTRY
                                ; entry ID for the command (if applicable)
;
                MY_TID
                                ; originating Task ID
                PROFILE
                                ; profile pointer (in CS) for entry parameters
                                ; current calling mode (in or out)
                MODE
                                ; pointer (seg/offset) for parameter list
                PARM_LIST
;
IO_Xmit:
        push
                bр
        mov
                bp,sp
                                ; mark stack
                [SYNC_PHASE], OFFFFH; see if in sync phase, if so, don't use rts
        test
                Xmit_05
;
    Normally, we use vendor runtime to lock xmitter
        push
                                     ; push segment of transmit ctrl semaphore
                ax,TX_READY
        lea
        push
                                    ; push offset of semaphore
                зχ
                                     ; do p semaphore operation
        call
                VRTIF_Wait
;
   Now get acknowledge request buffer
;
        call
                Ack_Allocate
                                     ; returns ack buffer ptr in BX ;AAA
                Xmit_08
        jmp
    But During SYNC_PHASE we simply lock with a clear
Xmit_05:
                [TX READY],0
                                      ; set it not busy, set by Interrupt rtn
Xmit_08:
   put header in packet buffer
        cld
                                     ; set auto increment
                di,[CARD_RAM]
        les
                                    ; point to hardware buffer area
                si,[bp+DEF PID]
                                     ; fetch Destination Task PID
                cl.3
                                     ; mult by 8 (bytes/address entry)
        mov
                si,cl
                                     ; index into address table
                si,NET_TABLE[si]
                                    ; fetch address of dest.
        lea
                cx,DEF_addr_size
                                    ; in words
        тον
        гер
                MOVSW
                                     ; copy in dest address
        mov
                si,[PID]
                                    ; get our processor id
                cl,3
                                    ; mult by 8 (bytes/address entry)
        mov
                                    ; index into address table
        shl
                si,cl
        lea
                si, NET_TABLE[si]
                                    ; fetch our address
        mov
                cx,DEF_addr_size
        гер
                movsw
                                    ; copy in source addr
   skip over length field for now
        add
                di,2
  Update Sequence Number and put it in packet and acknowledge entry
```

```
;
                ax, [SEQUENCE]
                                    ; get sequence number
        mov
        inc
        stosw
                                    ; put in packet
                [SEQUENCE], ax
                                    ; update
        mov
                [SYNC_PHASE], OFFFFH; see if in sync phase
        test
                Xmit_09
        jnz
                                    ; put it in outstanding requests ;AAA
                [bx+ACK_SEQ],ax
        mov
                                    ; add this ack entry to the pending list ;AAA
        call
                Ack_Add
Xmit_09:
        mov
                ax,[bp+DEF_CMD]
                                    ; get packet command
                                    ; put in buffer
        stosw
                                    ; get Destination TID
                ax, [bp+DEF_TID]
        mov
        stosw
ï
                ax, [bp+DEF ENTRY]
                                       ; if entry applies
        MOV
        stosw
                ax, [PID]
                                        ; fetch my processor ID
        mov
        stosw
                ax, [bp+DEF_MY_TID]
                                       ; get my task ID
        mov
        stosw
  copy the parameters into the packet buffer. Use the TCB definitions
; to determine how many parameters, their size, and what type (ie. must
; allow for unconstrained arrays).
                si,[bp+DEF_Profile]
                                         ; get parameter profile ptr
        mov
                                         ; get number of parameters
        Lodsw
Xmit_10:
        or
                ax,ax
                                         ; see if done
        ίz
                Xmit 30
                                         ; if done
        mov
                [XPARM_COUNT],ax
                                         ; update parameter count
                ax,[si]
                                         ; get parameter Mode
        mov
                cx,[si+2]
                                         ; fetch parameter type/length
        mov
                si,4
        add
                [PROFILE_PTR],si
                                         ; save profile pointer
                                         ; point to parameter list
        lds
                si,[bp+DEF_parm_list]
                [si]
        push
                                         ; segment of data
                [si+2]
                                         ; offset of data
        push
                                         ; see if unconstrained type
        or
                cx,cx
                Xmit_15
        jge
   process an unconstrained object as a parameter, always copy descriptor
                [si+4]
        push
                                         ; descriptor segment
                (si+6]
                                         ; offset of descriptor
        push
        add
                word ptr [bp+DEF_parm_list],si; update parameter list index
        mov
                                         ; get offset of descriptor
        pop
```

```
ds
                                       ; get segment of descriptor
       pop
                                       ; save MODE of parameter
       push
               ax
               cx,[si+DEF_low_desc]
                                      ; get low bound of constraint
               es:[di],cx
                                       ; put in packet
       mov
       add
               di,2
               ax,[si+DEF_high_desc]
                                      ; get high bound of constraint
       mov
       stosw
                                       ; put in packet
       mov
               dx,[si+DEF_size_desc]
                                      ; get size of object
               es:[di],dx
                                       ; put in packet
               di.2
       add
  Copy the parameter data iff MODE is correct and array is not null
               bx
                                       ; get mode of parameter
       pop
       pop
               si
                                       ; get offset of data
                                       ; get segment of data
               ds
       pop
       and
               bx, [bp+DEF MODE]
                                       ; see if we should copy data
               Xmit 20
                                       ; if not, go on
        ίz
       sub
               ax,cx
                                       ; compute difference in range
       inc
                                       ; adjust to include end points
               аx
               Xmit 20
                                       ; if array is empty go to next parm
       jle
                                       ; compute size in words (descriptor)
       mul
               dx
                                       ; put in count register
       mov
               cx,ax
                                       ; transfer to packet buffer
       rep
               movsw
        jmp
               Xmit 20
                                       ; go on to next parameter
   Constrained parameter, CX is length in words, copy it into packet buffer
Xmit_15:
       add
               si.4
                                       ; move to next object address
       mov
               word ptr [bp+DEF parm list], si; update parameter list index
               s i
                                       ; get data offset
       pop
                                       ; get data segment
       pop
                                       ; see if mode is right
               ax, [bp+DEF_MODE]
        and
               Xmit 20
                                       ; skip copy of data if not
        jΖ
               СХ
                                       ; round up to nearest word count
        inc
                                       ; by adding one and divide by two
        shr
               cx.1
        rep
               MOVSW
Xmit_20:
               ax,cs
                                       ; restore data segment
       mov
       mov
               ds,ax
               si, [PROFILE PTR]
                                       ; get next parameter profile
       mov
               ax, [XPARM_COUNT]
                                       ; get the counter back in ax
       mov
                                       ; count down
       dec
               ax
        jmp
               Xmit_10
; Setup NIC registers to begin transmission
; Must prevent a RECEIVE interrupt from arriving, which would interfere
  with the registers being updated for Transmission.
```

```
load start address of packet
Xmit_30:
      pushf
                                 ; save interrupt status
      cli
                                 ; disable any interrupts
      mov
             dx,nic_cr
                                 ; select Page_0
             al,eth_access_Page_0
      mov
             dx,al
             dx, nic tpsr
                                 ; page start register
             al,eth_xmit_buf_start
                                 ; transmit page at DC00:0000
      mov
             dx,al
; load length of packet
             ax,di
                                 ; save current packet pointer
             di,[CARD RAM]
                                 ; point to hardware buffer area
      les
             ax, di
                                 ; subtract base to get size in bytes
      sub
                                 ; add offset to data length field
      add
             di,DEF_pkt_length
      stosw
                                 ; stick in PACKET length
                                 ; make sure it is at least minimum
      стр
             ax,min_packet
      jge
             Xmit 40
             ax,min_packet
      mov
Xmit_40:
             dx,nic_tbcr0
                                 ; load number to transfer into H/W
      out
             dx,al
      mov
             dx,nic_tbcr1
      mov
             al.ah
      out
             dx,al
; start transmit
             dx, nic cr
             al, send
                           ; command to initiate transmission
      mov
      out
             dx,al
                           ; restore interrupt status
      popf
                           ; restore bp
      pop
             bp
       ret
                           ; return and remove stack frame
INTERRUPT SERVICE ROUTINE
; Currently, this must have a stack frame similar to other vendor
; interrupt routines so that the interrupt-mode Signal routine will
  be able to find the interrupt return address and status
Interrupt Handler
                    label
      push
             bp
      mov
             bp,sp
      push
             ax
```

```
push
        push
                СX
        push
        push
                si
        push
                di
        push
                ds
        push
  First keep interrupt request line from triggering during processing
  of interrupts (and clearing interrupt bits)
                                ; for all string operations
       cld
                dx,nic_cr
        mov
                                ; select Page_0
        mov
                al,eth_access_Page_0
                dx,al
        out
                dx,nic_imr
                                        ; interrupt mask register
       mov
        mov
                al,eth ints disabled
                                        ; disable all interrupt requests
                dx,al
        out
  Process any packet receptions
  NOTE: since this is done inside the interrupt routine, interrupts
  are disabled, and therefore there is no interference from other
  interrupts is expected (especially clock interrupts).
  Careful attention to race conditions is necessary to prevent a received
  buffer from not getting processed and interrupts getting lost
Receive:
   point DS:SI to packet in hardware buffer
        lds
                si,cs:[CARD_RAM] ; source is ethernet RAM
        add
                si,cs:[RECEIVE_PTR]; add current receive buffer page address
                ax,[si]
                                ; fetch status into AL, NEXT PTR into AH
        MOV
                                ; since we only receive good packets ignore stat
                al,al
                                ; see if any packet arrived (if not zero!)
        jnz
                RECV100
                                ; go on if data is there
        jmp
                End_Receive
                                ; otherwise, leave the receive section
;$$$;
      No data left, go ahead and clear receive interrupt
;$$$;
           RACE CONDITION HERE...
;$$$;
: $$$
            mov
                    dx,nic_isr
                                    ; clear any pending receive interrupts
;555
            mov
                    al, nic_prx
                                    ; receive interrupt bit
;555
                                    ; clear receive interrupt (if present)
            out
                    dx,al
;$$$;
        put in little delay, then make sure nothing just arrived..
;$$$;
;$$$;
                    ax, 10
;$$$
            mov
;$$$RECV020:
;$$$
            dec
                    ax
```

```
RECV020
;$$$
           jnz
                                  ; see if something has arrived
;$$$
                   al,[si]
           mov
;$$$
                   al,al
           OF
                                  ; if nothing, good... no worries
; $$$
                   RECV030
           jΖ
;$$$; something did just arrive, see if we will see the interrupt
                                  ; fetch interrupt status now
;$$$
           in
                   al,dx
;$$$
           and
                   al, nic prx
                                 ; see if a receive interrupt was shut off
                   RECV040
                                  ; if we lost the interrupt go sound alarm
;$$$
           jΖ
:$$$RECV030:
                   Check_Xmit
;$$$
           jmp
;$$$;
;$$$; We shut off a receive interrupt by accident
;$$$;
;$$$RECV040:
: $$$
                   al,7
                                  ; aaa print bell
           mov
;$$$
           call
                   outch.r
                   al,'_'
                                  ; visible evidence
;$$$
           mov
;$$$
                   Check_Xmit
           jmp
;$$$
RECV100:
                               ; aaa check for non recieve ok ptr
               al,1
                               ; is it a one?
       cmp
               RECV101
       jΖ
               al,7
                               ; aaa print bell
       mov
       call
               outchr
               al,'?'
                               ; if non-zero print something special
       mov
                outchr
       call
RECV101:
                              ; zero low byte, leaving a new pointer
       XOL
               al,al
               ax,eth_offset ; correct for memory vs page offset
       sub
               cs:[RECEIVE_PTR],ax ; get ready for next reception
       mov
                              ; skip over receive header (status/page, count)
       add
  SI now points to first part of transmitted packet
  First check to see if it is an ACK message
               ax,[si+DEF pkt cmd]; check message type
       MOV
       ٥r
               ax,ax
                              ; command is negative for Acks
       jns
               RECV105
                               ; if regular packet, go on
   It is an ACK message. Clear it from pending list and free up buffer
       mov
               byte ptr [si-4],0 ; clear status flag for next time
       call
               ACK REMOVE
                              ; check off the ack
               END_RECEIVE
                              ; all done with one packet
       jmp
; Received a real message, first reload watchdog timer for the source PID
RECV105:
               di,[si+DEF_pkt_my_pid] ; get source processor ID
       mov
               di,di
       add
```

```
cs:WATCH_DOG[di],DEF_WATCH_DOG_LIMIT
        MOV
  Allocate a buffer, and transfer data to the buffer
  after the following call, the buffer descriptor is in BX. DO NOT DESTROY BX!
                                ; destination segment is CS
                ax,cs
        mov
        mov
                es,ax
                IO_Allocate
       call
                                ; destination offset is buffer header in BX
                di,cs:[bx]
                                ; get address of buffer in DI
       mov
                ax,[si+DEF_pkt_length]; get size of valid packet in bytes
        mov
                                ; make sure we get odd packets
        inc
        shr
                ax,1
                                ; convert to words
  Now transfer memory from hardware buffer pages to software buffer.
  Note that the buffer will wrap around at 4000H back to 2600.
  Also, the first word of each page is cleared after the data is removed
  so that received packets can easily be detected. (Since the header bytes
  are the last thing written, you are guaranteed that the whole packet has
  been received.)
                dx.80H-2
                                ; page size in words (reduced to get aligned)
        mov
RECV110:
                ax.dx
                                ; see if more than a page
        стю
                RECV120
        jge
                                ; otherwize only move the remaining words
                dx,ax
        mov
RECV120:
                cx,dx
        mov
                MOVSW
                                ; do the transfer
       push
                si
        dec
                                ; make sure we are in page just processed
                si,OFFOOH
                                ; backup to its beginning
        and
                byte ptr [si],0; and clear status byte for next time
       pop
                si,eth_recv_end; see if at end of hardware buffer
        cmp
        jnz
                RECV130
                si,eth_recv_begin; reset pointer to begin
        mov
RECV130:
        sub
                ax,dx
                                ; reduce total count by those moved
                RECV140
                                ; finished if so
        jΖ
                dx,80H
        mov
                                ; keep page alignment
                RECV110
        jmp
RECV140:
        mov
                ax,cs
                                ; restore data segment
                ds,ax
        mov
  Check what phase we are in. If Sync Phase, do not ack the message or
  invoke the distributed runtime.
        test
                [SYNC_PHASE],OffffH
                                        ; NZ means true (sync phase)
                RECV150
        jΖ
```

```
; System is still in synchronization phase, simply log that the message
; arrived by setting the RECEIVE FLAG with the buffer descriptor
; During sync phase, only one message can be recieved, so no concern
; for overwriting the RECEIVE_FLAG exists.
                [RECEIVE_FLAG],bx
        may
        jmp
                End_Receive
                                        ; done receiving
; Queue an ACK for the sendor then...
; Call Receive portion of Distributed Runtime code to determine
; what should be done with the newly arrived packet.
RECV150:
                si,[bx]
                                ; get beginning of buffer back
        mov
                Ack Hold
        call
                                ; first queue an Ack message to go out ;AAA
        call
                NET Receive
; END RECEIVE: do check on buffer, if no packet there, clear interrupts
End Receive:
                si,cs:[CARD_RAM]
        lds
                                  ; source is ethernet RAM
                si,cs:[RECEIVE_PTR]; add current receive buffer page address
        add
                                ; fetch status into AL, NEXT PTR into AH
        mov
                ax,[si]
                                ; since we only receive good packets ignore stat
                               ; see if any packet arrived (if not zero!)
        or
                Clear_Interrupt; go on if no data is there
        iz
        jmp
                Check_Xmit
  No data left, go ahead and clear receive interrupt
       RACE CONDITION HERE...
Clear Interrupt:
                dx,nic_isr
                               ; clear any pending receive interrupts
        mov
                               ; receive interrupt bit
        mov
                al,nic prx
                dx,al
                                ; clear receive interrupt (if present)
        out
;
    Check if we won the race...
        mov
                ax,10
CI10:
        dec
        inz
        test
                byte ptr [si], OFFH
                                        ; see if something just arrived
        jΖ
                Check_Xmit
    Something just arrived, see if we can see the interrupt
                al, dx
                                ; get interrupt status
        in
        and
                al, nic prx
        jnz
                Check Xmit
                                ; ok, we still see the interrupt
                al.7
                                ; print bell!
        mov
```

```
outchr
                                ; interrupt has been lost! due to race
       call
                al,'x'
        mov
        call
                outchr
  Now check for transmit complete interrupt
Check_Xmit:
                                ; insure data segment is for DRT
                ax,cs
                ds,ax
        mov
                dx,nic_isr
                                ; get interrupt status
        in
                al,dx
                                ; check for packet transmitted
                ax,nic_ptx
                Transmit
        jnz
;
  No xmit complete interrupts, see if there is a ACK to go out
                [ACK HOLDING], OFFFFH
                                        ; see if any acks are waiting to go out
        test
                                ; nothing to go out
        jΖ
                ax,[TX_READY]
                                ; check if transmitter is busy
                ax,ax
                EOI
                                ; still busy, just exit
        ile
        call
                ACK Send
                                ; otherwise send out one of the holding acks
        jmp
  Transmit complete, see if an ACK is waiting to go out. If so,
  send it. Otherwise signal READY semaphore.
Transmit:
                dx,al
                                        ; clear the transmit interrupt
        out
                [ACK_HOLDING], OFFFFH
        test
                                        ; see if any acks are waiting to go out
        iΖ
                transmit10
                word ptr [TX_READY]
                                        ; give credit for transmit complete
        call
                ACK_SEND
                                         ; go issue the ack
                EOI
        jmp
  Only free up transmitter if no acks waiting to go
; (and out of sync phase, Note: Acks never occur during sync phase)
transmit10:
                [SYNC_PHASE], OFFFFH;
                                         if sync phase, indicate free xmitter
        test
                transmit20
        jΖ
                [TX READY],1
                                        ; by setting it ready
        mov
                E01
        jmp
transmit20:
                                ; segment of semaphore
        push
                ax,TX_READY
        lea
                                ; offset of semaphore
        push
                VRTIF_Signal_I ; signal ready for next IO
        call
```

```
; Interrupt processing has been completed. Any new interrupts that have
; come in since clearing the status bit will be recorded by the 8259
  when we enable the 3Com card interrupt mask. This creates the edge
  trigger necessary for the 8259
EOI:
  Clear the 8259 Interrupt Request
       cli
                             ; make absolutely sure we don't nest
              dx, nic imr
                             ; point to mask register
       mov
       mov
              al, nic_ptx+nic prx; enable transmit (tx) and receive (rx) ints
              dx,al
       out
              al, NET_EOI
                             ; issue EOI to interrupt controller
       MOV
              dx, VRT1F_18259
       mov
       out
              dx,al
       pop
              es
                             ; restore registers and flags (interrupt)
              ds
       pop
       рор
       pop
              si
       рор
              dx
       pop
              СХ
              bx
       pop
       pop
              ax
       pop
              bo
       iret
IO_ALLOCATE - Allocates next buffer from Avail list
      Return BX pointing to buffer queue index.
      By design, the buffer should queue should never be empty.
  Destroys AX \, , BX has new descriptor pointer
IO_ALLOCATE:
       pushf
       cli
              bx,CS:[RX BUFF HEAD]
       mov
                                    ; fetch head pointer
              bx,bx
       or
                                    ; see if empty
       inz
              IO_ALLOC10
                                    ; go on if not
  Normally, might raise storage error here, but design prevents
  exceeding buffer capacity unless there is some code flaw.
       popf
       mov
              al,'M'
                                    ; print message
       call
              outchr
              al,'T'
       mov
                                        and
       call
              outchr
              3
       int
                                    ; trap
```

```
Remove buffer descriptor from free list
IO_ALLOC10:
             ax,CS:[bx+DEF_NEXT_PTR]; fetch next pointer
      MOV
      mov
             CS: [RX_BUFF_HEAD], ax
                                ; pull buffer off list, replace head
                                ; null next pointer in buffer
      XOL
      mov
             CS: [bx+DEF_NEXT_PTR], ax
      popf
      ret
; IO_DEALLOCATE - Deallocates buffer into Avail list
      Takes BX pointing to buffer descriptor.
     By design, the buffer should queue should never be full.
; Destroys AX
IO_DEALLOCATE:
      pusinf
      cli
      mov
             ax, [RX_BUFF_HEAD]
                                ; get head of list
             [bx+DEF_NEXT_PTR],ax
      mov
                                ; put behind this entry
      MOV
             [RX_BUFF_HEAD],bx
                                ; make this entry new head
      popf
      ret
; ACK_ALLOCATE - Allocates next buffer from Free list
     Return BX pointing to Ack entry.
     By design, the free list should never be empty.
 Destroys AX , BX has new descriptor pointer
     Interrupts are disabled to maintain list consistency
ACK_ALLOCATE:
      pushf
      cli
      mov
             bx, [ACK_FREE]
                                ; fetch head pointer
      Or
             bx,bx
                                ; see if empty
             ACK_ALLOC10
                                ; if failure
      jΖ
 Remove buffer descriptor from free list
             ax, [bx+ACK_NEXT]
                                ; fetch next pointer
      mov
             [ACK_FREE],ax
      mov
                                ; pull buffer off list, replace head
      XOL
             ax,ax
                                ; null next pointer in buffer
             [bx+ACK_NEXT],ax
      MOV
      popf
                                ; restore interrupts
      ret
  Normally, might raise storage error here, but design prevents
```

```
; exceeding buffer capacity unless there is some code flaw.
ACK_ALLOC10:
      popf
                                ; print message
      mov
             al,'M'
      call
             outchr
             al,'T'
                                     and
      mov
      call
             outchr
      int
             3
                                 ; trap
; ACK_DEALLOCATE - Deallocates buffer into Free list
      Takes BX pointing to buffer descriptor.
     By design, the ack list should never be full prior to call.
  Destroys AX
ACK_DEALLOCATE:
      pushf
      cl
             ax, [ACK_FREE]
                                ; get head of list
      mov
             [bx+ACK_NEXT],ax
                                ; put behind this entry
      MOV
             [ACK_FREE],bx
                                ; make this entry new head
      mov
      popf
      ret
ACK_ADD - Add another ack entry to the pending list
            Input: BX is ack entry
            ax is destroyed
Ack Add:
             si
      push
      push
             di
       push
                                 ;&&& save regs
             ax
       push
             CX
      push
             di
       and
             ax,OfffH
                                 ; only use 0-4095
       push
                                 ; push sequence # &&&
             aх
       call
             VRTIF_Timestamp
                                 ; time-stamp it &&&
             di
       pop
       pop
             СX
             aх
                                 ; &&& restore regs
       pop
      pushf
      cli
       mov
             ax,cs:[ACK_TIMER]
       add
             ax,ack_delay
                                 ; number of ticks
             cs: [bx+ACK_COUNT], ax
       mov
  Find the end of the ack list
```

```
si,ACK PENDING
                                      ; point to header
       lea
ack add10:
                                      ; fetch next pointer
               di,cs:[si]
       mov
       or
               di.di
                                      : see if at end
                                      ; jump if so
               ack add20
       jΖ
       mov
               si,di
                                      ; go down the list
               ack_add10
       jmp
ack_add20:
       mov
               cs:[si],bx
                                      ; put behind this entry
       popf
               di
       pop
               si
       pop
        ret
ACK REMOVE - Remove ack entry from the pending list.
     THIS IS ONLY CALLED DURING RECEIVE INTERRUPT ROUTINE
              SI : points to ACKNOWLEDGE PACKET BUFFER IN HARDWARE
                   relative to the DS segment which points to the
                   hardware packet buffer (NOTE: ACK's are never
                   unloaded from the hardware buffer).
              ax,bx,si,cx destroyed
   NOTE: ACK's have the SEQUENCE they are acking in the normal
         sequence field.
Ack_Remove:
        mov
               cx,[si+DEF_pkt_sequence]
                                        ; get SEQUENCE value
                                      ;&&& save regs
        push
        push
               СХ
        push
               di
               cx,OfffH
                                         ; only use 0-4095
        and
        push
               СХ
                                         ; &&& push for timestamp
               VRTIF_timestamp
                                         ; &&&
        call
        рор
               СХ
        pop
                                      ; &&& restore regs
        pop
               bx, ACK_PENDING
        lea
Ack_remove10:
               si,cs:(bx)
                                         ; get next pointer
        mov
               si,si
        or
                                         ; exit if at end of list
        jΖ
               ack remove30
                                         ; all done (not there!!)
               cx,cs:[si+ack_seq]
                                         ; check for matched sequence
        стр
               ack remove25
        jΖ
ack_remove20:
               bx,si
                                         ; bx is always the previous pointer
        mov
```

```
jmp
              ack_remove10
  Found the entry, remove from pending, and place it on FREE list
ack remove25:
       mov
              ax,cs:[si]
                                     ; get next in list
              cs:[bx],ax
       mov
                                      ; link over removed entry
; put removed node into free list
              ax,cs:[ACK_FREE]
       mov
                                     ; get head of list
              cs:[si],ax
                                     ; put behind this entry
       mov
              cs:[ACK_FREE],si
       mov
                                     ; make this entry new head
ack_remove30:
       ret
ACK_HOLD - Add another ack message to the holding list
             Input: SI points to received message
Ack_HOLD:
       push
                                   ; save message descript-
              ACK_ALLOCATE
                                   ; fetch a free ack entry
       call
       mov
              ax,[si+DEF_pkt_my_pid] ; get pid of sender
              [bx+ack_pid],ax
       mov
                                   ; put in record
       mov
              ax, [si+DEF_pkt_sequence]; fetch received Sequence #
       mov
              [bx+ack_seq],ax
                                   ; stick in record
   put at end of HOLDING list
       push
              si
       push
              si,ACK_HOLDING
       lea
ack_hold10:
       mov
              di,[si]
                                   ; fetch ptr
              di,di
       or
                                   ; see if at end
              ack_hold20
       jΖ
                                   ; jump if so
              si,di
       mov
                                   ; go down the list
       jmp
              ack_hold10
ack_hold20:
       mov
              [si],bx
                                   ; put behind this entry
       pop
              di
       pop
              si
                                   ; restore received message pointer
                                   ; restore message descriptor
       pop
       ret
ACK_SEND - Transmit next acknowledge message on the HOLDING list
;
```

```
NOTE: This is ONLY called during interrupt servicing when the
   transmitter is available. This prevents interference with the
   IO_XMIT routine above. (They both access the H/W)
   INPUTS:
        The PID and SEQUENCE number to acknowledge is at the head of
        the "HOLD" queue.
   Acknowledgements simply have: DST, SRC, length, ACK_SEQ, ACK_CMD
ACK_Send:
                                ; save all registers
        push
                aх
                bx
        push
        push
                сх
        push
                dx
        push
                si
        push
                di
        push
                ds
        push
                es
                word ptr [TX_READY]
                                         ; mark transmitter as busy
        dec
  Get entry off of HOLDING list
                bx, [ACK_HOLDING]
        mov
                ax, [bx]
                                     ; get next pointer
        mov
                                     ; remove this ACK from holding list
                [ACK_HOLDING],ax
        mov
                                     ; get processor id of originator
        mov
                si,[bx+ack_pid]
                dx, [bx+ack_seq]
        mov
                                     ; and sequence #
                ACK_DEALLOCATE
                                     ; put on free list
        call
   Build acknowledge packet
        cld
                                     ; set auto increment
                di, [CARD_RAM]
                                     ; point to hardware buffer area
        les
        mov
                cl,3
                                     ; 8 bytes per net table index
                                     ; convert PID to net table index
        shl
                si,cl
                si,NET_TABLE[si]
                                     ; fetch address of dest.
        Lea
                cx,DEF_addr_size
                                     ; in words
                movsw
                                     ; copy in dest address
        rep
                si,[PID]
                                     ; get our processor id
        mov
                cl,3
                                     ; mult by 8 (bytes/address entry)
        mov
        shl
                si,cl
                                     ; index into address table
        Lea
                si, NET_TABLE[si]
                                     ; fetch our address
                cx,DEF_addr_size
        mov
                movsw
                                     ; copy in source addr
        rep
   length field is fixed to include up to command only
                 ax,DEF_pkt_cmd+2
        mov
                                     ; put in buffer
        stosw
```

```
Put ACKING Sequence Number in packet
     mov
            ax, dx
                          ; get acking seq number
      stosw
                           ; put in buffer
            ax,DEF_ACK
                           ; set command to ACK
      mov
                           ; stuff in buffer
      stosw
; That's it for loading the packet buffer, now kick off transmission
; Setup NIC registers to begin transmission
mov
           dx, nic cr
                              ; select Page_0
     mov
           al,eth_access_Page_0
      out
           dx,al
      mov
            dx,nic_tpsr
                             ; page start register
            al,eth_xmit_buf_start ; transmit page at DC00:0000
      mov
      out
            dx,al
; load length of packet
      mov
            ax,min_packet
                              ; make it is the minimum
            dx,nic tbcr0
      mov
                              ; load number to transfer into H/W
      out
            dx.al
            dx,nic_tbcr1
      mov
      mov
            al,ah
      out
            dx,al
; start transmit
            dx,nic_cr
      mov
      mov
            al, send
                        ; command to initiate transmission
            dx,al
      out
      DOD
            es
            ds
      pop
            di
      DOD
            si
      pop
      DOD
            dx
      pop
            СХ
      pop
            bx
      pop
      ret
ACK_CHECK - Check to see if any acknowledgment requests are over
           ACK_LIMIT clock interrupts old. Cause shutdown if so.
INTERRUPT
                      SERVICE
                                   ROUTINE
······
  This routine is invoked by the runtime timer interrupt routine to
   allow the network IO funtions to be checked. As a result,
  ALL REGISTERS except AX and DS MUST BE PRESERVED!!!
  Note: because this routine is run every 5 milliseconds, it has
```

```
been optimized to take up little time in the typical case. For
    this reason, there are three exit points which are executed in
   straight line code.
ACK_CHECK:
        mov
                ax,cs
        mov
                ds,ax
  For each communicating processor, check watch dog timer
        push
                si
        push
                di
                СХ
        push
                si, [WATCH_LIST]
        mov
                cx,[si]
        mov
                                         ; get number of processor to watch
                ACK08
        jcxz
        add
                si,2
ACK05:
                di,[si]
        mov
                                         ; get processor ID of next to watch
        add
                di, di
                word ptr WATCH_DOG[di] ; check watch_dog
        dec
                                         ; FAILURE
        jΖ
                ACK20
        add
                si,2
                ACK05
        loop
ACKO8:
                СХ
        pop
        pop
                di
                si
        pop
  Now see if any outstanding messages
                ax, [ACK_PENDING]
        mov
                                         ; get pending list
        or
                ax,ax
        jnz
                ACK10
                                         ; if message on list, must check
ACK10:
                si
        push
                si,ax
        MOV
                ax, [ACK_TIMER]
        mov
                                         ; get current TIME
        inc
                [ACK_TIMER],ax
        mov
                                         ; update
                ax,[si+ack_count]
                                         ; see if our TIME is UP
        jΖ
                ACK20
        pop
                si
        retf
                                         ; return, still time before error
ACK20:
                al,TIMER_EOI
        mov
                                         ; clear timer channel interrupt
        mov
                dx, VRT1F 18259
                dx,al
        out
                al,OfdH
        mov
                                         ; shut off everything but keyboard
```

```
inc
                                 ; point to mask reg
      out
             dx, al
      XOL
             ax,ax
      push
             VRTIF_Timestamp
      call
                                ; Acknowledgment timed out!
      jmp
             Shut_Down
; Data AREA
align
ISR
             d₩
                   ?
                                ; interrupt status register
PACKET SIZE
                                ; packet size
CARD RAM
             dd
                   0dc000000h
                                ; address of ram buffer on enet card
RECEIVE PTR
             dw
                                ; points to current next page to rcv
XPARM_COUNT
                               ; number of xmit params left to copy
                                ; current ptr to parameter profile
PROFILE_PTR
             dw ?
SEQUENCE
                                ; this processor's packet sequencer
NULL_LIST
                                ; zero parameters
; The following semaphore is used to provide mutual exclusion to the
; transmit side of the Ethernet card.
                               ; semaphore count
TX READY
                                ; task value
                                 ; task value
RECEIVE FLAG
                               ; contains buffer desc in SYNC_PHASE
; BUFFER QUEUE STRUCTURE
     record
        BUFFER OFFSET
        NEXT_PTR
      end record;
RX BUFF HEAD
             d₩
RX BUFFER
             db
                    num_buff dup (buff_size dup (?))
RX_BUFF_Q
                    num_buff dup (2 dup(?)) ; (BUFFER_PTR, NEXT_DESC_PTR)
; The outstanding packet acknowledgement queue contains the
; Task id and the sequence number used when transmitting
; each (non-acknowledgement) message. When an acknowlegement message
; is received, this list is checked and if the ids are found,
```

```
; they are removed. If they are not found, the acknowledgement is
; trashed (this should not occur unless the master CPU restarts while
; a slave is still transmitting an acknowledge. However, in future
  versions with retry implemented, multiple acknowledgements may be
  possible.
  Queue structure:
                     * NEXT QUEUE PTR
                       TASK ID
                     ******
                       SEQUENCE NUMBER
                     *******
                     * TIMER
                                 COUNTER
                     *******
ack_delay
               equ
                      20
                                      ; interrupts = 5 ms each
ACK_TIMER
               d₩
                      ?
ACK_FREE
               d₩
                      ?
                                      ; list of unused acks
ACK_PENDING
               d₩
                                      ; list of acks we are waiting for
ACK_HOLDING
               d₩
                                      ; list of acks waiting to go out
ack_size
               equ
                      8
                                      ; bytes per entry
; list structure
ack_next
                      0
                                     ; point to next in ack list
               equ
ack_pid
                      2
                                      ; processor ID of packet to be acked
               equ
ack_seq
                      4
               equ
                                      ; sequence number
ack_count
               equ
                                      ; counter (used to time-out ack)
ACK_RECORDS
                      num_buff dup (ack_size dup(?)) ; NEXT, SEQ#, COUNT
cseg
               ends
               end
```

```
page
              55,132
       TITLE VRTIF - Vendor Runtime Interface Module
; file: DA_VRTIF
; Distributed Ada - Vendor Runtime Interface
; This module provides the addresses within the
; Vendor supplied runtime for required tasking primatives.
; Copyright(C) 1989, LabTek Corporation
include DA_DEF.ASM
              public VRTIF_I8259, VRTIF_vector_base
              public VRTIF_Wait, VRTIF_Signal, VRTIF_Signal_I
              public VRTIF_Lower_Priority
              public VRTIF_Create_Task
              public VRTIF_Activate_Complete
              public VRTIF_Entry
              public VRTIF_Rendezvous_Complete
              public VRTIF_Accept
              public VRTIF_Select
              public VRTIF_PutCh
              public VRTIF_Timestamp
              public VRTIF_TCBTID
              public VRTIF_APPLICATION
              public VRTIF_Init
              public VRTIF_task_ptr
              public VRTIF_DS
              public VRTIF_SELECT_REC
              extrn
                     Create_Task:near
              extrn
                    Activate_Complete:near
                     Request_Entry:near
              extrn
                     Rendezvous_complete:near
              extrn
              extrn
                    Select:near
              extrn
                     Accept:near
                     PID:word
              extrn
VRT1F_18259
                     20H
              equ
                               ; address of interrupt controller chip
                               ; offset in TCB to priority (identifies task)
VRTIF TCBTID
              equ
                     22
VRTIF_SELECT_REC equ
                               ; bytes per stack record for each open
                               ; alternative in a select statement
far_jmp_instruction
                            000EAH; jmp intersegment
                     equ
retf2 instruction
                            OCAO2H; return intersegment pop two bytes
retf instruction
                            000CBH; return far
                     equ
short_call_instruction equ
                            000E8H; short call
```

```
NOTE: During start up (COLD START) the vector base is that of DOS. After
  runtime intitialization has completed it is moved to 200H. However, this
  occurs after the network initialization, and the vectors in place at 20H
  are moved to 200H. Subsequent restarts are "WARM" and do not effect any
  of the interrupt vector tables.
VRTIF_vector_base
                              20H
                                        ; Initial base of vector table (DOS)
                segment at DEF_VRTIF_ADDR
VRTIF
                                         ; this is just for convience
                        0
                org
VRTIF_DS
                                         ; actually in different segment
                label
                        word
                        970H
                                         ; offset in RT DS
                ora
                                         ; offset in DS for current task
VRTIF_TASK_PTR
               label
                        word
    RUNTIME TASKING CALLS ADDRESSES TO ALLOW VECTORING TO THE DISTRIBUTED
    Ada RUNTIME
                         11DAH
                org
R1Accept
                label
                         far
                                         ; simple accept
                         12C1H
                org
R1Entry
                label
                                         ; simplecallentry uncond
                         13F3H
                org
                                         ; rendezvouscomplete
R1RendezvousComplete label
                              far
                         1476H
                 org
R1Activated
                 label
                         far
                                         ; activated
                         161CH
                 org
R1CreateTask
                label
                         far
                                         ; createtask
                         1D28H
                 org
R1Select
                label
                                         ; select
                         2608H
VRTIF_SETPRIORITYLOWER label
                                far
                         3988H
                 org
VRTIF_PutCh
                 label
                         far
                                         ; put_character
                         3CCEH
                 org
                                         ; timestamp
VRTIF_Timestamp label
                         far
                                         ; patch for short calls to set priority
                         40FOH
                 org
PATCH_40F0
                 label
                         far
                         519BH
                 org
```

```
VRTIF_Signal_I label
                        far
                                ; R1TESI?VI V semaphore operation (interrupt)
                org
                        51D0H
VRTIF_Wait
                        far
                label
                                ; R1TESS?P P semaphore operation (non interrupt)
                        51E6H
                org
VRTIF_Signal
                label
                        far
                                ; R1TESS?V V semaphore operation (non-interrupt)
VRTIF
                ends
    APPLICATION ENTRY ADDRESS
vrtif2
                segment at 5374H
                org
                        6
VRTIF_APPLICATION label far
vrtif2
                ends
        segment common
        assume cs:cseg,ds:cseg,es:cseg
                1100H
; perch in calls to distributed runtime to allow runtime checking
  of distribution.
VRTIr_Init:
                ax,DEF_VRTIF_ADDR
        mov
                                        ; segment for Vendor Runtime
                es,ax
        MOV
; Crcate Task
                di,R1createtask
        lea
        mov
                byte ptr es:[di],far_jmp_instruction
        inc
        lea
                ax,Create_Task
                es:[di],ax
        mov
        add
                di,2
                ax,cs
        mov
                es:[di],ax
        mov
; Activate Complete
        lea
                di,R1Activated
        mov
                byte ptr es:[di],far_jmp_instruction
        inc
        lea
                ax,Activate_Complete
                es:[di],ax
        add
                di,2
        mov
                ax,cs
                es:[di],ax
        mov
```

```
; entry calls
        lea
                di,R1entry
       mov
                byte ptr es:[di],far_jmp_instruction
        inc
       lea
                ax,Request_Entry
       mov
                es:[di],ax
                di,2
        add
                ax,cs
       mov
       MOV
                es:[di],ax
; end rendezvous
        lea
                di,R1rendezvouscomplete
       MOV
                byte ptr es:[di],far_jmp_instruction
        inc
        lea
                ax, Rendezvous_Complete
                es:[di],ax
       mov
                di,2
        add
                ax,cs
       mov
                es:[di],ax
; Select
                di,R1Select
        lea
                byte ptr es:[di],far_jmp_instruction
        mov
        inc
        lea
                ax, Select
                es:[di],ax
        mov
                di,2
        add
                ax,cs
        MOV
                es:[di],ax
        πον
; Accept
                di,R1Accept
        lea
        mov
                byte ptr es:[di],far_jmp_instruction
        inc
        lea
                ax,Accept
                es:[di],ax
                di,2
        add
                ax,cs
                es:[di],ax
   SETUP special short call transfer area at end of patch for setting
    the priority
        lea
                di,PATCH_40F0
        mov
                byte ptr es:[di],short_call_instruction
        MOV
                word ptr es:[di+1],offset VRTIF_SETPRIORITYLOWER-(PATCH_40F0+3)
                byte ptr es:[di+3],retf_instruction
        MOV
: If this is not the master, do not use PutChar... must patch
```

```
; it out.
                CS:[PID],OFFFFh
        test
                                   ; master is always zero
        jΖ
                Init_10
        lea
                di, VRTIF_PutCh
                word ptr es:[di],retf2_instruction
        mov
                byte ptr es:[di+2],0; high half of count
        MOV
Init_10:
        mov
                ax,cs
                                ; restore data segment
        mov
                ds,ax
        ret
        assume ds:VRTIF
VRTIF_Accept:
                ds, [VRTIF DS]
        MOV
        mov
                si,[VRTIF_TASK_PTR]
        jmp
                R1Accept+8
VRTIF_Entry:
                ds,[VRTIF_DS]
        mov
        mov
                si,[VRTIF_TASK_PTR]
                R1Entry+8
        jmp
VRTIF_Rendezvous_Complete:
        mov
                ds, [VRTIF_DS]
                si,[VRTIF_TASK_PTR]
        mov
                R1rendezvouscomplete+8
        jmp
VRTIF_Activate_Complete:
                ds, [VRTIF_DS]
        mov
        mov
                si,[VRTIF_TASK_PTR]
                R1activated+8
        jmp
VRTIF_Create_Task:
        push
                bp
        mov
                bp,sp
                ds, [VRT[F_DS]
        mov
        jmp
                R1createtask+7
VRTIF_Select:
        push
                bр
        mov
                sp,دط
                ds, [VRTIF_DS]
        mov
        jmp
                R1select+7
VRTIF_Lower_Priority:
        call
                Patch_40F0
        ret
```

cseg ends

end

```
page
           55,132
     TITLE D.CB - Distributed Task Control Block Module
; FILE: DA_DTCB.ASM
; DA - Distributed Task Control Block Module
; Copyright(C) 1989, LabTek Corporation, Woodbridge, CT
; Ver Date
           Description
; 0.2 Dec-89 : Enhanced to support error detection and dynamic
           configuration
include DA_DEF.ASM
      .model large
     public DTCB_INIT
     public SYNCHRO_SEMAPHORE
      public CONTINUE_SEMAPHORE
      public TASK_DIRECTORY
      public proc_table_size
      public REMOTE_CPU_TABLE
      public NAME_TABLE
      public WATCH LIST
      public WATCH_DOG
      extrn MODE_SELECT:word
      extrn PID:word
csea
      segment common
      assume cs:cseg,ds:cseg,es:cseg
           HOOAO
Task Control Blocks
      Semaphore
                 3 words
      Reply Pointer 1 word
      Return Address 2 words
     Num Entries
                 1 word
     Entry Table
                 N * 3 words (where N is the number of entries)
           Profile Ptr 1 word
           Wait Flag
                       1 word
           Entry Queue
                       2 words
```

```
Profile List:
              Number of Parameters
                                    1 word
                                    1 word ("in", "out", "in out")
              Mode
              Type/Length
                                    1 word (negative means unconstrained)
The TCB contains a synchronize semaphore which is used to
    suspend itself and wait for a signal from another task.
    This is followed by a reply pointer used to hold the buffer
    descriptor of the message to which a reply is due. Then
    the entry information is provided. This begins with the number
    of entries for this task, followed by a record for each entry.
    Each entry record contains:
     - A profile pointer which provides the offset within the CS for
       information on the parameter profile for this entry.
     - A waiting flag used by the accepting task to indicate that it
       has suspended waiting for a call on this entry (and possibly
     - A buffer List Pointer, This points to the buffer descriptor
       for the first caller to this entry. The buffer descriptor
       provides the actual buffer address and a link to the next
       descriptor. This provides the FIFO queue for each entry.
semaphore
              struc
              dw
                      3 dup (?)
semaphore
              ends
TCB_INIT - Initialize Distributed Task Control Blocks
DTCB_Init:
               ax,ax
       mov
               [SYNCHRO_SEMAPHORE], ax
               [SYNCHRO_SEMAPHORE+2],ax
       mov
               [SYNCHRO_SEMAPHORE+4], ax
       mov
               [CONTINUE_SEMAPHORE],ax
       MOV
               [CONTINUE_SEMAPHORE+2], ax
       mov
       mov
               [CONT!NUE_SEMAPHORE+4],ax
   Initialize Distributed Task Directory Based on Current Operating Mode
   This calls the Init_TCB for each task to initialize its structures.
INIT_DIRECTORY:
       mov
              si, [MODE_SELECT]
                                            ; fetch mode
              si
       dec
                                            ; mode1 => offset 0
       add
              si,si
                                            ; make word index
              si,MODE_TABLE[si]
       mov
                                            ; fetch address of mode values
```

```
cx,total_tasks
       mov
               di,TASK_DIRECTORY
       lea
               ax,ds
       mov
               es,ax
       mov
       XOL
               ax,ax
       cld
INIT_DIR_LOOP:
       movsw
                                     ; transfer local/distrib flag
                                     ; transfer PID
       MOVSW
                                     ; get TCB pointer
       MOV
               bx,[di]
       call
               Init_TCB
               di,2
                                     ; skip the distrib TCB pointer
       add
                                      ; zero counter for tasks of this type
       stosw
               INIT_DIR_LOOP
       loop
   Initialize Watch Dog Timer Information based on configuration and
   this processor's ID
       mov
               ax,[PID]
       mov
               bx,DEF_max_cpus*2
                                     ; bytes per cpu entry
               bx,ax
                                      ; BX = cpu offset
       mov
               ax, [MODE SELECT]
       MOV
                                      ; adjust by one to make zero origin
       dec
               dx,(DEF_max_cpus*DEF_max_cpus) * 2 ; bytes per mode table
       mov
       mul
               si,ax
       mov
               si, WATCH TABLE[si+bx] ; get address of watch list entry
        lea
                                     ; set value for other's use
               [WATCH_LIST],si
       mov
                                      ; fetch number of timers to init
        mov
               cx,[si]
               Watch_init_done
        icxz
               si,2
        add
Watch_Init:
               bx,[si]
                                      ; fetch PID of processor to watch
        mov
               bx,bx
        add
        mov
               WATCH_DOG[bx], DEF_WATCH_DOG_LIMIT; init timer for this pid
               si,2
        add
               Watch_init
        loop
Watch_init_done:
        ret
Init_TCB : zero all semaphore words and entry table values for the TCB
    pointed to by:
                               BX points to TCB of interest
                       INPUT:
                               AX contains zero
Init_TCB:
                              ; does this task have a TCB?
               bx,bx
        or
```

```
jΖ
               Init_TCB_30
                              ; exit if not applicable
       mov
               [bx],ax
                              ; clear semaphore
               [bx+2],ax
       WC.A
               [bx+4],ax
       mov
       push
               cx,[bx+DEF_num entries]
       MOV
                                          ; fetch number of entries
               bx,DEF_entry_table[bx]
       lea
               Init_TCB_20
       icxz
                                          ; if no entries
Init_TCB_10:
               DEF_entry_wait[bx],ax
       mov
                                          ; zero wait flag
               DEF_entry_queue(bx),ax
       mov
                                          ; zero buffer descriptor
       mov
               DEF_entry_queue+2[bx],ax
                                          ; zero next pointer
               bx,size DEF_entry_rec
       add
                                          ; go to next record
               Init_TCB_10
       t oop
Init_TCB_20:
       рор
               СХ
Init_TCB_30:
       ret
       align
Configuration Mode Control
   THESE TABLES WOULD NORMALLY BE PRODUCED BY A CONFIGURATION CONTROL
   TOOL, BUT FOR PROTOTYPE PURPOSES THEY ARE GENERATED BY HAND.
   The current configuration control allows for four different
   operating modes and three processors (alpha, bravo, and charlie):
     MODE 1: All tasks are on the alpha processor
     MODE 2 : All BDS tasks are on alpha, all simulator tasks are on bravo.
     MODE 3: All BDS tasks are on alpha, all simulator tasks are on charlie.
     MODE 4: All BDS tasks except one of the guidance tasks is on alpha,
              the simulator is on bravo, and one guidance task is on chartie
   The mode (below) is initialized during system startup. The master
   processor asks the operator which mode to use. If a system failure
   occurs, the master shuts down the system and brings it back up as
   a single processor version. Note that a system function:
   Configuration Table - for each task, the location is defined in terms
   of current operating mode.
MODE_TABLE
               label
                       word
               offset MODE1
               offset MCOE2
       d₩
               of:set MODE3
       dw
       d₩
               offset MODE4
```

```
; TASK LOCATION DIRECTORY because of lack of compiler
  support, very little information is available to uniquely correlate
  tasks during runtime calls. As a workaround, unique priorities
  are used for each task type, and counters are supplied for multiple
 tasks within the type which modify the identification with respect
  to the task priority. In this way, each task can be quickly correlated
  to its distributed characteristics at runtime.
  The following directory contains entries for each task and is indexed
  by task priority. The entries are:
            <LOCAL/DISTRIBUTED FLAG> <PID> <DIST TCB PTR> and <spare>
  The LOCAL/DISTRIBUTED FLAG indicates if all entry calls are local. If
  one is distributed, they must all go through the distributed runtime,
  even if the call being made is local. <PID> is the processor that the
  task is resident on. For calls being made through the distributed
  runtime, additional task control information is located by the pointer
  DIST TCB PTR.
  This directory is initialized during configuration time based on
  operator or automatic mode selection. The first two values are
   set according to mode, the last two are statically defined.
TASK DIRECTORY label
       dw
               4 dup (0)
                                                   ; dummy to offset 32
        d₩
               ?.?
                         SAVE TCB,
                                                   ;(12)save
                                                                      31
               ?,?, DISPLAY_TCB, ?
                                                   ;(11)display
                                                                      30
        d₩
               ?,?, TRACK DAT TCB,
                                                                      29
                                                   ;(10)track_data
               ?,?, REPORTBUF_TCB,
        d₩
                                                   ;(09)report_buf
                                                                      28
        d₩
               ?,?, GUIDEBUF_TCB,
                                     ?
                                                                      27
                                                   ;(08)guide buf
        dw
               ?,?, ROCKSUP TCB,
                                     ?
                                                   ;(07)rock sup
                                                                      26
        dw
                ?,?, TARGSUP TCB,
                                                   ;(06)targ_sup
                                                                      25
        d₩
                ?,?,
                      CONTROL TCB,
                                     ?
                                                   ;(05)control
                                                                      24
               ?,?, GUIDANCE1_TCB,
                                     ?
                                                                      23
        d⊌
                                                   ;(04)guidance(1)
        СW
                ?,?, GUIDANCE2_TCB,
                                                   ;(03)guidance(2)
                ?,?,
                                     ?
        dw
                       TRACK_TCB,
                                                   ;(02)track
                                                                      21
        d₩
                ?,?,
                       UPDATE TCB,
                                                                      20
                                                   ;(01)update
        d₩
                        MAIN_TCB,
                                    ?
                                                   ;(00)bds
                                                                      19
                ?,?,
                       ($-TASK DIRECTORY)/8; must follow definitions above
total_tasks
                equ
local_entries
                       n
                equ
dist_entries
                equ
   For each mode (of four) the local/distrib flag must be set and the pid
```

must be set.

```
MODE1
        label
                word
                 DISTRIBUTED , PID
;
                -----
;
        d⊭
                2 dup (0)
                                           ; dummy to offset priority by one
                local_entries, DEF_alpha
        d₩
                                                      ;(12)save
        d₩
                local_entries, DEF_alpha
                                                      ;(11)display
        dw
                local_entries, DEF_alpha
                                                      ;(10)track_data
        dw
                local_entries, DEF alpha
                                                      ;(09)report buf
                local_entries, DEF_alpha
        dw
                                                      ;(08)guide_buf
                local_entries, DEF alpha
        dw
                                                      ;(07)rock_sup
        dw
                local_entries, DEF_alpha
                                                      ;(06)targ_sup
        d₩
                local_entries, DEF alpha
                                                      ;(05)control
        dw
                local_entries, DEF_alpha
                                                      ;(04)guidance(1)
        d₩
                                   DEF NA
                                                      ;(03)guidance(2)
        dw
                local_entries, DEF_alpha
                                                      ;(02)track
        d₩
                local_entries, DEF alpha
                                                      ;(01)update
        d₩
                local_entries, DEF_alpha
                                                      ;(00)bds
MODE2
        label
                word
                 DISTRIBUTED , PID
        d₩
                2 dup (0)
                                           ; dummy to offset priority by one
        dw
                local_entries, DEF_alpha
                                                      ;(12)save
        d₩
                local_entries, DEF alpha
                                                      ;(11)display
        dw
                local_entries, DEF_alpha
                                                      ;(10)track_data
        d₩
                dist_entries,
                               DEF_bravo
                                                      ;(09)report_buf
        d₩
                dist_entries,
                               DEF_bravo
                                                      ;(08)guide_buf
        dw
                !ocal_entries, DEF bravo
                                                      ;(07)rock_sup
        dw
                dist_entries,
                               DEF_bravo
                                                      ;(06)targ_sup
                local_entries, DEf_alpha
        ď₩
                                                      ;(05)control
        d₩
                local_entries, DEF alpha
                                                      ;(04)guidance(1)
        dw
                            0.
                                   DEF NA
                                                      ;(03)guidance(2)
        d₩
                local_entries, DEF alpha
                                                      ;(02)track
        dw
                local_entries, DEF alpha
                                                      ;(01)update
        du
                local_entries, DEF_alpha
                                                      ;(00)bds
MODE3
       Label
                 DISTRIBUTED , PID
       dw
                2 dup (0)
                                           ; dummy to offset priority by one
                local_entries, DEF alpha
       dw
                                                      :(12)save
        dw
                local_entries, DEF_alpha
                                                      ;(11)display
        d₩
                local_entries, DEF alpha
                                                      ;(10)track_data
        dw
                dist_entries,
                               DEF_charlie
                                                      ;(09)report_buf
        dw
                dist_entries,
                               DEF charlie
                                                      ;(08)guide_buf
        d₩
                local_entries, DEF_charlie
                                                      ;(07)rock_sup
        d₩
                dist_entries,
                               DEF charlie
                                                      ;(06)targ_sup
       d₩
                local_entries, DEF_alpha
                                                      ;(05)control
        dw
                local_entries, DEF_alpha
                                                      ;(04)guidance(1)
```

;

```
dw
                     ٥,
                          DEF_NA
                                        ;(03)guidance(2)
      d₩
            local_entries, DEF_alpha
                                        ;(02)track
      d₩
            local entries, DEF alpha
                                        ;(01)update
            local_entries, DEF_alpha
      dw
                                         ;(00)bds
MODE4
     label
           word
            DISTRIBUTED , PID
            -----
                                ; dummy to offset priority by one
      d₩
            2 dup (0)
      d₩
            local_entries, DEF_alpha
                                        ;(12)save
            local_entries, DEF_alpha
      d₩
                                        ;(11)display
            local_entries, DEF_alpha
                                        ;(10)track_data
      d₩
            dist_entries, DEF_bravo
      d⊌
                                        ;(09)report_buf
      d₩
            dist_entries, DEF_bravo
                                        ;(08)guide_buf
            local_entries, DEF_bravo
      d⊌
                                        ;(07)rock_sup
      ₫₩
            dist_entries, DEF_bravo
                                        ;(06)targ sup
            local_entries, DEF_alpha
      d₩
                                        ;(05)control
      d₩
            dist entries, DEF charlie
                                        ;(04)guidance(1)
            local_entries, DEF_alpha
      d₩
                                        ;(03)guidance(2)
      d₩
            local_entries, DEF_alpha
                                        ;(02)track
            local_entries, DEF_alpha
      d₩
                                        ;(01)update
      dw
            local_entries, DEF_alpha
                                        ;(00)bds
: Task Control Blocks
Main TCB
            semaphore
                       ; Reply Pointer
            d₩
                  2 dup (?); Return Address
                      ; Number of Entries
SEMAPHORE
Targsup_TCB
                ? ; reply
                  2 dup (?); Return Address
                       ; num of distributed entries
            DEF_entry_rec <offset Next_Target_Msg,?,?,?>
   Next_Target_Msg
                  dw 1 ; ! parameter = TARGET_MSG_TYPE
                  dw
                        DEF_OUT ; mode = out
                        802 ; only allow 50 targets for now!
Rocksup_TCB
            SEMAPHORE
            d⊌
                       ; reply
                  2 dup (?); Return Address
                       ; num of distributed entries
```

```
Guidebuf TCB
              SEMAPHORE
                             ; reply
                      2 dup (?); Return Address
                             ; num of distributed entries
                      2
               DEF_entry_rec <offset Put_Guide,?,?,?>
               DEF_entry_rec <offset Get_Guide,?,?,?>
   Put_Guide
                      dw
                                     ; 1 parameter
                              DEF IN ; in mode
                             122 ; # bytes
                      dw
                              1 ; 1 parameter
   Get_Guide
                              DEF_OUT ; out mode
                              122 ; # bytes
Reportbuf_TCB SEMAPHORE
                              ; reply
                      2 dup (?); Return Address
                      2
                              ; num of distributed entries
               DEF_entry_rec <offset Put_Report,?,?,?>
               DEF_entry_rec <offset Get_Report,?,?,?>
    Put_Report
                                     ; 1 parameter
                              DEF_IN ; in mode
                              322
                                     ; # bytes
    Get_Report
                                     ; 1 parameter
                      d₩
                              DEF OUT; out mode
                              322 ; # bytes
Track_TCB
               SEMAPHORE
                            ; reply
                      2 dup (?); Return Address
                              ; num of distributed entries
Control_TCB
               SEMAPHORE
                             ; reply
                      2 dup (?); Return Address
                             ; num of distributed entries
               SEMAPHORE
Guidance1_TCB
               d₩
                              ; reply
                       2 dup (?); Return Address
                              ; num of distributed entries
               DEF_entry_rec <offset History,?,?,?>
               DEF_entry_rec <offset Next_Guidance,?,?,?>
    History
                       d₩
                              1 ; 1 parameter
```

```
DEF IN ; mode is in
                    -1 ; unconstrained
  Next_Guidance
               ď₩
                         ; 1 parameter
                    DEF_OUT; mode is out
               ď₩
                    -1 ; unconstrained
Guidance2_TCB SEMAPHORE
              ?
          dw
                   ; reply
               2 dup (?); Return Address
              2 ; num of distributed entries
          DEF_entry_rec <offset History,?,?,?>
          DEF_entry_rec <offset Next_Guidance,?,?,?>
SAVE_TCB
          SEMAPHORE
          dw ? ; reply
          dw 2 dup (?); Return Address
                   ; num of distributed entries
;······
          SEMAPHORE
DISPLAY_TCB
          dw ? ; reply
          dw 2 dup (?); Return Address
             0 ; num of distributed entries
TRACK_DAT_TCB SEMAPHORE
          dw ? ; reply
          dw 2 dup (?); Return Address
                   ; num of distributed entries
·
UPDATE_TCB
          SEMAPHORE
          dw ? ; reply
          dw 2 dup (?); Return Address
              0 ; num of distributed entries
; These semaphores are used for synchronization of tasks among all
; processors during program startup.
SYNCHRO_SEMAPHORE
              d₩
                     3 dup (?)
CONTINUE_SEMAPHORE
              dw 3 dup (?)
; PROCESSOR TABLE: Given the Mode, the number and pids are provided ;
      fields:
               # of remote CPU's, CPU1-ID, CPU2-ID
proc_table_size equ 6
                        ; bytes per entry
REMOTE_CPU_TABLE label word
```

```
d₩
                      O, DEF_NA,
                                    DEF NA
              dw
                      1, DEF_bravo,
                                    DEF NA
              d₩
                      1, DEF_charlie, DEF NA
              dw
                      2, DEF_bravo,
                                    DEF_charlie
NAME_TABLE
              label
                      word
              offset ALPHA_NAME
       d₩
       dw
              offset BRAVO_NAME
              offset CHARLIE_NAME
       ď₩
ALPHA_NAME
              db
                      'Alpha',0
BRAVO_NAME
              ďb
                      'Bravo',0
CHARLIE_NAME
                      'Charlie',0
              фb
; Watch Dog Timer Data : These Structures determine which processors ;
; to monitor for activity as a function of processor ID and mode.
align
; The following table contains a block for each mode, which contains
  an entry for each processor. Each entry contains a count, followed
  by the PIDs to watch. This table defines which processors communicate
  with each other during the various modes.
Watch_table_entry_size equ
WATCH_TABLE
              label word
:MODE1
                  -- no processors to watch
                O,DEF NA,
                           DEF NA
                                    ; PID 0
                O,DEF_NA,
         d⊌
                           DEF_NA
                                    ; PID 1
                                    ; PID 2
                O,DEF NA,
                           DEF_NA
:MODE2
                1,DEF_bravo,DEF_NA
                                    ; PID 0
         d₩
                1,DEF alpha,DEF NA
                                    ; PID 1
                O,DEF NA, DEF NA
                                    ; PID 2
;MODE3
                1,DEF_charlie, DEF_NA
                                     ; PID 0
         d₩
                O,DEF_NA,
                                     ; PID 1
                              DEF_NA
         dw
                1,DEF_alpha,
                             DEF_NA
                                     ; PID 2
;MODE4
                2,DEF_bravo,
         dw
                             DEF_charlie
                                              ; PID 0
         dw
                1,DEF_alpha,
                              DEF_NA
                                              ; PID 1
         d₩
                1,DEF_alpha,
                             DEF_NA
                                              ; PID 2
WATCH_LIST
              d₩
                                    ; points to list for current config
WATCH_DOG
                     DEF_max_cpus dup (?) ; table of timers
cseg
         ends
```

end

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```
page
                        55,132
                 TITLE Setup - Distributed Ada Network Initialization
; FILE: DA_SETUP.ASM
; Distributed Ada - Setup
 This module initilizes the network to prepare for distributed
; processing.
; Copyright(C) 1989, LabTek Corporation, Woodbridge, CT. USA
.model large
                 public Setup
                 public PID
                                  ; processor ID
                 public NET_TABLE ; addresses indexed 8 per PID
                 include DA_HW.ASM
cseq
                 segment
                           common
                 assume cs:cseg,ds:cseg,es:cseg
                 org 1000H
Setup:
                      dx,cntrl
                                    ; Gate array controller
                      al,eth_enable_reset
                 mov
                      dx,al
                 out
                      al,eth_disable_reset
                 mov
                 out
                      dx,al
                      al,eth_access_prom
                 mov
                      dx,al
                 out
                      cx,6
                 mov
                 mov
                      ax,cs
                                           ; set es:di to receive board
                 MOV
                      di, offset BOARD_ADDRESS : address from prom
                 mov
                 mov
                      dx,prom_address 0
                 cld
GET_ADDRESS:
                 in
                      al,dx
                 stosb
                 inc
                      dx
                 LOOP GET_ADDRESS
                      dx,cntrl
                                          ; select no-sharing adapter,
                 MOV
                      al,eth_recv_select
                                          ; and external transceiver
                 out
                      dx,al
                 mov
                      dx,gacfr
                                           ; 8K of memory mapped space,
                      al,eth_lan_config
                 MOV
                                           ; with interrupts enabled
                 out
                      dx,al
                     dx,dqtr
                                           ; # of bytes to transfer on
                 mov
                 mov al,eth_rem_DMA_burst
                                           ; a remote DMA burst (n/a)
```

```
out dx,al
      dx,idcfr
                            ; interrupt IRQ and DMA
mov
     al,eth_irq_line
                             ; channel selection (DMA n/a)
mov
     dx,al
out
mov
      dx,damsb
                             ; 8k configuration for remote
mov
      al,eth_rem_DMA_config ; DMA. Not used, but minimum
out
      dx,al
                             ; value needed
MOV
      dx,pstr
                             ; start of receive buffer.
      al,eth_recv_buf_start ; Value MUST match that in
MOV
out
                             ; NIC pstart
      dx,pspr
                             ; end of receive buffer.
MOV
      al,eth_recv_buf_end
                             ; Value MUST match that in
MOV
      dx,al
                             ; NIC_pstop
      dx,NIC cr
                             ; stop NIC activity
mov
      al,eth_nic_stop
MOV
      dx,al
out
      dx,NIC dcr
                              ; local DMA transfers as
mov
      al,eth_nic_DMA_config ; 8 byte bursts
mov
out
      dx,al
      dx,NIC rbcr0
mov
                             ; remote DMA setup (remote
mov
      al,eth_remote_DMA_lo
                            ; DMA not used, only local
out
      dx,al
                             ; used)
      dx,NIC_rbcr1
                             ; hi byte of # of bytes to
mov
      al,eth_remote_DMA_hi
                             ; transfer during a remote
out
      dx,al
                             ; DMA operation
      dx,NIC_rcr
                             ; accept only good packets
mov
      al,eth_packet_types
mov
out dx,al
      dx,NIC_tcr
                             ; go into internal loopback
mov
      al,eth_nic_mode
                             ; mode to finish programming
mov
out
      dx,al
                             ; (see anomalies - p. 52)
      dx,NIC_bndy
mov
                             ; overwrite protection rgtr.
      al,eth_bndy_start
                              ; (protects unread packets)
mov
     dx,al
      dx,NIC pstart
                              ; start of receive queue
mov
mov
     al,eth_recv_buf_start
out dx,al
mov dx, NIC_pstop
                              ; end of receive queue
```

```
al, eth_recv_buf_end
                         dx,al
                   out
                                                 ; clear interrupt status
                         dx,NIC_isr
                   mov
                         al,eth_int_status
                         dx,al
                   out
                         dx,NIC_imr
                                                 ; disable interrupts
                   MOV
                         al,eth_ints_disabled
                                                 ; for receive and xmit
                   MOV
                   out
                         dx,al
                         dx,NIC_cr
                                                 ; access page 1 registers
                   mov
                         al,eth_access_page_1
                   MOV
                         dx,al
                   out
                         dx,phys_address_0
                                                  ; let NIC know its address
                   mov
                   mov
                         ds, ax
                   mov
                         si, offset BOARD_ADDRESS ; from the prom
                   mov
                   cld
                    mov
                         cx,6
                                                  ; number of addresses to give
GIVE_ADDRESS:
                    lodsb
                         dx,al
                    out
                    inc
                    loop GIVE_ADDRESS
                                                 ; toad all addresses
                         dx,NIC curr
                                                  ; load current receive pointer
                    MOV
                         al,eth_recv_buf_start ; with pstart
                    mov
                        dx,al
                    out
                    mov
                         dx,NIC_cr
                                                  ; access page 0 registers
                    mov
                         al,eth_access_page_0
                        dx,ai
                    out
                         dx,NIC_cr
                                                  ; start NIC chip
                    mov
                         al,eth_start_nic
                    mov
                    out
                         dx,al
                         dx,NIC tcr
                                                  ; exit internal loopback mode
                    mov
                          al,eth_exit_mode
                    mov
  Note: The RAM initialization is necessary for the multi-packet processing
; done in the receive interrupt routine
                          ax,net_memory_seg
                                                  ; initialize LAN memory to
                    mov
                          es,ax
                                                  ; zeroes
                    MOV
                         cx,net_memory_size/2
                    mov
                                                  ; in words
                    xor
                         di di
                                                  ; start at begin of segment
                    cld
```

```
mov ax,0000
                                                ; initialization value
FILL:
                   stosw
                   loop FILL
; Now check our address against the known Ethernet addresses to determine
; our processor ID
                   mov ax,cs
                                             ; ds already = cs
                   mov es,ax
                   mov bx,0
                                              ; init processor ID
                   mov di,offset NET_TABLE
                   cld
                                              ; search direction = increment
Search:
                   push di
                                              ; save start of current net addr
                   mov cx,3
                                              ; three words per address
                   mov si, offset BOARD_ADDRESS
                   repe cmpsw
                   pop di
                                              ; restore current table index
                   jz Found
                   add di,8
                                              ; go to next index
                   inc bx
                                              ; count processor id
                   cmp bx, NET_COUNT
                                              ; see if all searched
                   jnz Search
                                              ; loop back if more
; If not found, it will return processor id = NET_COUNT
Found:
                   mov [PID],BX
                                             ; record Processor ID
                                              ; done with Setup
                   ret
                   align 2
  VALID PROCESSOR ID's Determined by Ethernet ADDRESS
           0 - ALPHA
           1 - BRAVO
           2 - CHARLIE
                                               ; Processor ID
BOARD_ADDRESS
                   dЬ
                         5 dup (?)
                                               ; holds board address
   PROCESSOR STATION ADDRESS TABLE
NET_COUNT
               equ
                                               ; number of processor on net
NET_TABLE
              label byte
             02H, 60H, 8CH, 47H, 61H, 82H,0,0 ; processor Alpha
                                                                 O EARTH
       db
             02H, 60H, 8CH, 47H, 63H, 55H,0,0 ; processor Bravo 1 VENUS
```

```
db 02H, 60H, 8CH, 48H, 51H, 60H,0,0 ; processor Charlie 2 db 02H, 60H, 8CH, 58H, 35H, 68H,0,0 ; processor Delta 3 db 02H, 60H, 8CH, 02H, 00H, 58H,0,0 ; processor Echo 4 db 02H, 60H, 8CH, 44H, 52H, 09H,0,0 ; processor Foxtrot 5 cseg ends
```

```
55,132
       page
       TITLE Sync - Distributed Ada Network Synchronization
; FILE: DA_SYNC.ASM
  Distributed Ada - Setup
; This procedure varies depending on the processor type (master/slave);
; and the operational phase (Sync Phase vs. normal). During
  SYNC_PHASE, the vendor runtime is not used at all (ie. no tasking)
; and a wait loop is used to detect incoming packets. Since it is
; likely that messages will be lost during SYNC_PHASE, a different
  protocol is used which does not specifically utilize ACK messages.
  Instead, resend and long time-outs are used to synchronize. A
  "Cold_Start" command is used here to definitively restart the system.;
; During normal operation, the master sends a "sync_start" and waits
 for a "sync ready" from each slave. Then it sends a "sync continue";
 to continue with processing (all processors have syncrhonized).
  Copyright(C) 1989, LabTek Corporation, Woodbridge, CT. USA
.model large
       include DA_DEF.ASM
       public Sync
                                   MODULE
       extrn
              MASTER: word
                                     (da)
              SYNC_PHASE:word
       extrn
                                     (da)
       extrn
              NUM_ROCKETS:word
                                     (da)
              NUM_TARGETS:word
       extrn
                                     (da)
              MODE SELECT: word
                                     (da)
       extrn
              Print:near
       extrn
                                     (da)
              Shut down:near
       extrn
                                     (da)
              SYNCHRO_SEMAPHORE:word ;(dtcb)
       extrn
              CONTINUF SEMAPHORE: word ; (dtcb)
       extrn
              RECEIVE_FLAG:word
       extrn
                                    ;(io)
              TX READY:word
                                    ;(io)
       extrn
       extrn
              IO_Deallocate:near
                                    ;(io)
              IO_Xmit:near
       extrn
                                    ;(io)
       extrn
              VRTIF_WAIT: far
                                    ;(vrtif)
              proc_table_size:abs
       extrn
              REMOTE_CPU_TABLE:word
       extrn
              NAME_TABLE: word
       extrn
       segment
                  common
cseg
       assume cs:cseg,ds:cseg,es:cseg
```

```
2100H
Sync:
       push
               ax
       push
       push
               СХ
       push
               si
       push
       push
       push
       push
               [MASTER], OFFFFH
       test
                                      ; are we a master?
       jnz
               Sync10
                                       ; jmp if master
               slave
                                       ; no, go act like a slave
       jmp
Sync10:
       test
               (SYNC_PHASE), OFFFFH
                                      ; are we in sync phase
       jnz
               Master Sync
       jmp
               Master_Normal
MASTER SYNC PHASE:
  synchronize with slave processors and send them configuration
  information.
Master_Sync:
               [MASTER_SYNC_DATA_PIR],cs; first setur Parameter Data Pointer
       mov
               ax, [NUM_ROCKETS]
                                      ; load Configur Lion Record
       mov
               [CONFIG.ROCKETS], ax
       mov
               ex, [NUM_TARGETS]
       mov
               [CONFIG.TARGETS],ax
       mov
               ax, [MODE_SELECT]
       mov
               [CONFIG.SELECT], ax
       mov
       dec
                                      ; adjust mode1 => 0
       mov
                [RETRY_COUNT], DEF_retry_times ; initialize retry counter
               dx,proc_table_size
       mov
                                      ; number of bytes per entry (ax=mode)
       mul
                                       ; compute address
       lea
               si,REMOTE_CPU_TABLE
       add
               si,ax
                                      ; index to proper selected mode
               [REMOTE_INDEX],si
       mov
                                      ; save index into table
       MOV
               cx,[si]
                                      ; fetch number of processors
       mov
               [CPU_COUNT],cx
                                      ; remaining CPUS to process
       add
               si,2
                                      ; skip of number
       mov
               [CPU_PTR],si
                                      ; save pointer to current CPU
       or
               cx,cx
       jnz
               MSP10
                                      ; if there are some remote CPUs
               Sync90
                                      ; if none
       jmp
MSP10:
```

```
lea
                si,crlf
        call
                Print
        jmp
                Shut_down
  Sync occured, print notification and Go on to next processor in list
MSP30:
        mov
                bx, [RECEIVE FLAG]
                                        ; get buffer pointer
                IO_Deallocate
        call
                                         ; return buffer
                si,Success
        lea
        call
                Print
        lea
                si.crlf
        call
                Print
                si, [CPU PTR]
                                        ; get CPU pointer
        mov
                si,2
        add
                [CPU PTR],si
        mov
                                         ; update
        dec
                [CPU_COUNT]
                                         ; count down
        jΖ
                MSP35
                                        ; continue if done with loop
                MSP10
        jmp
                                         ; otherwise loop back
  Now All processors have "checked in". Send them each a "continue"
KSP35:
                si,Sync_Complete
        lea
        call
                Print
                si, [REMOTE_INDEX]
        mov
                                        ; get index into table back
                cx,[si]
                                         ; fetch number of processors
        TOV
                [CPU_COUNT],cx
        MOV
                                        ; remaining CPUS to process
                si,2
        add
                                        ; skip of number
                [CPU_PTR],si
        MOV
                                        ; save as current CPU pointer
MSP40:
                [TX_READY], OFFFFH
        test
                                         ; make sure the transmitter is free
                MSP40
        jle
                                         ; wait if not
        sub
                                         ; skip parameter stuff
        lea
                ax, MASTER_CONTINUE_PROFILE ; profile
                ax
        push
                sp.6
                                         ; skip MY_TID, ENTRY, and TID
        mov
                ax,DEF_sync_continue
                                         ; command
        push
                [si]
        push
                                         ; processor ID of destination
                IO_Xmit
        call
                                         ; send message
                si,[CPU_PTR]
        mov
                                        ; get CPU pointer
                si,2
        add
        mov
                [CPU PTR],si
                                        ; update
        dec
                [CPU_COUNT]
                                        ; count down
                MSP40
        jnz
```

```
; Wait for last transmit complete interrupt
MSP50:
       test
               [TX READY], OFFFFH
                                      ; make sure the transmitter is free
                                      ; wait if not
               MSP50
       jle
       jmp
               Sync90
                                      ; done
; MASTER NORMAL PHASE : runtime synchronization after configuration setup
; just synchronize with slave processors
Master_Normal:
       mov
               ax, [MODE_SELECT]
       dec
                                      ; mode1 => 0
               ax
               dx,proc_table_size
                                      ; number of bytes per entry (ax=mode)
       mov
       mul
               dx
                                      ; compute address
               SI, REMOTE CPU TABLE
        lea
       add
               si,ax
                                      ; index to proper selected mode
                                      ; save index into table
               [REMOTE INDEX], si
       mov
               cx,[si]
                                      ; fetch number of processors
       mov
               [CPU_COUNT],cx
                                      ; remaining CPUS to process
       MOV
        add
               si,2
                                      ; skip of number
               [CPU PTR],si
                                      ; save pointer to current CPU
        mov
               cx,cx
        10
               MNP10
                                      ; if there are some remote CPUs
        inz
               Sync90
                                      ; if none
        jmp
MNP10:
        sub
               sp,6
                                      ; skip parameter stuff
               ax, MASTER_CONTINUE_PROFILE; no parameters
        lea
        push
               aх
                                      ; My TID
        sub
               sp,6
                                      ; skip ENTRY, and TID
               ax,DEF_sync_start
                                      ; command
        mov
        push
               si,[CPU_PTR]
                                      ; get cpu pointer back
        mov
               [si]
                                      ; processor ID of destination
        push
               10 Xmit
        call
                                      ; send message
   Now wait for a reply
        push
               cs
        lea
               ax, SYNCHRO SEMAPHORE
        push
        call
               VRTIF_Wait
                               ; do a wait
  Sync occured, Go on to next processor in list
MNP30:
               si, [CPU_PTR]
                                      ; get CPU pointer
        MOV
```

```
add
               si,2
               [CPU_PTR],si
       mov
                                      ; update
       dec
               [CPU_COUNT]
                                      ; count down
               MNP35
       jΖ
                                      ; continue if done with loop
               MNP10
       jmp
                                      ; otherwise loop back
; Now All processors have "checked in". Send them each a "continue"
MNP35:
               si,[REMOTE_INDEX]
                                      ; get index into table back
       mov
       mov
               cx,[si]
                                      ; fetch number of processors
               [CPU_COUNT],cx
                                      ; remaining CPUS to process
       MOV
       add
               si,2
                                      ; skip of number
               [CPU_PTR],si
                                      ; save as current CPU pointer
       mov
MNP40:
                                      ; skip parameter stuff
       sub
               ax, MASTER_CONTINUE_PROFILE ; profile
       lea
       push
               sp,6
       sub
                                      ; skip MY_TID, ENTRY, and TID
               ax,DEF_sync_continue
       mov
                                      ; command
       push
       push
               [si]
                                      ; processor ID of destination
       call
               IO_Xmit
                                      ; send message
               si,[CPU_PTR]
                                      ; get CPU pointer
       mov
       add
               si,2
               [CPU_PTR],si
       mov
                                      ; update
               [CPU_COUNT]
       dec
                                      ; count down
               MNP40
        jnz
               Sync90
                                      ; DONE
        jmp
Slave:
        test
               [SYNC PHASE], OFFFFH
                                      ; see if initial sync phase
        jnz
               Slave05
        jmp
               Slave_Normal
Slave05:
               si,Slave_sync
        lea
SLAVE SYNC MODE
       Wait for configuration information
Slave10:
               [RECEIVE_FLAG],OFFFFH ; see if incoming data
        test
               Slave10
        įΖ
   We got a message, check the command for "start"
               bx, [RECEIVE_FLAG]
       mov
```

```
[RECEIVE_FLAG],0
                                        ; zero for next time
        MOV
                                        ; fetch buffer ptr
        MOV
                si,[bx]
        стр
                word ptr [si+DEF_pkt_cmd], DEF_cold_start; is this a START
        jΖ
                Slave20
   Must be some other traffic, ignore it!
                IO Deallocate
                                         ; free up buffer
        call
        jmp
                Stave10
    Got a valid Cold Start... respond!
Slave20:
        lea
                si,[si+Def_pkt_data]
                                         ; point to data area of packet buffer
                ax,[si+ROCKETS]
                                         ; unload Configuration Information
        mov
                [NUM_ROCKETS], ax
        mov
                ax, [si+TARGETS]
        Mov
        mov
                [NUM_TARGETS], ax
                ax, [si+SELECT]
        mov
        mov
                [MODE_SELECT],ax
        call
                IO_Deallocate
Slave30:
                [TX_READY], OFFFFH
        test
                                         ; make sure the transmitter is free
        jle
                Slave30
                                         ; wait if not
        sub
                                         ; skip parameter stuff
                sp.6
                ax, SLAVE_READY_PROFILE ; profile
        lea
        push
                aх
                                         ; skip MY TID, ENTRY, and TID
        sub
                sp,6
                ax,DEF_sync_ready
                                         ; command
        MOV
        push
                                         ; PID of master is always zero
        xor
                ax.ax
        push
                IO_Xmit
                                         ; send message
        call
        lea
                si,Success
        call
                Print
    Now wait for Continue
Slave40:
        test
                 [RECEIVE_FLAG], OFFFFH ; see if incoming data
                Slave40
        jΖ
    We got a message, make sure it is continue
ï
        mov
                bx, [RECEIVE_FLAG]
        mov
                 [RECEIVE_FLAG],0
                                         ; clear for next time
        mov
                si,[bx]
                                         ; get buffer pointer
                word ptr [si+DEF_pkt_cmd], DEF_sync_continue; is this a CONTINUE?
        CIND
```

```
jΖ
              Slave50
   Must be some other traffic, deallocate buffer, and check for another cold
   start.
       call
               IO_Deallocate
                                     ; free up buffer
               word ptr [si+DEF_pkt_cmd],DEF_cold_start
       стр
                                                      ; COLD START?
       jnz
               Slave40
                                     ; if not, simply ignore it
               Slave10
       jmp
                                     ; if so, start all over
; Slave synchronization has completed, deallocate buffer and exit
Slave50:
       call
               IO_Deallocate
                                    ; free up buffer
       jmp
               Sync90
SLAVE NORMAL MODE
       Wait for "Start" semaphore, issue "ready" then wait for "continue"
Slave Normal:
       push
                             ; push address of Semaphore
               cs
       lea
               ax, SYNCHRO_SEMAPHORE
       push
       call
               VRTIF_Wait
                            ; do a wait
   issue a "ready" message
                                     ; skip parameter stuff
       sub
               sp,6
               ax, SLAVE_READY_PROFILE ; profile
       lea
       push
                                     ; skip MY_TID, ENTRY, and TID
       sub
               sp,6
               ax,DEF_sync_ready
       MOV
                                     ; command
       push
       хог
               ax,ax
                                     ; PID of master is always zero
       push
       call
               IO_Xmit
                                     ; send message
       push
               cs
               ax, CONTINUE_SEMAPHORE
       lea
                              ; wait for the "go ahead"
       push
       call
               VRTIF_Wait
                              ; all done
Sync90:
               [SYNC_PHASE],0 ; NO longer in Sync Phase!
               es
       pop
               ds
       pop
               di
       pop
       pop
               si
               dx
       pop
```

```
СХ
        pop
                bx
        pop
                ax
        pop
        ret
        align
                                                ; provide 1 param: config record
MASTER_SYNC_PROFILE
                                                ; mode in
                                DEF_IN
                        d₩
                                                ; number of bytes in record
                                6
                        d₩
                                                 ; segment address
MASTER SYNC_DATA_PTR
                        d₩
                                offset CONFIG
                        dw
MASTER CONTINUE_PROFILE dw
                                                 ; no parameters
                                                 ; no parameters
                                 0
SLAVE_READY_PROFILE
CONFIG RECORD
                struc
                                         ; NUM_ROCKETS
                d₩
                         ?
ROCKETS
                                         ; NUM_TARGETS
                         ?
                dw
TARGETS
                                         ; MODE_SELECT
                dw
SELECT
CONFIG RECORD
                ends
                CONFIG_RECORD
CONFIG
                         ?
RETRY_COUNT
CPU_COUNT
                 d۳
                         ?
CPU_PTR
                 ď₩
                         ?
                         ?
REMOTE_INDEX
                 dw
                         13,10,'Trying To Sync With: ',0
                 đb
 Attempt
                         ': Synchronization failed',0
 Failure
                 db
                         ': Synchronization Succeeded',0
                 ďb
 Success
                         13,10,'SYNCHRONIZATION COMPLETED',13,10,0
 Sync_Complete
                 db
                         13,10,0
                 ďb
 crlf
                          ' . ',0
 Period
                 db
                         'Slave Mode, Trying to Synchonize...',0
                 ďb
 Slave_sync
         ends
 cseg
         END
```

```
55,132
                 page
                 TITLE Setup - Distributed Ada Network Initialization
; FILE: DA_SETUP.ASM
; Distributed Ada - Setup
 This module initilizes the network to prepare for distributed
; processing.
 Copyright(C) 1989, Labiek Corporation, Woodbridge, CT. USA
.model large
                 public Setup
                                   ; processor ID
                 public PID
                 public NET_TABLE ; addresses indexed 8 per PID
                 include DA_HW.ASM
cseg
                 segment
                           common
                 assume cs:cseg,ds:cseg,es:cseg
                 org 1000H
Setup:
                 MOV
                       dx,cntrl
                                     ; Gate array controller
                       al,eth_enable_reset
                 mov
                 out
                       dx,al
                       al,eth_disable_reset
                 mov
                 out
                       dx,al
                       al,eth_access_prom
                 mov
                 out
                       dx,al
                       cx,6
                 mov
                 mov
                       ax,cs
                                            ; set es:di to receive board
                       es,ax
                 mov
                       di, offset BOARD_ADDRESS; address from prom
                       dx,prom_address_0
                 mov
                  cld
GET_ADDRESS:
                  in
                       al,dx
                 stosb
                  inc
                  loop GET_ADDRESS
                                            ; select no-sharing adapter,
                       dx,cntrl
                 mov
                       al, eth recv select
                                            ; and external transceiver
                 mov
                  out
                       dx,al
                       dx,gacfr
                                            ; 8K of memory mapped space,
                 mov
                                            ; with interrupts enabled
                       al,eth_lan_config
                 mov
                       dx,al
                  out
                 mov
                       dx,dqtr
                                            ; # of bytes to transfer on
                 mov
                       al,eth_rem_DMA_burst
                                            ; a remote DMA burst (n/a)
```

```
out
     dx,al
      dx, idcfr
                             ; interrupt IRQ and DMA
      al,eth_irq_line
                             ; channel selection (DMA n/a)
MOV
      dx,al
out
      dx,damsb
                              ; 8k configuration for remote
mov
      al,eth_rem_DMA_config ; DMA. Not used, but minimum
mov
out
                             ; value needed
      dx,pstr
                             ; start of receive buffer.
mov
      al,eth_recv_buf_start ; Value MUST match that in
MOV
out
      dx,al
                              ; NIC_pstart
mov
      dx,pspr
                             ; end of receive buffer.
MOV
      al,eth_recv_buf_end
                             ; Value MUST match that in
      dx,al
                             ; NIC_pstop
out
      dx,NIC_cr
mov
                             ; stop NIC activity
mov
      al,eth_nic_stop
out
      dx,al
      dx,NIC_dcr
                              ; local DMA transfers as
mov
      al,eth_nic_DMA_config ; 8 byte bursts
      dx,al
out
      dx,NIC_rbcr0
                             ; remote DMA setup (remote
mov
      al,eth_remote_DMA_lo ; DMA not used, only local
MOV
      dx,al
out
                             ; used)
      dx,NIC_rbcr1
mov
                             ; hi byte of # of bytes to
mov
      al,eth_remote_DMA_hi ; transfer during a remote
                             ; DMA operation
      dx,al
out
      dx,NIC_rcr
mov
                             ; accept only good packets
      al,eth_packet_types
mov
out
      dx,al
MOV
      dx,NIC_tcr
                             ; go into internal loopback
      al,eth_nic_mode
                             ; mode to finish programming
mov
out
      dx,al
                             ; (see anomalies - p. 52)
      dx,NIC bndy
                             ; overwrite protection rgtr.
      al,eth_bndy_start
mov
                             ; (protects unread packets)
      dx,al
out
      dx,NIC_pstart
MOV
                             ; start of receive queue
mov
     al,eth_recv_buf_start
out
     dx,al
mov dx,NIC_pstop
                              ; end of receive queue
```

```
al,eth_recv_buf_end
                   mov
                   out
                         dx,al
                   mov
                         dx,NIC_isr
                                                 ; clear interrupt status
                         al,eth_int_status
                   mov
                   out
                        dx,al
                                                  ; keep interrupts off
                          dx,NIC_imr
                   mov
                          al,eth_ints_disabled
                   mov
                   out
                   mov
                         dx,NIC cr
                                                 ; access page 1 registers
                          al,eth_access_page_1
                    MOV
                         dx,al
                    out
                          dx,phys_address_0
                    mov
                                                   ; let NIC know its address
                    MOV
                          ax,cs
                    mov
                          ds,ax
                          si, offset BOARD_ADDRESS ; from the prom
                    mov
                    cld
                    mov
                          cx,6
                                                   ; number of addresses to give
GIVE ADDRESS:
                    lodsb
                    out
                          dx,al
                    inc
                    loop GIVE_ADDRESS
                                                 ; load all addresses
                    mov
                          dx,NIC curr
                                                 ; load current receive pointer
                    MOV
                          al,eth_recv_buf_start ; with pstart
                    out dx, al
                          dx,NIC_cr
                    mov
                                                  ; access page 0 registers
                    mov
                          al,eth_access page_0
                    out dx,al
                         dx,NIC_cr
                                                  ; start NIC chip
                    mov
                         al,eth_start_nic
                    out dx, al
                          dx,NIC_tcr
                                                  ; exit internal loopback mode
                    mov
                          al,eth_exit_mode
                    MOV
                          dx,al
                    out
                    mov
                          ax,net_memory_seg
                                                  ; initialize LAN memory to
                    mov
                                                  ; zeroes
                          cx,net_memory_size/2
                    mov
                                                 ; in words
                          di,di
                    хог
                                                  ; start at begin of segment
                    cld
                          ax,0000
                    mov
                                                  ; initialization value
FILL:
                    stosw
```

loop FILL

```
; Now check our address against the known Ethernet addresses to determine
; our processor ID
                   mov ax,cs
                                               ; ds already = cs
                   mov es,ax
                   mov bx,0
                                               ; init processor ID
                   mov di, offset NET_TABLE
                   cld
                                               ; search direction = increment
Search:
                                               ; save start of current net addr
                   push di
                   mov cx,3
                                               ; three words per address
                   mov si, offset BOARD_ADDRESS
                   repe cmpsw
                   pop di
                                               ; restore current table index
                   jz Found
                    add di,8
                                               ; go to next index
                                               ; count processor id
                    inc bx
                   cmp bx,NET_COUNT
                                               ; see if all searched
                    jnz Search
                                               ; loop back if more
; If not found, it will return processor id = NET_COUNT
Found:
                    mov [PID],BX
                                               ; record Processor ID
                    ret
                                               ; done with Setup
                    align 2
  VALID PROCESSOR ID's Determined by Ethernet ADDRESS
           0 - ALPHA
           1 - BRAVO
           2 - CHARLIE
PID
                                                ; Processor ID
                                                ; holds board address
BOARD_ADDRESS
                    db
                         6 dup (?)
    PROCESSOR STATION ADDRESS TABLE
NET_COUNT
                                               ; number of processor on net
NET_TABLE
                label byte
              02H, 60H, 8CH, 47H, 63H, 55H,0,0 ; processor Bravo
       db
       db
              02H, 60H, 8CH, 47H, 61H, 82H,0,0 ; processor Alpha
                                                                    0 EARTH
       db
              02H, 60H, 8CH, 48H, 51H, 60H,0,0 ; processor Charlie 2
              02H, 60H, 8CH, 58H, 35H, 68H,0,0 ; processor Delta
       db
              02H, 60H, 8CH, 02H, 00H, 58H,0,0 ; processor Echo
       db
```

db 02H, 60H, 8CH, 44H, 52H, 09H,0,0 ; processor Foxtrot 5

cseg ends

END

```
page
             55.132
      TITLE DA - Distributed Ada Module
; FILE: DA.ASM
; DA - Distributed Ada Module
; Copyright(C) 1989, LabTek Corporation, Woodbridge, CT
; This code is code that would be part of the runtime system, but ;
  must be linked in to replace some part of the regular runtime
; routines. It is Linked to the runtime via (hand) editing.
; Since the compiler does not supply information
  on the parameters in the code (it is implicitly maintained by
  the compiler amoung entry call/accept pairs), tables are placed
; here to provide the information.
; Each packet header is statically formed and placed in this
; module to be reference by the TRANSMIT CONTROL PTR (TCP) used in ;
; the runtime call parameter list. This reduces the overhead
; associated with packetizing the data. These packet headers
  could be generated by the compiler/linker/distributor and
  optimally would be placed in the controller card memory at
  elaboration time so that loading of header data would be
; necessary.
  Ver Date
             Description
  0.1 Nov-88: Initial prototype
  0.2 Dec-89 : Enhanced to support error detection and dynamic
             configuration
······
       include DA_DEF.ASM
       .model large
                                 ; prints out msg, and restarts
       public Shut_down
       public COLD_START
                                  ; NZ if this is cold start
       public MODE SELECT
                                  ; Selected Operating Mode
       public SYNC_PHASE
                                  ; During startup to synchronize CPUs
       public Print
                                  ; for sync printout
       public MASTER
                                  ; for sync
       public NUM TARGETS
                                  ; for sync Config set
       public NUM_ROCKETS
                                  ; for sync Config set
       extrn Initialize:near
                                  ; (rte)
```

; (io)

```
Sync:near
                                         ; synchronize procedure
        extrn
                VRTIF APPLICATION: far
                                        ; (vrtif)
        extrn
                VRT1F_18259:abs
                                        ; (vrtif)
        extrn
                DTCB INIT:near
                                        ; (dtcb)
        extrn
                PID:word
                                        ; processor id (Setup)
        extrn
        segment common
cseg
; BIOS Vectors
                                         40H
int10
                                equ
                                         58H
int16
                                equ
                                         OFDH
initial_imask
                                                 ; mask off all but keyboard
                                equ
                                         3000H
da_base
                                equ
                                                ; segment for da runtime
upper case
                                         ODFH
                                                 ; mask for upper case characters
                                equ
EGA ROM SEGMENT
                                         ОСОООН
                                equ
ROM_PRESENT
                                         0AA55H
                                equ
STACK_SIZE
                                         200
                                                 ; bytes in local stack
                                equ
MAX ROCKETS
                                         20
                                                 ; BDS maximum # rockets
                                equ
MAX TARGETS
                                         50
                                                 : BDS maximum # targets
                                equ
MAX_MODE
                                equ
                                                 ; BDS maximum mode value
ERROR_DELAY
                                         70H
                                                 ; delay roughly 5 seconds
                                equ
FLOPPY_STOP
                                equ
                                         0CH
                                                ; Shuts off motors
FLOPPY_DIGITAL
                                         3F2H
                                                 ; address of digital ctrl reg.
                                equ
        assume cs:cseg,ds:cseg,es:cseg
; The following jump table provides (static) control transfers from the
; Ada application code to the respective support code located here
        align
                8
                                 ; 00
        jmp
                Restart
                                 ; prior to elaboration
                                 ; 08
        align
                8
                Ack Check
                                 ; Check on Acknolwedgment of Messages
        jmp
                                 ; 10
        align
                8
        jmp
                Get Master
                                 ; Returns a boolean if this is the master
        align
        jmp
                Get_Rockets
                                 ; Returns the number of Rockets Configured
                                 ; 20
        align
                Get_Targets
                                  ; returns the number of Targets Configured
        jmp
        align
        jmp
                Get Tasks
                                  ; returns the number of Guide Tasks Configured
        align
                                  ; 30
        test
                word ptr cs:[EGA_PRESENT],OFFFFH
                No_EGA
        jΖ
                dword ptr cs:[BIOS_VIDEO] ; vector to current EGA location
        imo
No_EGA: iret
                                  ; simply skip any EGA activity
```

extrn

Ack_Check:near

```
Restart to initialize the network hardware and configure the system
Restart:
              cs:[AUTO],0
       mov
                                   ; clear auto configure mode
Error_Restart:
       cli
              dx, VRT1F_18259+1
                                   ; address of interrupt mask register
       MOV
              al, initial_imask
       mov
                                   ; initial interrupt mask
              dx,al
       out
                                   ; set mask
  SETUP TEMPORARY STACK
       mov
              ax, seg sseg
              ss,ax
       mov
              ax,STACK_SIZE
       mov
              sp,ax
       call
              clear
                                   ; aa this is fix for compiler bug
  SETUP DATA SEGMENT
              ax,cs
       mov
       ITOV
              ds,ax
  CHECK COLD_START FLAG
;
              [COLD_START], OFFFH
       test
       jΖ
              Warm_start
COLD START ... FIRST RELOCATE TO SEGMENT 3000
              ax,da_base
                            ; first move stack segment
       may
              es,ax
                            ; save for later relocation of code/data
              cx,cs
       mov
       sub
              ax,cx
                            ; compute diff between load and base addr
              cx,ss
                            ; now adjust stack segment
       mov
              ax,cx
       add
              ss,ax
       MOV
              cx,8000H
       MOV
              si,OfffEH
       MOV
              di,OFFFEH
       mov
       std
                            ; auto decrement
              movsw
       rep
       cld
              ax,da_base
       mov
              ds,ax
       push
       lea
              ax, continue
       push
              ax
```

```
; switch to 3000: segment
       retf
Shut off floppy motor
continue:
               al, FLOPPY STOP
       mov
               dx, FLOPPY DIGITAL
       mov
               dx,al
       out
  Get BIOS Vectors
               ax,0
       mov
       mov
               es,ax
                               ; point to zero page
       mov
               ax,es:[int10]
               word ptr [BIOS_VIDEO],ax
               ax,es:[int10+2]
       mov
       mov
               word ptr [BIOS_VIDEO+2],ax
               ax, es: [int16]
       mov
               word ptr [BIOS KB],ax
               ax, es: [int16+2]
       mov
       mov
               word ptr [BIOS_KB+2],ax
               ax,EGA_ROM_SEGMENT
       mov
               es,ax
               word ptr es:[0],ROM_PRESENT
       CIND
       jnz
               warm start
                                      ; if not present leave flag zero
                [EGA_PRESENT],1
                                      ; otherwise set flag
       mov
Warm_Start:
       call
               Initialize
                                      ; Initialize Ethernet Board
                [SYNC_PHASE],1
                                      ; set synchronization phase
       mov
       cmp
                (PID),0
                                      ; see if we are the master
               Master_CPU
       jΖ
                                      ; go on if master
       jmp
               Slave
  if here, this is the master processor with a console
Master_CPU:
                [MASTER],1
                                       ; indicate this is the master
       mov
       test
                [FAILURE], OFFFFH
                                       ; see if display already setup
       jnz
               skip_display
       call
               Set_Display
skip_display:
                [FAILURE],0
       mov
                               ; default is no failure (for next time)
       sti
                               ; enable interrupts now for master mode
                [AUTO], OFFFFH
       test
                                 ; see if in auto reconfiguration mode
        jnz
               Automatic
       call
               Configure
                               ; perform Configuration
        jmp
               TCB_setup
Automatic:
       call
               Auto_Configure
```

```
jmp
               TCB_Setup
                              ; go on and setup tasks
       if slave mode, do not attempt to configure, this is
       done during the SYNC_PHASE sync procedure
Slave:
               [MASTER],0
                              ; indicate THIS is not a master
                              ; enable interrupts for slave mode
       sti
TCB_Setup:
  Determine number of Guidance Tasks
       call
                              ; perform a synchronize
               si,[MODE_SELECT]; get selected mode
       mov
                              ; mode1 => offset 0
       dec
       add
               si,si
                              ; double for word index
               ax,GUIDE_TABLE[si]
       mov
               [NUM_TASKS],ax ; set number of tasks
       mov
               DTCB_Init
                              ; Initialize Task/Processor Directory
       call
   Go execute Application Code
               [COLD_START],0 ; FINISHED WITH INITIALIZATION!
       mov
               VRTIF_APPLICATION
        jmp
CONFIGURE - This routine sets the distributed system configuration
Configure:
               si, offset ANNOUNCE
       call
               Print
       call
               Get_Char
        and
               al,upper_case
       call
               Put Char
                                      ; Echo Response
               al,'Y'
        стр
        jnz
               Configure
rock00:
       mov
               si, offset ROCKET_QUES
               Print
        call
        call
               Get_Num
        OF
               ax,ax
               rock_error
        jle
        cmp
               ax, MAX ROCKETS
               rock20
        jle
rock_error:
               si, offset BAD_ROCKETS
        mov
        call
               Print
               rock00
        jmp
rock20:
```

```
[NUM_ROCKETS],ax
        точ
targ00:
                si, offset TARGET_QUES
        mov
                Print
        call
        call
                Get_Num
        or
                ax,ax
        jle
                targ_error
        стр
                ax, MAX_TARGETS
        jle
                targ20
targ_error:
        mov
                si, offset BAD_TARGETS
        call
                Print
                targ00
        jmp
targ20:
        mov
                 [NUM_TARGETS],ax
mode00:
                si,offset MODE_QUES
        mov
        call
                Print
        call
                Get_Num
                ax,ax
        or
                mode_error
         jle
        стр
                 ax, MAX_MODE
                mode20
        jle
mode_error:
                 si,offset BAD_MODE
        mov
        call
                Print
                 mode00
         jmp
mode20:
                 [MODE_SELECT],ax ; establish mode
        mov
                                          ; see if auto reconfiguration desired
                 si,offset AUTO_QUES
        mov
         call
                 Print
                 Get_Char
         call
         and
                 al,upper_case
         call
                 Put_Char
         cmp
                 al,'Y'
                 auto_no
         jnz
                 [AUTO],1
         mov
                 si, offset DELAY_QUES
                                         ; if auto, check if delay desired
         mov
         call
                 Get_Char
         call
         and
                 al,upper_case
                 Put_Char
         call
                 al,'Y'
         cmp
         jnz
                 delay_no
         mov
                 [DELAY],1
                 config_done
         jmp
auto_no:
```

```
; shut off automatic mode
              [AUTO],0
      mov
delay_no:
              [DELAY],0
config_done:
       ret
AUTO CONFIGURE - This routine sets the distributed system configuration
       using an automatic allocation algorithm.
Auto_Configure:
                                   ; for now, default to uniprocessor
       mov
              [MODE_SELECT],1
       test
              [DELAY],OFFFFH
                                   ; see if we should delay
              auto10
                                   ; if fast reconfigure requested
       jΖ
              si,DELAY_MSG
       lea
       call
              Print
       mov
              ax, ERROR_DELAY
       call
              DELAY_LOOP
auto10:
       ret
Delay_Loop:
              CX,CX
delay_loop10:
       loop
              delay_loop10
       dec
              ax
       jnz
              delay_loop10
       ret
; SHUT_DOWN - Causes a message to be displayed indicating a network
              error, and then jumps to Restart
   This routine is entered only by the ACKCHECK service, therefore all
   interrupts are currently disabled.
Shut_Down:
       cli
              dx, VRT1F_18259+1
                                   ; address of interrupt mask register
              al,initial_imask
                                   ; initial interrupt mask
       mov
              dx,al
                                   ; set mask
       out
              ax,cs
       MOV
              ds, ax
       mov
              [FAILURE],1
                                   ; indicate we have a failure
       mov
       mov
              dx,03CEH
                                   ; aaa straighten out display
              al,5
       MOV
              dx,al
       out
              dx,03CFH
       mov
```

```
al,0
    mov
         dx,al
    out
         Set_Display
                         ; make sure display of all bits
    call
         si,NET_ERROR
     lea
    call
         Print
     jmp
         Error_Restart
; Get_Master
Get_Master:
     push
          ds
     mov
          ax,cs
          ds,ax
     mov
          ax, [MASTER]
     mov
          ds
     рор
     retf
; Get_Rockets
Get_Rockets:
     push
          ds
     mov
          ax,cs
          ds,ax
          ax, [NUM_ROCKETS]
     mov
          ds
     pop
     retf
; Get_Targets
Get_Targets:
     push
          ds
          ax,cs
     mov
     mov
          ds,ax
          ax, [NUM_TARGETS]
     mov
     pop
          ds
     retf
; Get_Tasks
Get_Tasks:
     push
          ds
          ax,cs
     mov
     mov
          ds,ax
          ax, [NUM_TASKS]
     mov
     рор
     retf
; PRINT - print string pointed to by SI until null
```

```
; write color/attribute
BIOS_WRITE
                         09h
                equ
                         0
PAGE_SELECT
                equ
                                         ; background blue, forground red
COLOR
                         1fH
                 equ
                                         ; row=0 col=0
                         0000H
WINDOW_TOP
                 equ
                                         ; row=24 col=79
WINDOW_BOTTOM
                         184fH
                equ
                         0601H
                                         ; scroll up 1 row
SCROLL
                                          ; scroll 0 = clear screen
                         H0090
CLEAR_DISPLAY
                equ
                         0300H
GETCURSOR
                 equ
SETCURSOR
                         0200H
                 equ
                         000dH
                 equ
                         000aH
lf
                 equ
                         0008H
bs
                 equ
                                          ; EGA index control register
                         03сен
index_reg
                 equ
display_select
                            5
                equ
                            0
mode0
                 equ
                                          ; select mask register
mask_select
                            8
                 equ
                                          ; turn all bits on
                         OffH
mask_bits
                 equ
                         03C4H
sequence_reg
                 equ
                         2
map_mask
                 equ
   Set_Display - insures that the display bit mask has all bits turned on.
                  This is only necessary when switching from bit grahics
                  modes where typically only one bit is enabled.
Set_Display:
                 word ptr [EGA_PRESENT], OFFFFH
         test
                 Set_Display10
         jnz
         ret
 Set_Display10:
         mov
                 ax,2
         pushf
                 dword ptr [BIOS_VIDEO]
         call
                  ax, CLEAR_DISPLAY
         mov
                  CX, WINDOW_TOP
         mov
         mov
                  dx, WINDOW_BOTTOM
                  bh, COLOR
         MOV
                                                           (simulate INT 10H)
         pushf
                                          ; push flags
                 dword ptr [BIOS_VIDEO]
         call
         ret
         mov
                  dx,index_reg
 ;
                  al,display_select
         mov
                  dx,al
         out
                                          ; point to data register
         inc
                  dx
                  al, mode0
         mov
         out
                  dx,al
```

```
dx,index_reg
                al,mask_select
        mov
        out
                dx,al
                                        ; point to data register
                dχ
        inc
                al, mask bits
                                        ; set mask
        mov
                dx,al
        out
                dx,sequence_reg
        mov
                al,map_mask
        mov
                dx,al
        out
                al,mask_bits
        mov
        out
                dx,al
        ret
; Print - write text pointed at by SI until null (0) is encountered
Print:
        cld
print10:
        lodsb
                                         ; end of string?
                al,al
        jΖ
                print_end
        call
                Put_Char
                                         ; BIOS call preserves direction flag
                Print10
        jmp
print_end:
        ret
; Put_Char- writes character in AL on screen
Put_Char:
                word ptr [EGA_PRESENT], OFFFFH; see if screen
        test
        jnz
                Put_char10
        ret
Put_Char10:
        push
                                         ; Carriage return?
        cmp
                al,cr
        jΖ
                put_char_cr
                al, lf
                                         ; line feed
                put_char_if
        jΖ
        стр
                al,bs
                                         ; back space
                put_char_bs
        jΖ
                ah, BIOS_WRITE
        mov
                bh, PAGE SELECT
        MOV
                                         ; select page
                bl,COLOR
                                         ; set color
        mov
        mov
                cx,1
                                         ; 1 character
                                                          (simulate INT 10H)
                                         ; push flags
        pushf
```

```
call
                dword ptr [810S_VIDEO]
                ax, GETCURSOR
       mov
                bh, PAGE_SELECT
       mov
       pushf
                dword ptr [BIOS_VIDEO]
       call
                ax, SETCURSOR
        mov
                bh, PAGE_SELECT
        mov
                                         ; move cursor over
        inc
        pushf
                dword ptr [BIOS_VIDEO]
        call
                Put Char_end
        jmp
put_char_cr:
                ax, GETCURSOR
        mov
                bh, PAGE_SELECT
        mov
        pushf
                dword ptr [BIOS_VIDEO]
        call
                 ax, SETCURSOR
        mov
                 bh, PAGE_SELECT
        mov
        mov
                 dl,0
                                         ; reset column
        pushf
                 dword ptr [BIOS_VIDEO]
        call
        jmp
                 Put_char_end
put_char_lf:
                                          ; see if at bottom of screen
                 ax, GETCURSOR
        mov
                 bh, PAGE_SELECT
        mov
        pushf
                 dword ptr [BIOS_VIDEO]
        call
                                          ; at bottom?
         cmp
                 dh,24
                 put_char_lf10
         jΖ
                                          ; if not at bottom of screen, just go
         inc
                 ax, SETCURSOR
                                          ; down 1 more line
         mov
                 bh,PAGE_SELECT
         MOV
         pushf
         call
                 dword ptr [BIOS_VIDEO]
                 Put_char_end
         jmp
 put_char_lf10:
                                          ; if at bottom, then scroll
                 ax, SCROLL
         mov
                 CX,WINDOW_TOP
         mov
                 dx,WINDOW_BOTTOM
         mov
                 bh, COLOR
         mov
                                                           (simulate INT 10H)
                                          ; push flags
         pushf
                 dword ptr [BIOS_VIDEO]
         call
         jmp
                 Put_char_end
 put_char_bs:
```

```
ax, GETCURSOR
       mov
                bh, PAGE_SELECT
       mov
        pushf
                dword ptr [BIOS_VIDEO]
        call
                ax, SETCURSOR
        mov
                bh, PAGE_SELECT
        mov
                                         ; see if already at left margin
                dl,dl
        or
                put_char_bs2
        ìΖ
                dl
                                         ; adjust column
        dec
        pushf
                dword ptr [BIOS_VIDEO]
        call
put_char_bs2:
                Put_char_end
        j..p
put_char_end:
        рор
                ax
        ret
                                         ; read character (synchronous)
GET_KB
                        0
Get_Char:
                 ax,GET_KB
        mov
        pushf
        call
                dword ptr [B10S_KB]
        ret
  Accepts a number from console
        Returns with AX having value (0 if blank line entered)
Get_Num:
        call
                 Get_Line
                 si, offset LINE_BUFF
        mov
                 ax,0
                                ; init value
        mov
                 bx,10
                                 ; decimal numbers
        mov
                 ch,0
                                 ; high byte
        mov
Get_num10:
                 cl,[si]
        mov
                 si
        inc
                                 ; see if end of line
        стр
                 cl,cr
                 Get_num20
         jΖ
        cmp
                 cl,' '
                                 ; also terminate on space
                 Get_num20
        jΖ
                 cl,'0'
        стр
                 get_num_error
         jι
        cmp
                 cl,'9'
                 get_num_error
         jg
        mul
                 cl,OfH
         and
         add
                 ax,cx
```

```
jmp
                Get_num10
Get num error:
                si,offset Input_Error
        mov
        call
                Print
                Get Num
        jmp
Get_num20:
        ret
    Get_Line - fetches line from keyboard until <CR> is entered
       returns with line in LINE_BUFF terminated by <CR>
Get Line:
                si, offset LINE_BUFF
        mov
get_line10:
                si, offset END_OF_LINE
        стр
                get_line_cr
                                         ; force a <CR>
        jΖ
                Get_Char
        call
                al,bs
                                         ; backspace?
        стр
        jz
                get_line_bs
                al,cr
        jΖ
                get_line_cr
        mov
                [si],al
        inc
                si
                Put_Char
        call
                get_line10
        jmp
get_line_bs:
        cmp
                si, offset LINE BUFF
        jΖ
                get_line10
                                         ; do nothing if at begin of line
        call
                Put Char
                al,' '
        moν
        call
                Put Char
                al,bs
        mov
        call
                Put_Char
        dec
                si
                                         ; back up buffer pointer
        jmp
                get_line10
get_line_cr:
                al,cr
        MOV
                 [si],al
        mov
                Put_Char
        call
                al, lf
        mov
                Put_Char
        call
        ret
; CLEAR - routine to zero some of memory to compensate for code
          generator error.
Clear:
```

```
mov
           ax,951bH
           es,ax
     mov
           ax,ax
     xor
           cx.ax
     mov
     mov
           di,ax
     cld
     rep
           stosw
     ret
     align
 BIOS Actual Routine Addresses
BIOS_VIDEO
           dd
                ?
BIOS_KB
           dd
EGA_PRESENT
                            ; default is not present
; CONFIGURATION INFORMATION SUPPLIED TO THE APPLICATION
MASTER
                           ; NZ if this is the master CPU
NUM ROCKETS
           dw
                           ; max number of rockets to launch
NUM_TARGETS
           d₩
                           ; max number of targets to generate
NUM_TASKS
                            ; number of GUIDANCE tasks
  Indexed by mode
GUIDE_TABLE
                            ; mode 1 = 1 guide task
           d₩
                            ; mode 2 = 1 guige task
           d₩
                            ; mode 3 = 1 guide task
                            ; mode 4 = 2 guide tasks
           du
; OPERATION CONTROL VARIABLES
COLD_START
                            ; cold start=1 if first time through
SYNC PHASE
                            ; initial synchronization phase
MODE_SELECT
           du
                            ; selected operating mode
AUTO
                            ; default is not automatic mode
DELAY
           d₩
                 n
                            ; if a delay before restart is desired
FAILURE
           dw
                            ; if a network failure occured
; TEXT INPUT/OUTPUT DEFINITIONS
LINE BUFF
                 256 dup (?)
END_OF_LINE
                 $-1
           equ
           ďb
                 cr,lf,lf
ANNOUNCE
```

```
db
                                          THE
                                                    3 O R D E R
                                                                    DEFENSE'
               db
                       cr,lf,lf
               db
                       ' DISTRIBUTED
                                                     Ada
                                                      M E N U', or, lf, lf, lf
                        'CONFIGURATION
               db
                        'Type ''Y'' to continue: ',0
               db
ROCKET_QUES
               db
                       cr, lf, lf, 'Enter Number Of Rockets=> ',0
TARGET QUES
               db
                       cr, lf, lf, 'Enter Number of Targets=> ',0
MODE_QUES
                       cr, if, if, 'SELECT MODE: ', cr, if
               db
               db
                                     1 = Single
                                                   Processor', cr, lf
               db
                                     2 = Dual (AB) Processor', cr, lf
               db
                                     3 = Dual (AC) Processor', cr, lf
               db
                                     4 = Triple
                                                   Processor', cr, lf
               db
                        'MODE => ',0
AUTO_QUES
               dЬ
                       cr,lf,lf,'Automatic Reconfiguration? (Y)
DELAY_QUES
               db
                       cr,lf,lf,'Delay before Reconfiguration? (Y) : ',0
BAD_ROCKETS
               db
                       cr, if, 'Out Of Range!, Rockets must be between 1 and 20'
               db
                       cr, lf, lf, 'Reenter: ',0
BAD_TARGETS
                db
                       cr, lf, 'Out Of Range!, Targets must be between 1 and 50'
               dЬ
                       cr,lf,lf,'Reenter: ',0
BAD MODE
                db
                       cr, lf, 'Out Of Range!, Mode must be between 1 and 4'
                db
                       cr, lf, lf, 'Reenter: ',0
INPUT ERROR
               db
                       cr, lf, 'Invalid Number'
                db
                       cr, lf, 'Reenter: ',0
NET_ERROR
                db
                        lf, lf,' NETWORK
                                                 TRANSMISSION
                        'ERROR
                                   DETECTED!',cr,lf,lf,0
DELAY_MSG
                       'SYSTEM WILL RESTART IN FIVE SECONDS...', cr, if, if, 0
                db
          ends
cseq
        segment STACK
sseg
       db
                STACK_SIZE dup (0)
sseg
        ends
        endi
```